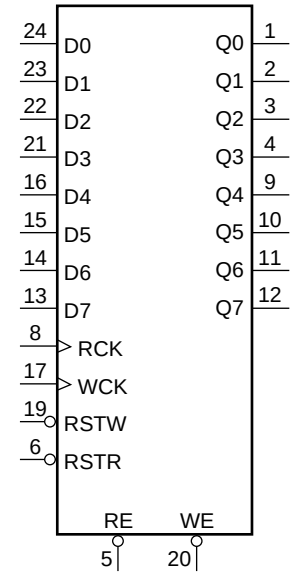
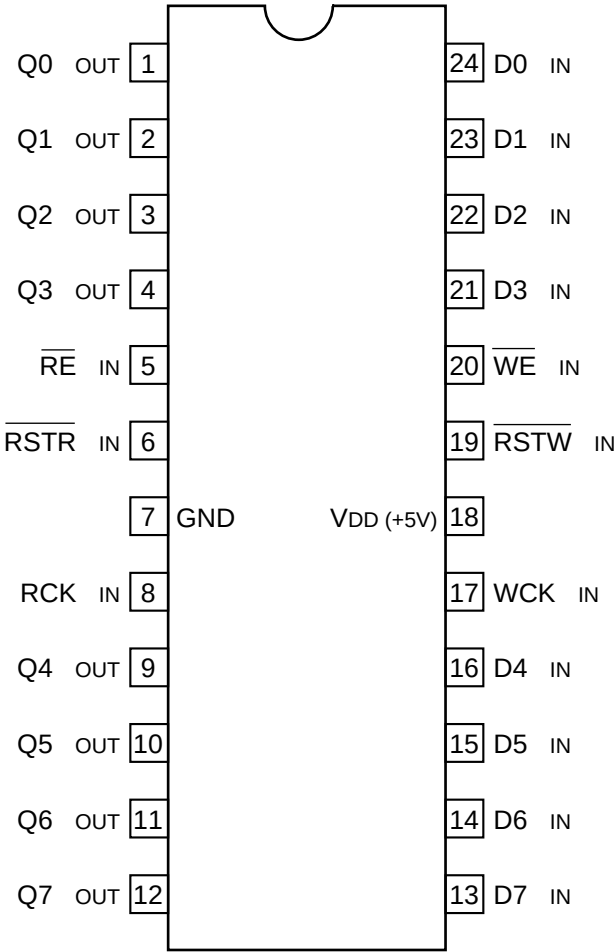

C-MOS 9K (1135 x 8)-BIT FIFO MEMORY
—TOP VIEW—



D 0 – D 7 ; DATA INPUTS
Q 0 – Q 7 ; DATA OUTPUTS
WCK ; WRITE CLOCK INPUT
RCK ; READ CLOCK INPUT
WE ; WRITE ENABLE INPUT
RE ; READ ENABLE INPUT
RSTW ; RESET WRITE INPUT
RSTR ; RESET READ INPUT

