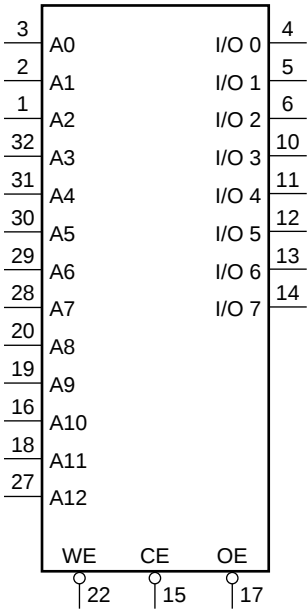
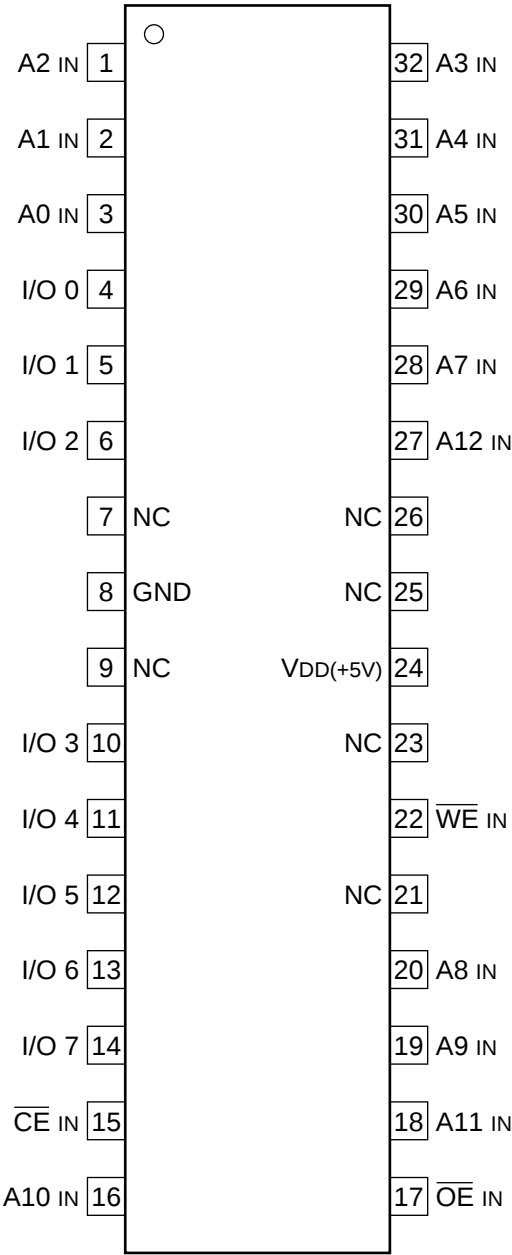
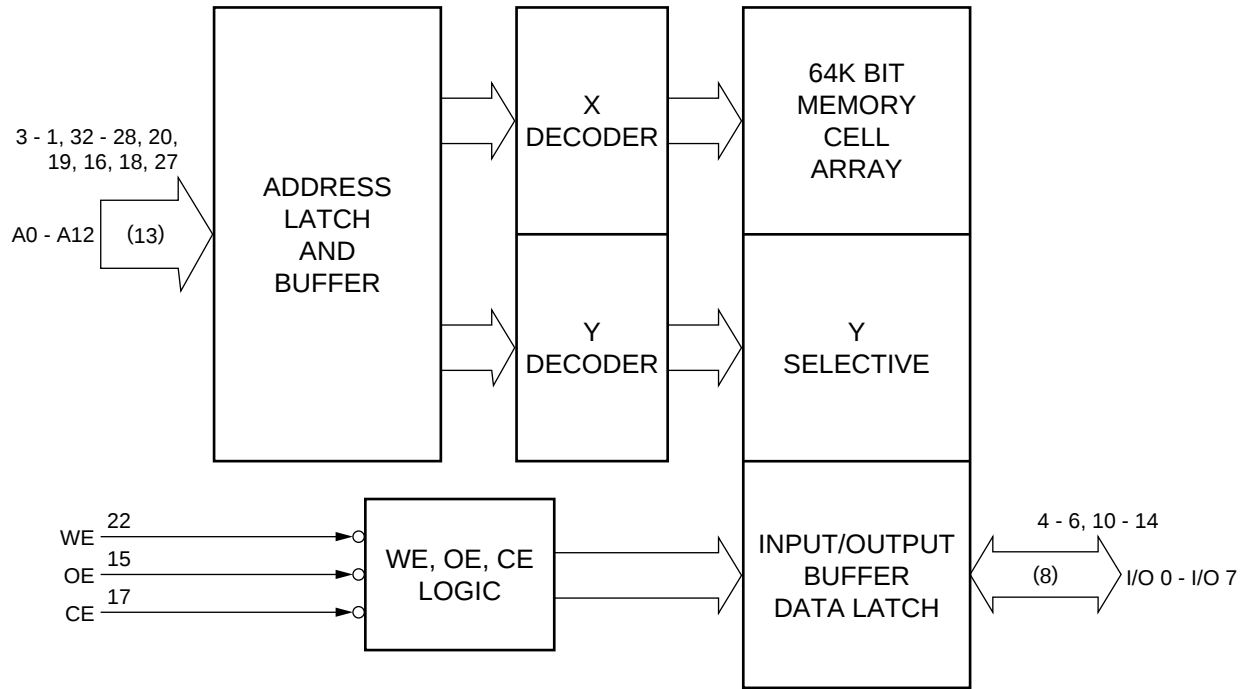

C-MOS 64K(8K 8)-BIT EEPROM
-TOP VIEW-



A0 - A12 ; ADDRESS INPUTS
I/O 0 - I/O 7 ; DATA INPUTS/OUTPUTS
WE ; WRITE ENABLE INPUT
CE ; CHIP ENABLE INPUT
OE ; OUTPUT ENABLE INPUT



*ABOVE DIAGRAM SHOWS CONDITIONS BEFORE PROGRAMMING.