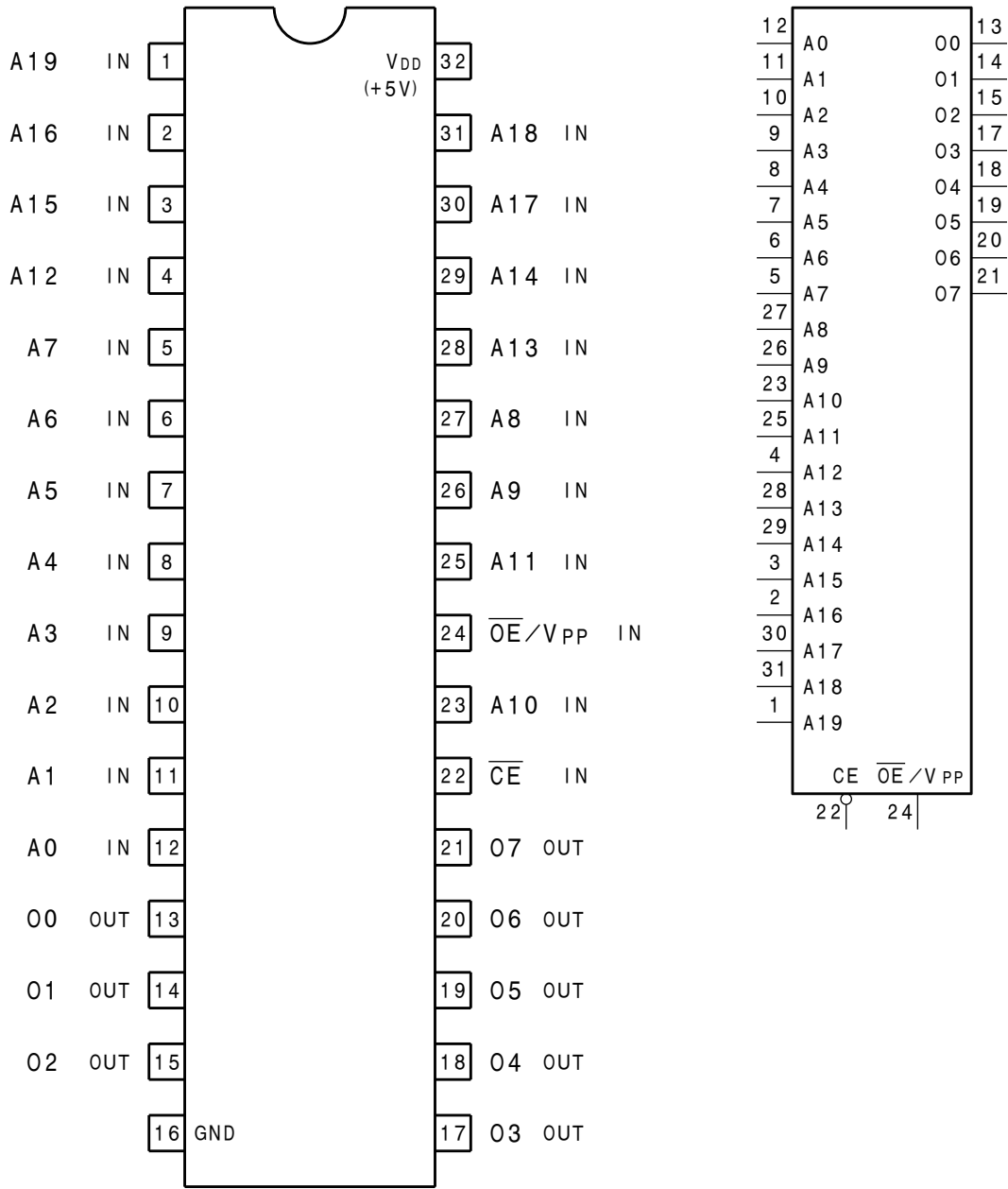
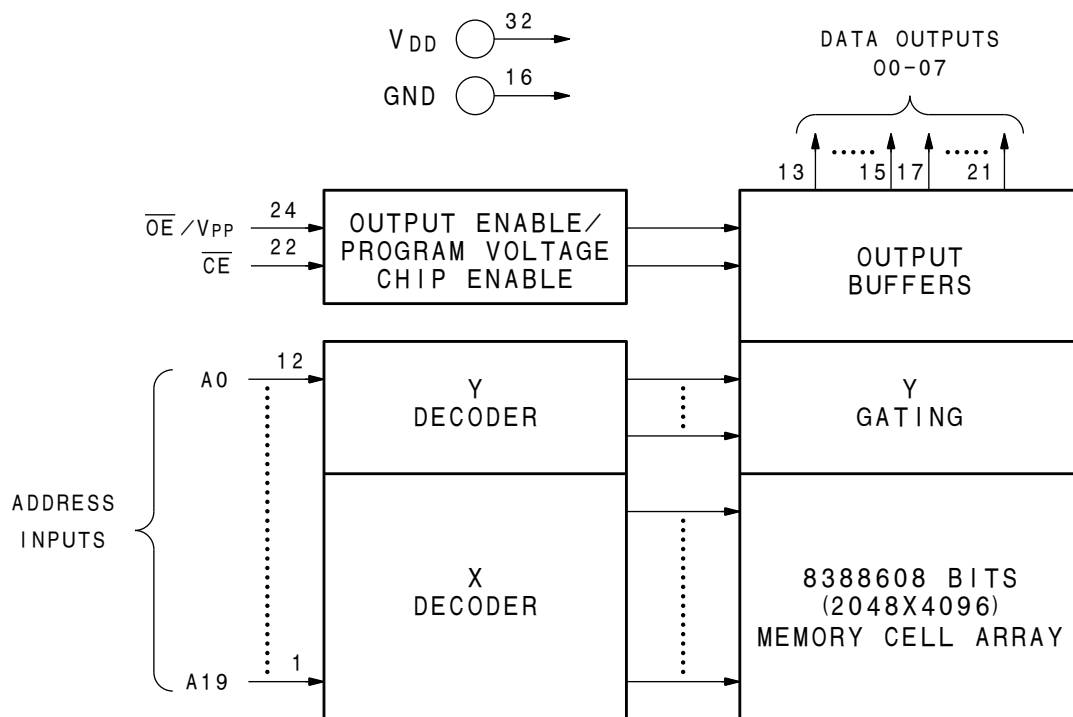

C-MOS 8M(1048,576x8)-BIT ERASABLE PROM
-TOP VIEW-



A0-A19 ; ADDRESS INPUTS
O0-O7 ; DATA OUTPUTS
CE ; CHIP ENABLE
OE/VPP ; OUTPUT ENABLE/PROGRAM VOLTAGE



ABOVE DIAGRAM SHOWS CONDITIONS BEFORE PROGRAMMING.

SYMBOL MODE	$\overline{\text{CE}}$	$\overline{\text{OE}}/\text{V}_{\text{PP}}$	V_{DD}	00-07
READ	0	0	+5V	DOUT
OUTPUT DISABLE	0	1		HI-Z
STANDBY	1	X		HI-Z
PROGRAM	0	+12.5	+6.5V	DIN
PROGRAM VERIFY	0	0		DOUT
PROGRAM INHIBIT	1	+12.5		HI-Z

1 ; HIGH LEVEL
0 ; LOW LEVEL
X ; DON'T CARE
HI-Z ; HIGH IMPEDANCE