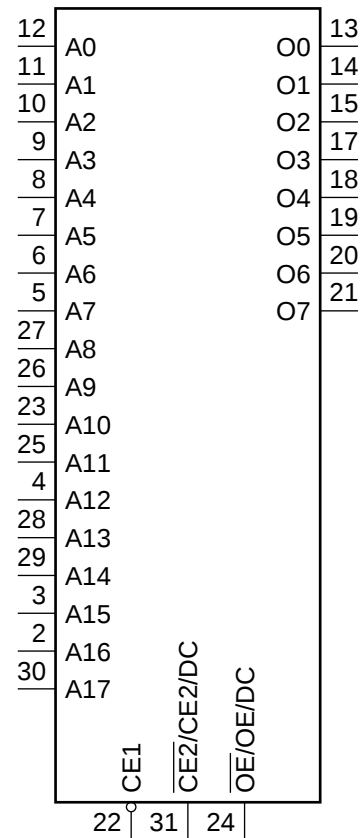
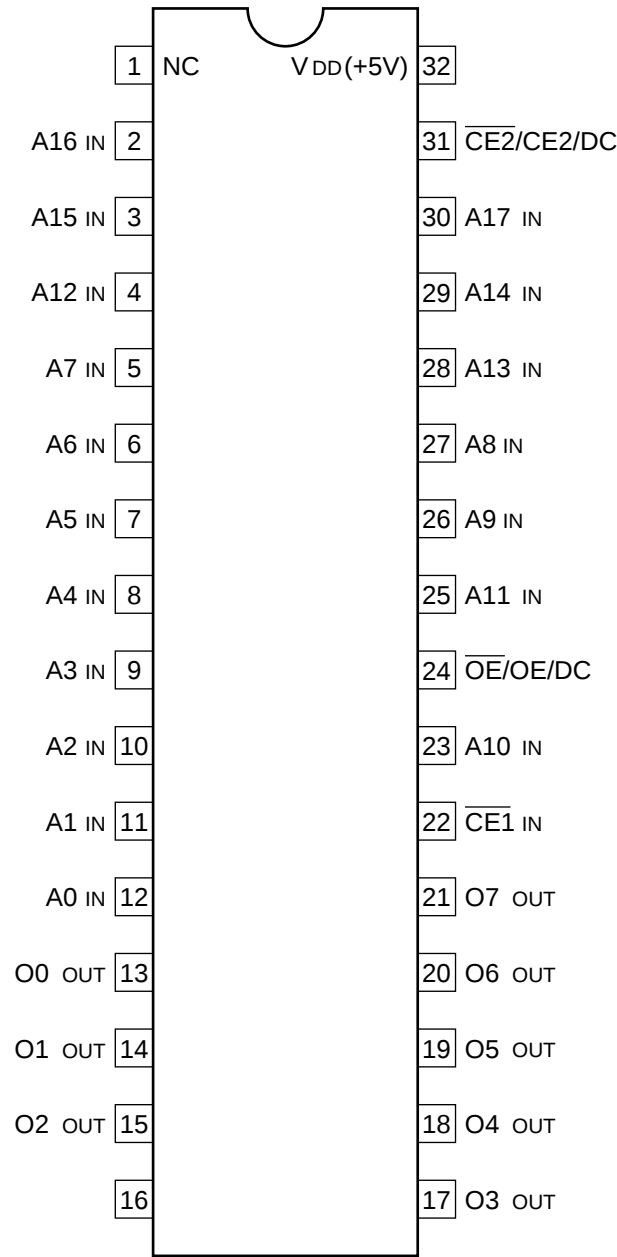
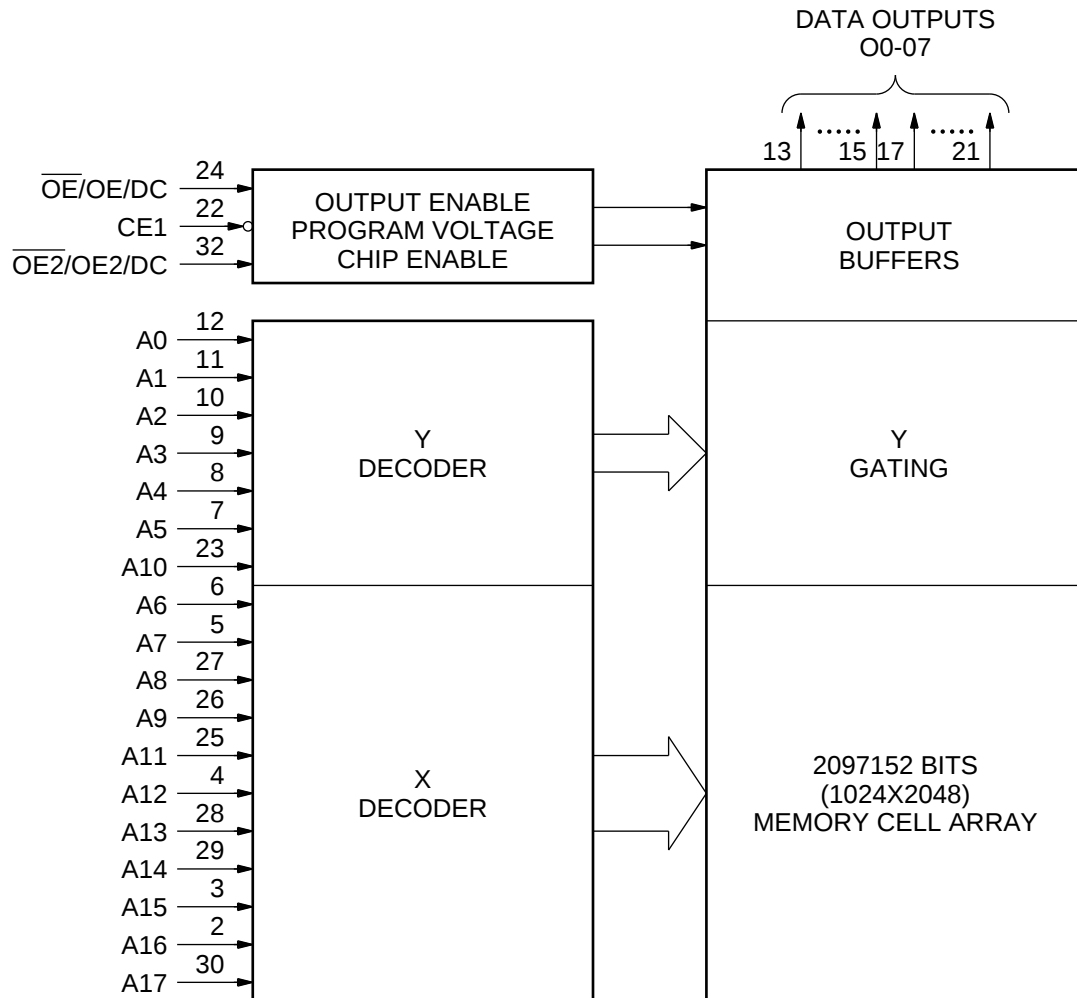


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C-MOS 2M (256k WX8)-BIT PROM  
- TOP VIEW -



A0-A17 ; ADDRESS INPUT  
O0-O7 ; DATA OUTPUT  
CE1, 2 ; CHIP ENABLE  
OE ; OUTPUT ENABLE



| SYMBOL<br>MODE  | CE | OE | PGM | V <sub>PP</sub> | V <sub>DD</sub> | O0-O7            |
|-----------------|----|----|-----|-----------------|-----------------|------------------|
| READ            | 0  | 0  | 1   | 5V              | 5V              | D <sub>OUT</sub> |
| OUTPUT DISABLE  | 0  | 1  | X   |                 |                 | Hi-Z             |
| STANDBY         | 1  | X  | X   |                 |                 | Hi-Z             |
| PAGE DATA LATCH | 1  | 0  | 1   | 12.5V           | 6.5V            | D <sub>IN</sub>  |
| PAGE PROGRAM    | 1  | 1  | 0   |                 |                 | Hi-Z             |
| BYTE PROGRAM    | 0  | 1  | 1   |                 |                 | D <sub>IN</sub>  |
| PROGRAM VERIFY  | 0  | 0  | 1   |                 |                 | D <sub>OUT</sub> |
| PROGRAM INHIBIT | X  | 0  | 0   |                 |                 | Hi-Z             |
|                 | X  | 1  | 1   |                 |                 |                  |

1 ; HIGH LEVEL

0 ; LOW LEVEL

X ; 1 (TTL LEVEL HIGH LEVEL) or 0 (TTL LEVEL LOW LEVEL)

Hi-Z ; HIGH IMPEDANCE