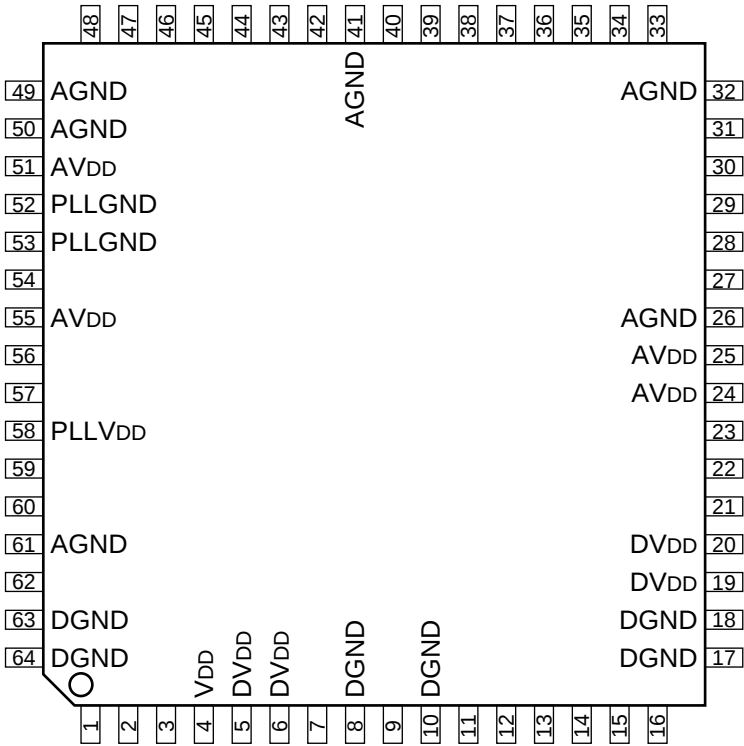


C-MOS TRIPLE CABLE TRANSCEIVER/ARBITER

—TOP VIEW—



PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	/RESET	17	—	DGND	33	O	TPB3 [−]	49	—	AGND
2	I	LPS	18	—	DGND	34	O	TPB3 ⁺	50	—	AGND
3	I	LREQ	19	—	DVDD	35	O	TPA3 [−]	51	—	AVDD
4	—	VDD	20	—	DVDD	36	O	TPA3 ⁺	52	—	PLLGND
5	—	DVDD	21	I	TESTM2	37	O	TPB2 [−]	53	—	PLLGND
6	—	DVDD	22	I	TESTM1	38	O	TPB2 ⁺	54	I/O	FILTER
7	I	PD	23	I	CPS	39	O	TPA2 [−]	55	—	AVDD
8	—	DGND	24	—	AVDD	40	O	TPA2 ⁺	56	I	XI
9	O	SYSCLK	25	—	AVDD	41	—	AGND	57	O	XO
10	—	DGND	26	—	AGND	42	O	TPB1 [−]	58	—	PLLVDD
11	I/O	CTL0	27	I/O	C/LKON	43	O	TPB1 ⁺	59	I	R0
12	I/O	CTL1	28	I	PC0	44	O	TPA1 [−]	60	I	R1
13	I/O	D0	29	I	PC1	45	O	TPA1 ⁺	61	—	AGND
14	I/O	D1	30	I	PC2	46	O	TPBIAS1	62	I	/ISO
15	I/O	D2	31	O	CNA	47	O	TPBIAS2	63	—	DGND
16	I/O	D3	32	—	AGND	48	O	TPBIAS3	64	—	DGND

INPUT

/ISO	: LINK INTERFACE ISOLATION DISABLE
/RESET	: LOGIC RESET
CPS	: CABLE POWER STATUS
LPS	: LINK POWER STATUS
LREQ	: LINK REQUEST
PC2 - PC0	: POWER CLASS
PD	: POWER DOWN
P0, R1	: CURRENT SETTING RESISTOR
TESTM2, TESTM1	: TEST MODE CONTROL
XI	: CRYSTAL OSCILLATOR

OUTPUT

CNA	: CABLE NOT ACTIVE
SYSCLK	: SYSTEM CLOCK
TPA3 ⁺ - TPA1 ⁺	: PORT N, TWISTED PAIR CABLE A POSITIVE SIGNAL
TPA3 ⁻ - TPA1 ⁻	: PORT N, TWISTED PAIR CABLE A NAGATIVE SIGNAL
TPB3 ⁺ - TPB1 ⁺	: PORT N, TWISTED PAIR CABLE B POSITIVE SIGNAL
TPB3 ⁻ - TPB1 ⁻	: PORT N, TWISTED PAIR CABLE B NAGATIVE SIGNAL
TPBIAS3 - TPBIAS1	: PORT N, TWISTED PAIR BIAS
XO	: CRYSTAL OSCILLATOR

INPUT/OUTPUT

C/LKON	: BUS MANAGER CAPABLE/LINK ON
CTL1, CTL0	: CONTROL
FILTER	: PLL FILTER
D3 - D0	: DATA

