

# DATA SHEET

## **TDA9804**

VIF-PLL demodulator with internal  
or external AGC and FM-PLL  
detector

Preliminary specification  
File under Integrated Circuits, IC02

November 1992

# VIF-PLL demodulator with internal or external AGC and FM-PLL detector

## TDA9804

### FEATURES

- Suitable for negative vision modulation controlled by internal or external AGC
- Gain controlled 3-stage IF amplifier; suitable for VIF frequencies up to 60 MHz
- True synchronous demodulation with active carrier regeneration (ultra-linear demodulation, good intermodulation figures, reduced harmonics and excellent pulse response)
- Peak sync AGC for negative modulation, e.g. B/G standard
- Voltage controlled external AGC enabled by switching pin
- Video amplifier to match sound trap and sound filter

- AGC output voltage for tuner; adjustable take-over point (TOP)
- AFC detector without extra reference circuit
- Alignment-free FM-PLL detector with high linearity
- Stabilizer circuit for ripple rejection and to achieve constant output signals
- 5 to 8 V positive supply voltage range, low power consumption (300 mW at +5 V supply voltage)

### GENERAL DESCRIPTION

The TDA9804 is a monolithic integrated circuit for vision and sound IF signal processing in TV and VTR sets.

### QUICK REFERENCE DATA

SYMBOL	PARAMETER	MIN.	TYP.	MAX.	UNIT
$V_P$	positive supply voltage (pin 20)	4.5	5	8.8	V
$I_P$	supply current	51	60	69	mA
$V_{i\text{ IF}}$	vision IF input signal sensitivity (RMS value, pins 1 and 2)	–	50	90	$\mu\text{V}$
	maximum vision IF input signal (RMS value, pins 1 and 2)	70	150	–	mV
$G_V$	IF gain control range	64	70	73	dB
$V_{o\text{ CVBS}}$	CVBS output signal on pin 7 (peak-to-peak value)	1.7	2.0	2.3	V
B	–3 dB video bandwidth on pin 7	6	8	–	MHz
S/N (W)	signal-to-noise ratio weighted; for video	56	59	–	dB
$\alpha_{1.1}$	intermodulation attenuation	56	62	–	dB
$\alpha_{3.3}$		56	62	–	dB
$\alpha_H$	suppression of harmonics in video signal	35	40	–	dB
$V_{o\text{ AF}}$	maximum AF output signal for THD < 1.5% (RMS value, pin 9)	0.8	–	–	V
$T_{\text{amb}}$	operating ambient temperature range	0	–	+70	$^{\circ}\text{C}$

### ORDERING INFORMATION

EXTENDED TYPE NUMBER	PACKAGE			
	PINS	PIN POSITION	MATERIAL	CODE
TDA9804	20	DIL	plastic	SOT146 <sup>(1)</sup>

### Note

1. SOT146-1; 1996 November 27.

VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804

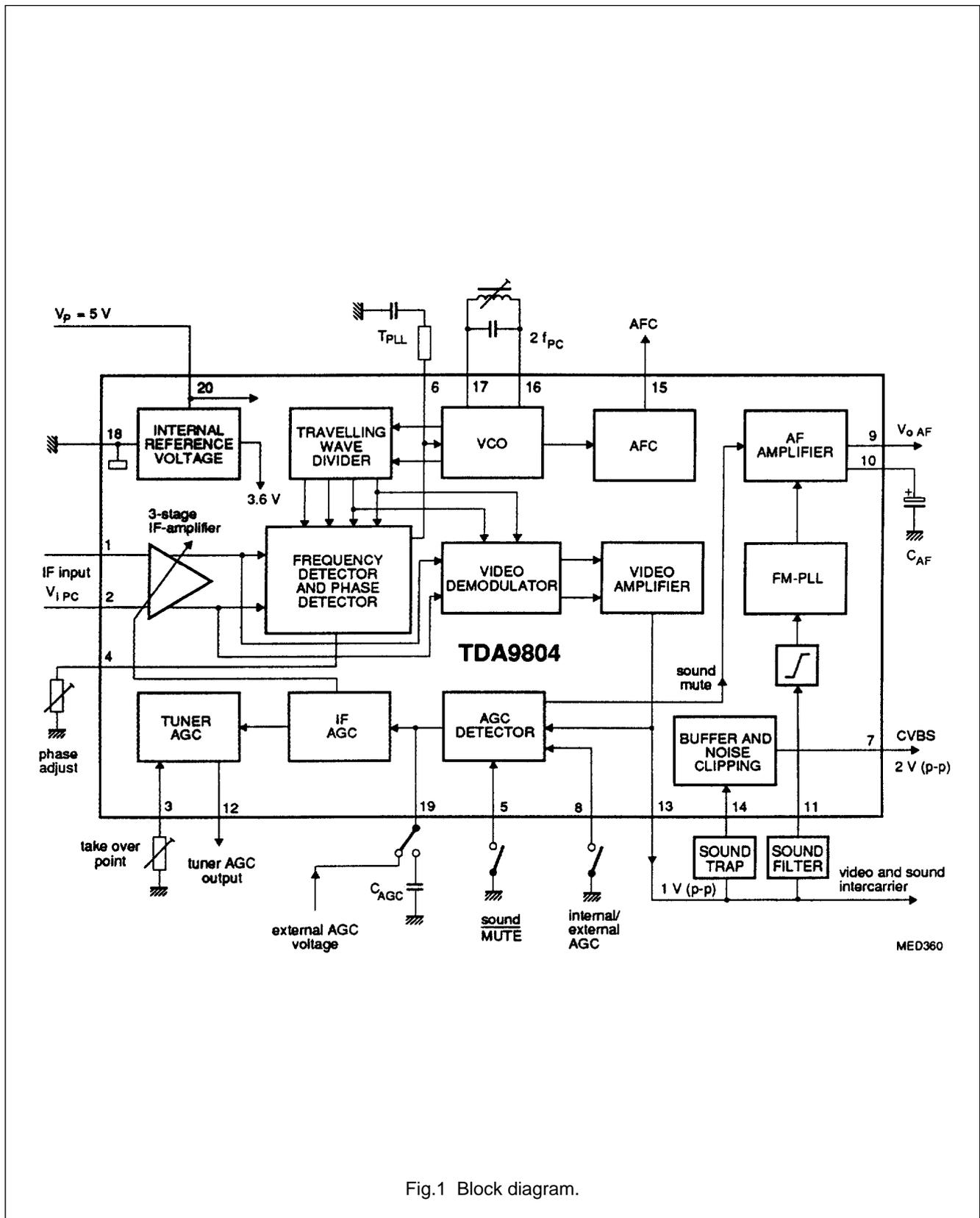


Fig.1 Block diagram.

VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804

PINNING

SYMBOL	PIN	DESCRIPTION
V <sub>i</sub> IF	1	vision IF differential input signal
	2	
TADJ	3	tuner AGC take-over adjust (TOP)
φADJ	4	phase detector adjust
Mute <sub>SWI</sub>	5	mute switch input
T <sub>PLL</sub>	6	PLL time constant of phase detector
V <sub>o</sub> CVBS	7	CVBS (positive) output signal
AGC <sub>SWI</sub>	8	internal/external AGC switch
V <sub>o</sub> AF	9	audio frequency output signal
C <sub>AF</sub>	10	decoupling capacitor of audio frequency amplifier
V <sub>i</sub> IC	11	sound intercarrier input signal
TAGC	12	tuner AGC output
V <sub>o</sub> VID	13	video and sound intercarrier output signal
V <sub>i</sub> VID	14	video input signal to buffer amplifier
AFC	15	automatic frequency control output
VCO1	16	VCO reference circuit for 2 fpc
VCO2	17	
GND	18	ground (0 V)
C <sub>AGC</sub>	19	AGC capacitor and external AGC voltage
V <sub>P</sub>	20	positive supply voltage

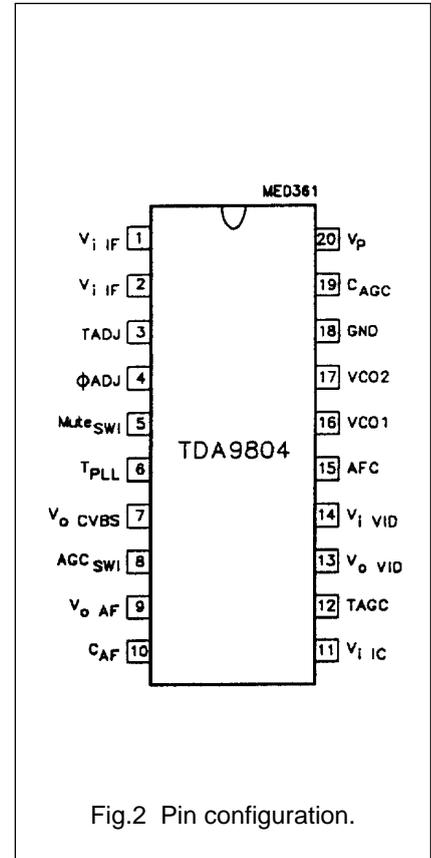


Fig.2 Pin configuration.

## VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804

### FUNCTIONAL DESCRIPTION

#### Vision IF input

The vision IF amplifier consists of three AC-coupled differential amplifier stages; each stage comprises a controlled feedback network by means of emitter degeneration.

#### IF and tuner AGC

The automatic control voltage to maintain the video output signal at a constant level is generated according to the transmission standard. Since the TDA9804 is suitable for negative modulation only the peak-sync level is detected. The AGC detector charges and discharges the capacitor on pin 19 to set the IF gain and the tuner gain. In case of an external AGC (depending on the input voltage at pin 8) the internal charging and discharging currents are switched off and an external AGC voltage has to be applied to pin 19. The AGC capacitor voltage is transferred to an internal IF control signal, and is fed to the tuner AGC to generate the tuner AGC output current on pin 12 (open-collector output). The tuner AGC voltage take over point is adjusted on pin 3. This allows the tuner and the IF SAW filter to be matched to achieve the optimum IF input level.

#### Frequency detector, phase detector and video demodulator

The IF amplifier output signal is fed to a frequency detector and to a phase detector. The frequency detector is operational before lock-in. A DC current is generated which is proportional to the frequency difference between the input frequency and the VCO frequency. After lock-in, the frequency detector and the phase detector generate a DC current proportional to the phase difference between VCO and input signals. The control signal for the VCO is provided by the phase detector. The video demodulator is a linear multiplier, designed for low distortion and wide bandwidth.

The vision IF input signal is multiplied by the in-phase component of the VCO output. The demodulated output signal is fed via an integrated low-pass filter ( $f_g = 12$  MHz) to the video amplifier for suppression of the carrier harmonics.

#### VCO and travelling wave divider

The VCO operates with a symmetrically-connected reference LC-circuit, operating at double vision carrier frequency. Frequency control is performed by an internal varicap diode. The voltage to set the VCO frequency to the actual frequency of double vision carrier frequency, is also amplified and converted for the AFC output current. The VCO signal is divided-by-two in a travelling wave divider, which generates two differential output signals with 90 degree phase difference independent of frequency.

#### Video amplifier, buffer and noise clipping

The video amplifier is a wide bandwidth operational amplifier with internal feedback. A nominal positive modulated video signal of 1 V (p-p) is present on the composite video output (pin 13). The input impedance of the 7 dB wideband buffer amplifier (with internal feedback) is suitable for ceramic sound trap filters.

The CVBS output (pin 7) provides a positive video signal of 2 V (p-p). Noise clipping is provided internally.

#### Sound demodulation

The FM sound intercarrier signal is fed to pin 11 and through a limiter amplifier before it is demodulated. This achieves high sensitivity and high AM suppression. The limiter amplifier consists of seven internal AC-coupled stages, minimizing the DC offset.

The FM-PLL demodulator consists of an RC-oscillator, loop filter and phase detector. The oscillator frequency is locked on the FM intercarrier signal from the limiter amplifier. As a result of this locking, the RC-oscillator is frequency-modulated.

The modulating signal voltage (AF signal) is used to control the oscillator frequency. By this, the FM-PLL works as an FM demodulator.

The audio frequency amplifier with internal feedback is designed for high gain and high common mode rejection. The low-level AF signal coming out of the FM-PLL demodulator is amplified and buffered in a low-ohmic audio signal output stage (pin 9). An external decoupling capacitor on pin 10 removes the DC voltage from the audio amplifier input.

# VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804

## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC134)

SYMBOL	PARAMETER	MIN.	MAX.	UNIT
$V_p$	supply voltage (pin 20) for a maximum chip temperature (note 1) SOT146 at +120 °C	0	8.8	V
$V_I$	voltage on pins 1, 2, 7, 11, 13, 14, 15 and 19	0	$V_p$	V
$t_{s\ max}$	short-circuit time	–	10	s
$V_{12}$	tuner AGC output voltage	–	13.2	V
$T_{stg}$	storage temperature range	–25	+150	°C
$V_{ESD}$	electrostatic handling for all pins (note 2)	–	±300	V

### Notes to the Limiting Values

- Supply current  $I_p = 69\text{ mA}$  at  $T_{amb} = +70\text{ °C}$ .
- Equivalent to discharging a 200 pF capacitor through a 0  $\Omega$  series resistor (negative and positive voltage).

## THERMAL RESISTANCE

SYMBOL	PARAMETER	THERMAL RESISTANCE
$R_{th\ j-a}$	from junction to ambient in free air SOT146	73 K/W

# VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804

## CHARACTERISTICS

$V_P = 5\text{ V}$ ;  $T_{\text{amb}} = +25\text{ °C}$ ;  $f_{\text{PC}} = 38.9\text{ MHz}$ ;  $f_{\text{SC}} = 33.4\text{ MHz}$  with  $V_{\text{PC}}/V_{\text{SC}} = 13\text{ dB (B/G)}$ ;  $V_{\text{IF}} = 10\text{ mV RMS value (sync level at B/G)}$ ; video modulation DSB; residual carrier: B/G = 10%; video signal in accordance with CCIR line 17; measurements taken in Fig.3 unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_P$	supply voltage range (pin 20)	see note 1	4.5	5	8.8	V
$I_P$	supply current		51	60	69	mA
<b>AGC switch input (pin 8)</b>						
$V_{\text{IH}}$	input voltage for internal AGC	see note 2	1.5	–	$V_P$	V
$V_{\text{IL}}$	input voltage for external AGC		0	–	0.8	V
$I_{\text{IL}}$	LOW level input current	$V_8 = 0\text{ V}$	–	–300	–360	$\mu\text{A}$
<b>Vision IF input (pins 1 and 2)</b>			B/G standard			
$V_i$	input signal sensitivity (RMS value)	–1 dB video at output	–	50	90	$\mu\text{V}$
	maximum input signal (RMS value)	+1 dB video at output	70	150	–	mV
$\Delta V_i$	IF amplitude difference between picture and sound carrier	within AGC range	–	0.7	1	dB
$G_{\text{IF}}$	IF gain control range	see Fig.4	64	70	73	dB
$B$	–3 dB IF bandwidth	upper cut-off frequency	70	100	–	MHz
$R_i$	input resistance		1.7	2.2	2.7	$\text{k}\Omega$
$C_i$	input capacitance		1.2	1.7	2.5	pF
$V_{1,2}$	DC input voltage		3.0	3.4	3.8	V
<b>True synchronous video demodulator</b>		see note 3				
$f_{\text{VCO}}$	maximum oscillator frequency for carrier regeneration	$f = 2f_{\text{PC}}$	125	130	–	MHz
$\Delta f_{\text{VCO}}$	oscillator drift (free running) as a function of temperature	see note 4; $\Delta T = 0\text{ to }+70\text{ °C}$	–	–	$\pm 1300$	$10^{-6}$
$V_{\text{o ref}}$	oscillator swing at pins 16 and 17 (RMS value)		tbn	120	tbn	mV
$\Delta f_{\text{PC}}$	vision carrier capture range (negative)		1.5	2	–	MHz
	vision carrier capture range (positive)		1.5	2	–	MHz
$t_{\text{acq}}$	acquisition time	see note 5; BL = 60 kHz	–	–	30	ms
$V_{\text{i IF}}$	IF input signal sensitivity (RMS value, pins 1 and 2)					
	for PLL still locked	see note 6; maximum IF gain	–	70	100	$\mu\text{V}$
	for C/N = 10 dB	see note 7	–	100	140	$\mu\text{V}$
$I_{\text{loop}}$	FPLL loop offset current at pin 6	see note 8	–	–	$\pm 4.5$	$\mu\text{A}$

# VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
<b>Composite video amplifier (pin 13)</b>		sound carrier off				
$V_{o\ vid}$	output signal (peak-to-peak value)	see Fig.7	0.9	1.0	1.1	V
$V_{13}$	sync level		1.4	1.5	1.6	V
	zero carrier level		2.5	2.6	2.7	V
	upper video clipping level		$V_P - 1.1$	$V_P - 1.0$	–	V
	lower video clipping level		–	0.3	0.4	V
$V_{0\ FM}$	IF intercarrier level (RMS value)	sound carrier on; see note 9	tbn	140	tbn	mV
$R_{13}$	output resistance		–	–	10	$\Omega$
$I_{int13}$	internal bias current for emitter follower	DC	1.8	2.5	–	mA
$I_{13}$	maximum output sink current	DC and AC	1.4	tbn	–	mA
	maximum output source current		2.0	tbn	–	mA
$B$	–3 dB video bandwidth	$C_{13} < 50\ \text{pF};$ $R_L > 1\ \text{k}\Omega$	7	10	–	MHz
$\alpha_H$	suppression of video signal harmonics	see note 10; $C_{13} < 50\ \text{pF};$ $R_L > 1\ \text{k}\Omega$	35	40	–	dB
RR	ripple rejection on pin 13	see Fig.9	32	35	–	dB
<b>CVBS buffer amplifier and noise clipper (pins 7 and 14)</b>						
$R_{14}$	input resistance		2.6	3.3	4.0	$\text{k}\Omega$
$C_{14}$	input capacitance		1.4	2	3.0	pF
$V_{14}$	DC voltage at input	pin 14 not connected	1.5	1.8	2.1	V
$G_V$	voltage gain	see note 11	6	7	7.5	dB
$V_{o\ CVBS}$	CVBS output signal on pin 7 (peak-to-peak value)	sound carrier off; see Fig.3	1.7	2.0	2.3	V
	CVBS output level	upper video clipping	tbn	4.0	–	V
		lower video clipping	–	1.0	tbn	V
		sync level	1.25	1.35	1.45	V
$R_7$	output resistance		–	–	10	$\Omega$
$I_{int7}$	internal bias current for emitter follower	DC	1.8	2.5	–	mA
$I_7$	maximum output sink current	DC and AC	1.4	tbn	–	mA
	maximum output source current		2.4	tbn	–	mA
$B$	–3 dB video bandwidth	$C_7 < 20\ \text{pF};$ $R_L > 1\ \text{k}\Omega$	8	11	–	MHz
<b>Measurements from IF input to CVBS output (pin 7) 330 <math>\Omega</math> between pins 13 and 14, sound carrier off</b>						
$V_{o\ CVBS}$	CVBS output signal on pin 7 (peak-to-peak value)		1.7	2.0	2.3	V
$\Delta V_o$	deviation of CVBS output signal at B/G	50 dB gain control	–	–	0.5	dB
		30 dB gain control	–	–	0.1	dB
	black level tilt	B/G standard; see note 12	–	–	1	%

# VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$\Delta G$	differential gain		–	2	5	%
$\Delta\phi$	differential phase		–	1	3	deg
B	–3 dB video bandwidth	$C_L < 20 \text{ pF}$ ; $R_L > 1 \text{ k}\Omega$	6	8	–	MHz
S/N(W)	signal-to-noise ratio; weighted	see Fig.5 and note 13	56	59	–	dB
$\alpha_{1.1}$	intermodulation at 'blue'	see Fig.6 and note 14; $f = 1.1 \text{ MHz}$	56	62	–	dB
	intermodulation at 'yellow'		58	64	–	dB
$\alpha_{3.3}$	intermodulation at 'blue'	see Fig.6 and note 14; $f = 3.3 \text{ MHz}$	56	62	–	dB
	intermodulation at 'yellow'		57	63	–	dB
$\alpha_C$	residual vision carrier (RMS value)	fundamental wave	–	1	10	mV
		harmonics	–	1	10	mV
$\alpha_H$	suppression of video signal harmonics	see note 10	35	40	–	dB
RR	ripple rejection on pin 7	see Fig.9	25	28	–	dB
<b>AGC detector (pin 19)</b>						
$t_{\text{resp}}$	response to an increasing amplitude step of 50 dB in input signal		–	1	10	ms
	response to an decreasing amplitude step of 50 dB in input signal		–	50	100	ms
$I_{19}$	charging current	see note 12	0.85	1.1	1.35	mA
	discharging current		17	22	27	$\mu\text{A}$
$V_{19}$	AGC voltage	see Fig.4				
	maximum gain		0	tbn	–	V
	minimum gain		–	tbn	$V_P - 0.7$	V
<b>Tuner AGC (pin 12)</b>						
$V_i$	IF input signal for minimum starting point of tuner take over (RMS value)	input at pins 1 and 2; $R_{\text{TOP}} = 22 \text{ k}\Omega$	–	–	5	mV
	IF input signal for maximum starting point of tuner take over (RMS value)	input at pins 1 and 2; $R_{\text{TOP}} = 0 \Omega$	50	–	–	mV
$V_{12}$	allowable voltage	from external source	–	–	13.2	V
	saturation voltage	$I_{12} = 1.7 \text{ mA}$	–	–	0.2	V
$\Delta V_{12}$	variation of take over point by temperature	$\Delta T = 0 \text{ to } +50 \text{ }^\circ\text{C}$	–	1	3	dB
$I_{12}$	sink current	see Fig.4				
		no tuner gain reduction	–	0.1	0.3	$\mu\text{A}$
		maximum tuner gain reduction	1.7	2.0	2.6	mA
$\Delta G_{\text{IF}}$	IF slip by automatic gain control	tuner gain current from 20 to 80%	–	6	8	dB

# VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
<b>AFC circuit (pin 15)</b>		see Fig.8 and note 15				
S	control steepness $\Delta I_{15}/\Delta f$	see note 16	0.6	0.72	0.84	$\mu\text{A}/\text{kHz}$
$\Delta f_{IF}$	frequency variation by temperature	$\Delta T = 0 \text{ to } +70 \text{ }^\circ\text{C}$ ; see note 4	–	–	$\pm 1300$	$10^{-6}$
$V_{15}$	output voltage upper limit	see Fig.8	$V_P - 0.5$	$V_P - 0.3$	–	V
	output voltage lower limit		–	0.3	0.5	V
$I_{15}$	output current source		160	200	240	$\mu\text{A}$
	output current sink		160	200	240	$\mu\text{A}$
$\Delta I_{15}$	residual video modulation current (peak-to-peak value)		–	20	30	$\mu\text{A}$
<b>Sound mute switch (pin 5)</b>		see note 17				
$V_{IL}$	input voltage for MUTE-ON		0	–	0.8	V
$V_{IH}$	input voltage for MUTE-OFF		1.5	–	$V_P$	V
$I_{IL}$	LOW level input current	$V_5 = 0 \text{ V}$	–	–300	–360	$\mu\text{A}$
$\alpha_{mute}$	audio attenuation	$V_5 = 0 \text{ V}$	70	80	–	dB
$\Delta V_5$	DC offset voltage at switching (plop)	switching to MUTE-ON	–	100	500	mV
<b>FM sound limiter amplifier (pin 11)</b>		FM 5.5 MHz				
$V_{i\text{ FM}}$	input signal (RMS value, pin 11)	CCIR468-4				
	for S/N = 40 dB	see Fig.11	–	200	300	$\mu\text{V}$
	for AM suppression $\alpha_{AM} = 40 \text{ dB}$	AM: $f = 1 \text{ kHz}$ ; $m = 0.3$	–	1	–	mV
	maximum input signal handling (RMS value)		200	–	–	mV
$R_{11}$	input resistance		480	600	720	$\Omega$
B	–3 dB IF frequency response of sound IF	lower and upper cut-off frequency	3.5	–	10	MHz
$V_{11}$	DC voltage		2.3	2.6	2.9	V

# VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
<b>FM-PLL sound demodulator and AF output (pin 9)</b>		see note 18				
$f_{i\text{ FM}}$	catching range of PLL		4	–	7	MHz
	holding range of PLL		3.5	–	8	MHz
$t_{\text{acqu}}$	acquisition time		–	–	4	$\mu\text{s}$
$\Delta f_{\text{AF}}$	frequency deviation	THD < 1.5%; see note 19	–	–	$\pm 50$	kHz
$V_{10}$	DC voltage at decoupling capacitor	voltage dependent on VCO frequency; see note 20	1.2	–	2.2	V
$V_{o\text{ AF}}$	AF output signal (RMS value, pin 9)	$\Delta f_{\text{AF}} = \pm 27\text{ kHz}$ ; see Fig.11	280	350	420	mV
	maximum output signal handling	THD < 1.5%	0.8	–	–	V
$\Delta V_o$	temperature drift of AF output signal	$\Delta T = 0\text{ to }+70\text{ }^\circ\text{C}$	–	0.2	0.5	dB
$R_g$	output resistance		–	100	–	$\Omega$
$R_L$	load resistance (pin 9)		2.2	–	–	k $\Omega$
$V_g$	DC voltage		1.6	2.0	2.4	V
B	–3 dB audio frequency bandwidth		95	120	–	kHz
THD	total harmonic distortion		–	0.1	0.5	%
S/N (W)	signal-to-noise ratio, weighted	CCIR468-4; see Fig.11	50	55	–	dB
$V_{\text{SC}}$	residual sound carrier and harmonics (RMS value)		–	–	75	mV
$\alpha_{\text{AM}}$	AM suppression	see Fig.10; AM: $f = \text{kHz}$ ; $m = 0.3$	46	50	–	dB
RR	ripple rejection on pin 9	see Fig.9	26	30	–	dB
<b>Measurements from IF input to audio output (pin 9)</b>		560 $\Omega$ between pins 13 and 11				
S/N (W)	weighted signal-to-noise ratio referred to 54% FM modulation	CCIR468-4; with offset alignment on pin 4				
	6 kHz sinusoidal waveform	black-to-white	39	46	–	dB
	black level	sync only	39	48	–	dB
	white picture		39	46	–	dB

## VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804

### Notes to the characteristics

1. Typical values of video and sound parameters are decreased at  $V_P = 4.5$  V.
2. The input voltage for internal AGC has to be  $V_g > 1.5$  V, or pin 8 open-circuit.
3. Loop bandwidth  $BL = 60$  kHz (natural frequency  $f_n = 15$  kHz; damping factor  $d = 2$  calculated with grey level and FPLL input signal level).  
Resonance circuit of VCO:  $Q_o > 50$ ;  $C_{ext} = 8.2$  pF;  $C_{int} \approx 8.5$  pF (loop voltage about 2.7 V).
4. The oscillator drift is related to the picture carrier frequency (at external temperature-compensated LC-circuit).
5.  $V_{iIF} = 10$  mV (RMS value);  $\Delta f = 1$  MHz (VCO frequency offset related to picture carrier frequency); white picture video modulation.
6.  $V_{iIF}$  for 0.9 V CVBS (peak-to-peak value) at composite video output pin 13; PLL is still locked.
7. Transformer at IF input (Fig.3). The C/N ratio at IF input for 'lock-in' is defined as the vision IF input signal (sync level, RMS value) in relation to a superimposed, 5 MHz band-limited white noise signal (RMS value); video modulation: white picture.
8. Offset current measured between pin 6 and half of supply voltage ( $V = 2.5$  V) under the following conditions: no input signal at IF input (pins 1 and 2) and IF-amplifier gain at minimum ( $V_{19} = V_P$ ), pin 4 (phase adjust) open-circuit.
9. The intercarrier output signal is superimposed to the video signal at pin 13 and can be calculated by the following formula:  

$$20 \log \left( \frac{V_{13 \text{ interc.}} (p-p)}{1 \text{ V } (p-p)} \right) = \frac{V_{iSC}}{V_{iPC}} \text{ dB} + 6.9 \text{ dB} \pm 2 \text{ dB}$$

with  
 $\frac{V_{iSC}}{V_{iPC}} \text{ dB} =$  sound to picture carrier ratio at IF input (pins 1 and 2) in dB  
and  
 $\pm 2 \text{ dB} =$  tolerance of intercarrier output amplitude  $V_{o \text{ FM}}$ .
10. Measurements taken with SAW filter G1956; modulation: VSB,  $f_{\text{video}} > 0.5$  MHz, loop bandwidth  $BL = 60$  kHz.
11. The 7 dB buffer gain accounts for 1 dB loss in the sound trap. Buffer output signal is typical 2 V (p-p). If no sound trap is applied a 330  $\Omega$  resistor must be connected from output to input (from pin 13 to pin 14).
12. The leakage current of the AGC capacitor has to be  $< 1$   $\mu\text{A}$  to avoid larger tilt.
13. S/N is the ratio of black-to-white amplitude to the black level noise voltage (RMS value, pin 7).  $B = 5$  MHz weighted in accordance with CCIR-567 at a source impedance of 50  $\Omega$ .
14.  $\alpha_{1.1} = 20 \log (V_o \text{ at } 4.4 \text{ MHz} / V_o \text{ at } 1.1 \text{ MHz}) + 3.6 \text{ dB}$ ;  $\alpha_{1.1}$  value at 1.1 MHz related to black/white signal  
 $\alpha_{3.3} = 20 \log (V_o \text{ at } 4.4 \text{ MHz} / V_o \text{ at } 3.3 \text{ MHz})$ ;  $\alpha_{3.3}$  value at 3.3 MHz related to colour carrier.
15. To match the AFC output signal to different tuning systems a current source output is provided (Fig.8).
16. Depending on the ratio  $\Delta C/C_o$  of the LC resonance circuit of VCO  
( $Q_o > 50$ ;  $C_o = C_{int} + C_{ext}$ ;  $C_{ext} = 8.2$  pF;  $C_{int} \approx 8.5$  pF).
17. No mute state is also valid for pin not connected.
18. A 5.5 MHz signal for second IF with 10 mV (RMS value) input level,  $f_{\text{mod}} = 1$  kHz and frequency deviation with 54% FM modulation of audio reference is fed directly to pin 11. Audio measurements are taken at 50  $\mu\text{s}$  de-emphasis.
19. To allow higher frequency deviation, the resistor  $R_x$  on pin 10 (see Fig.12) has to be increased to a value which does not exceed the AF output signal of nominally 0.35 V for THD = 0.1% ( $R_x = 4.7$  k $\Omega$  provides -6 dB amplification).
20. The leakage current of the 2.2  $\mu\text{F}$  capacitor is  $< 100$  nA.

VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804

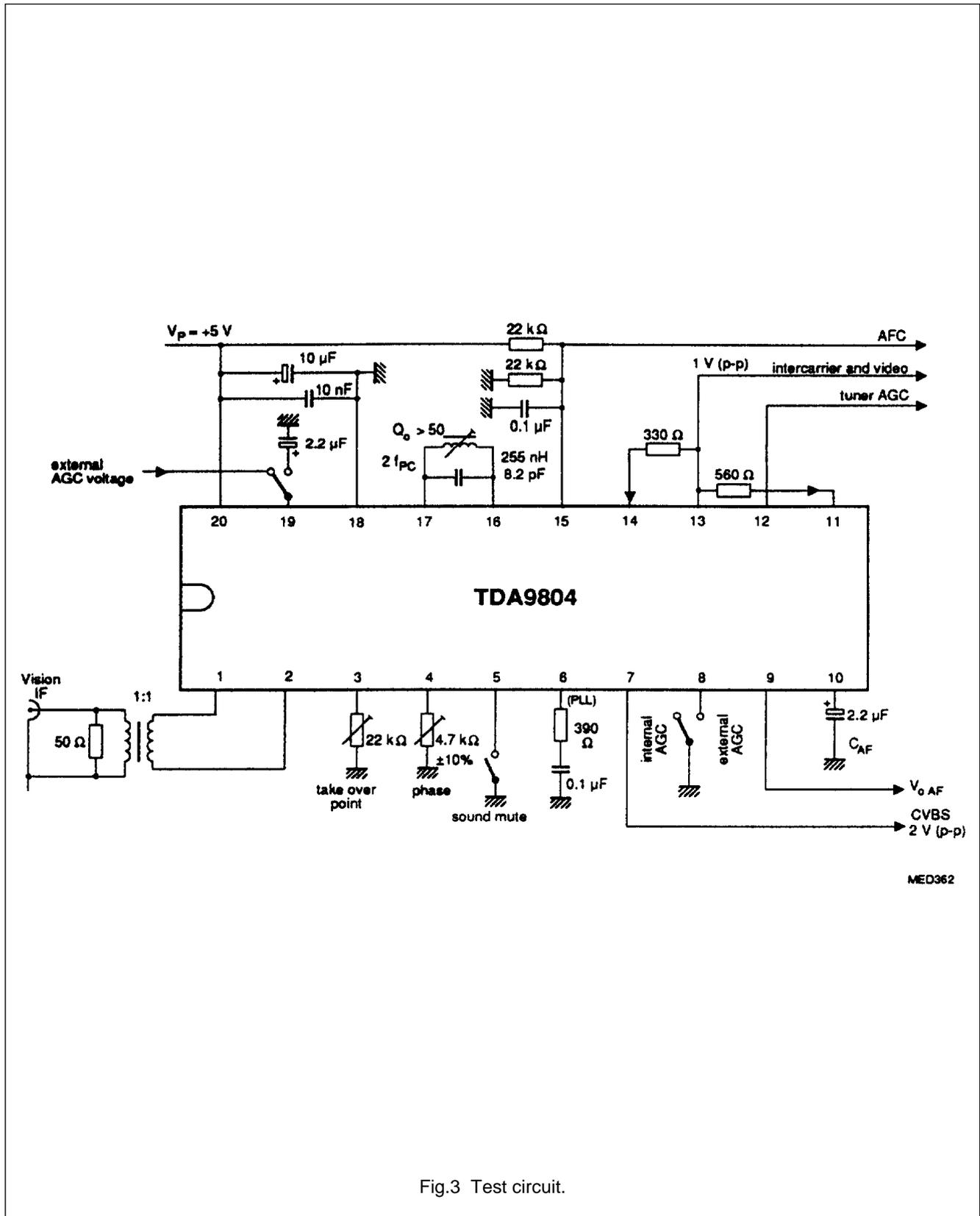


Fig.3 Test circuit.

VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804

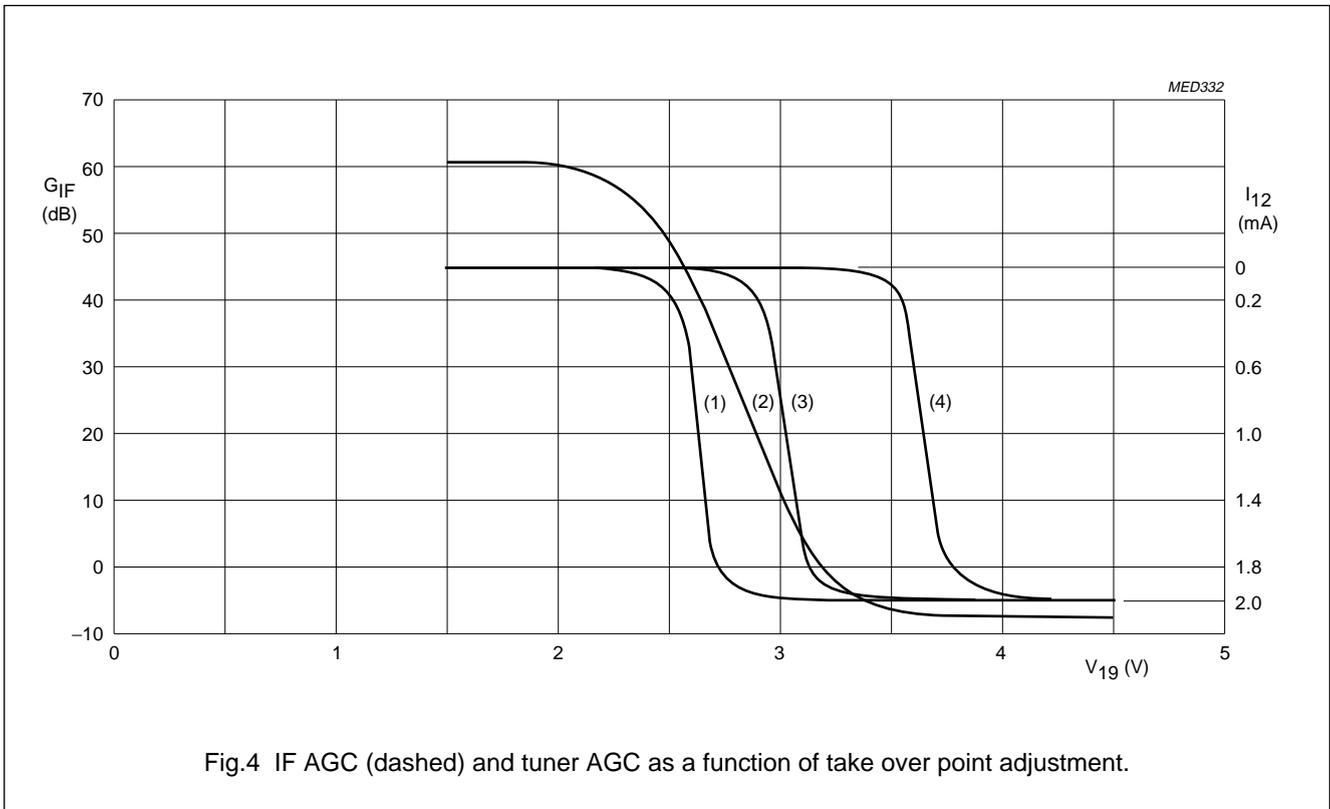


Fig.4 IF AGC (dashed) and tuner AGC as a function of take over point adjustment.

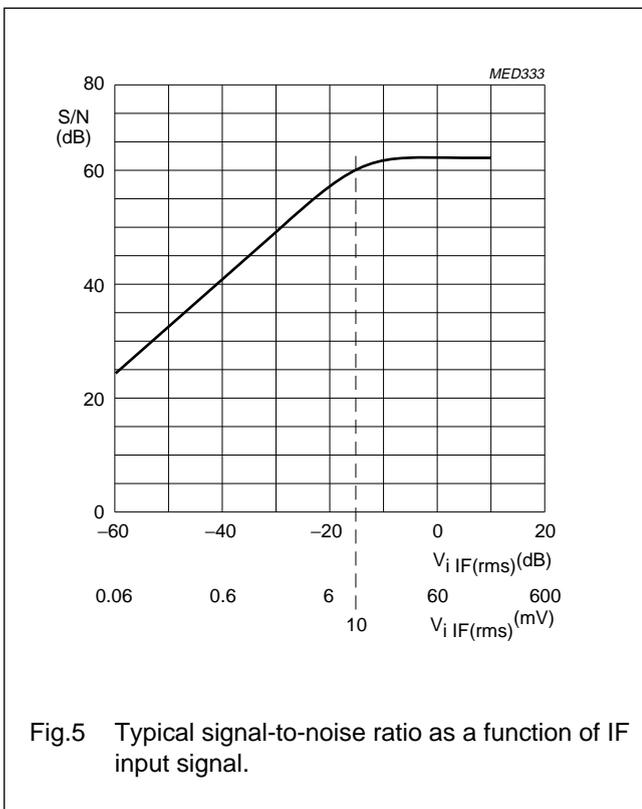
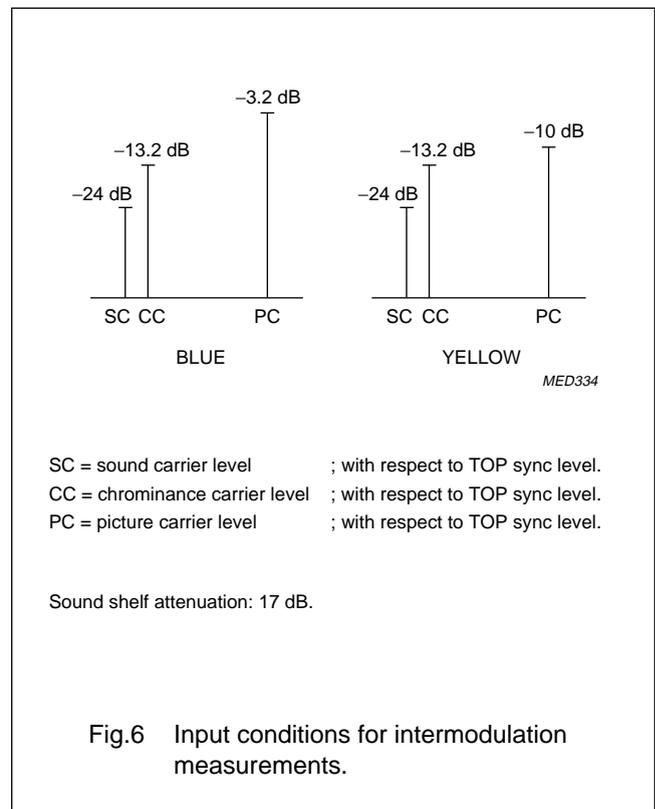


Fig.5 Typical signal-to-noise ratio as a function of IF input signal.



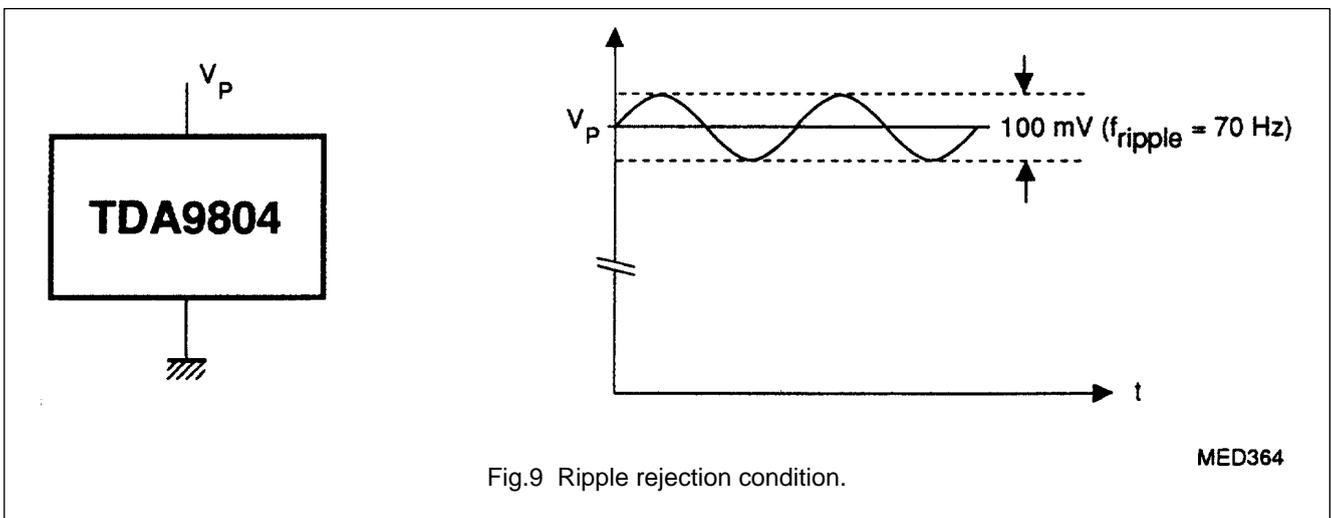
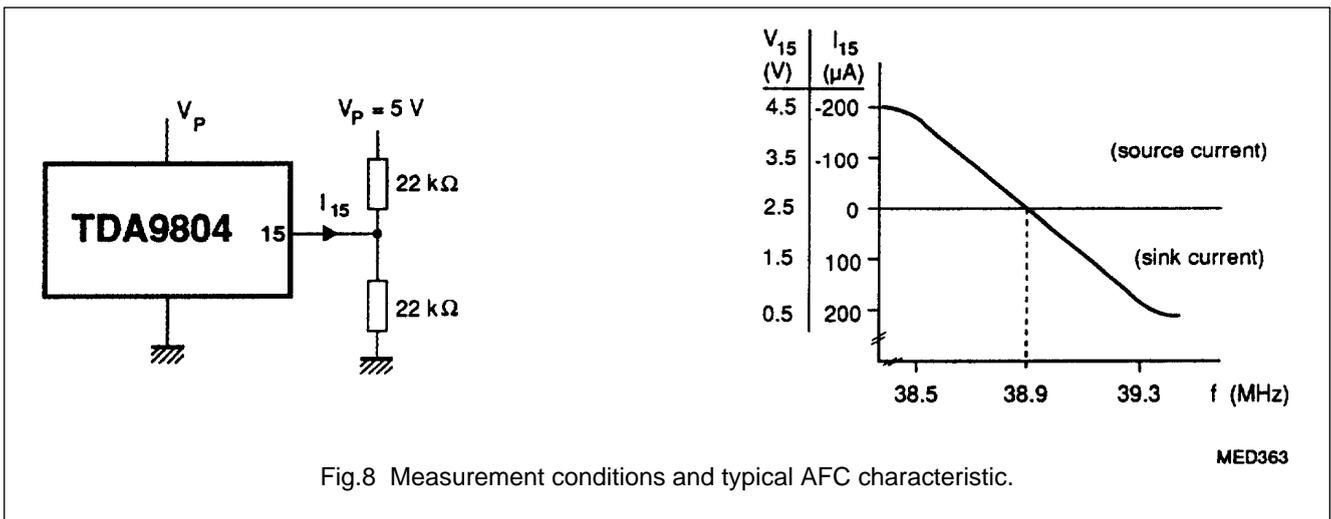
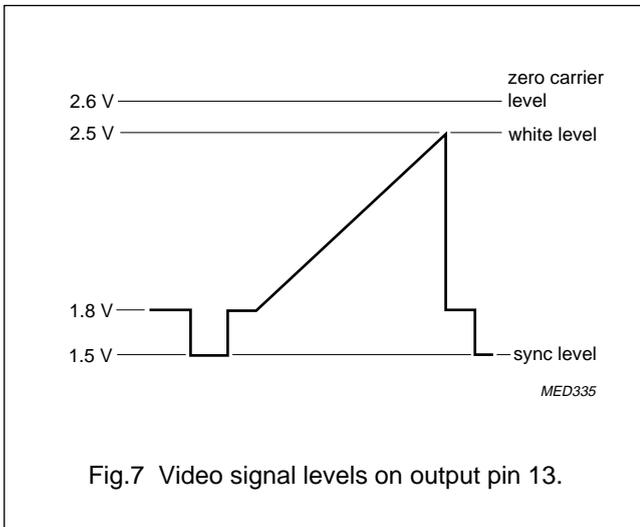
SC = sound carrier level ; with respect to TOP sync level.  
 CC = chrominance carrier level ; with respect to TOP sync level.  
 PC = picture carrier level ; with respect to TOP sync level.

Sound shelf attenuation: 17 dB.

Fig.6 Input conditions for intermodulation measurements.

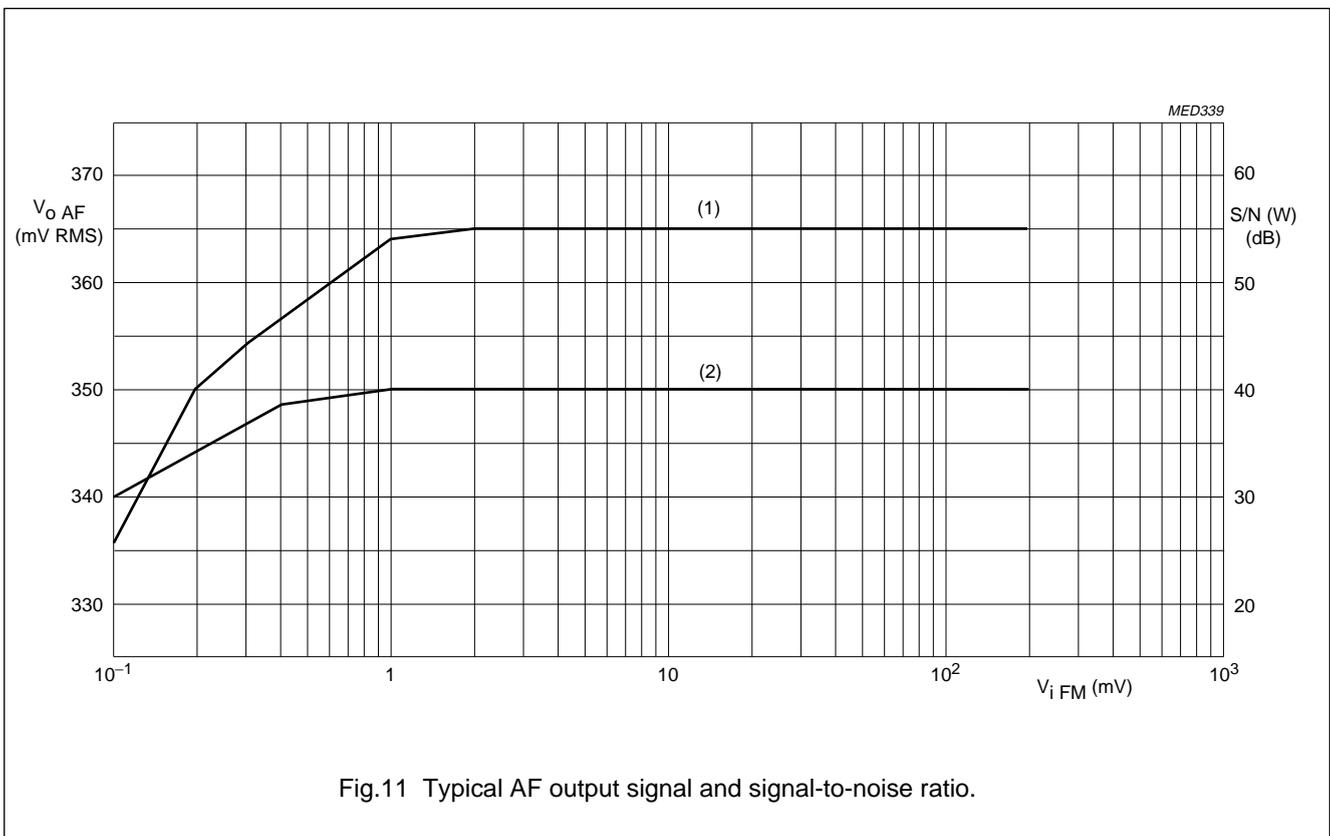
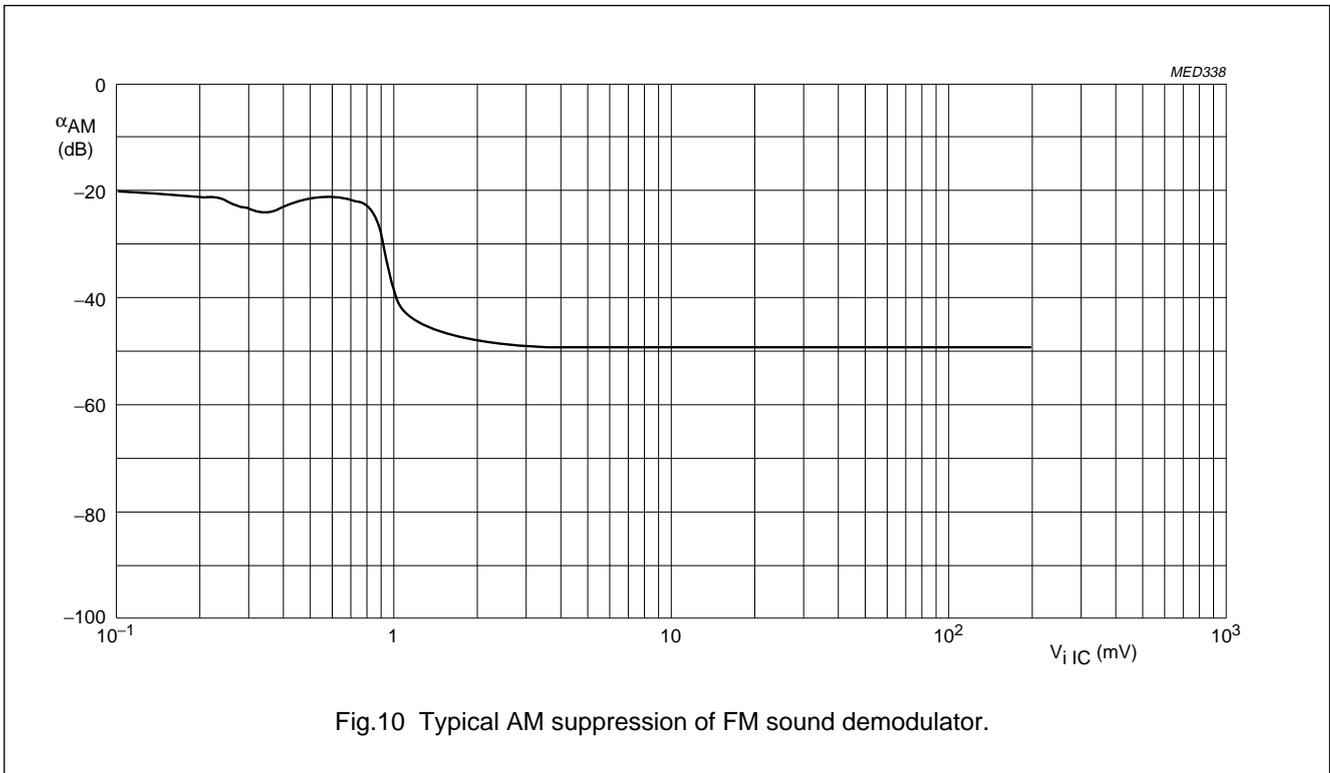
VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804



VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804



VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804

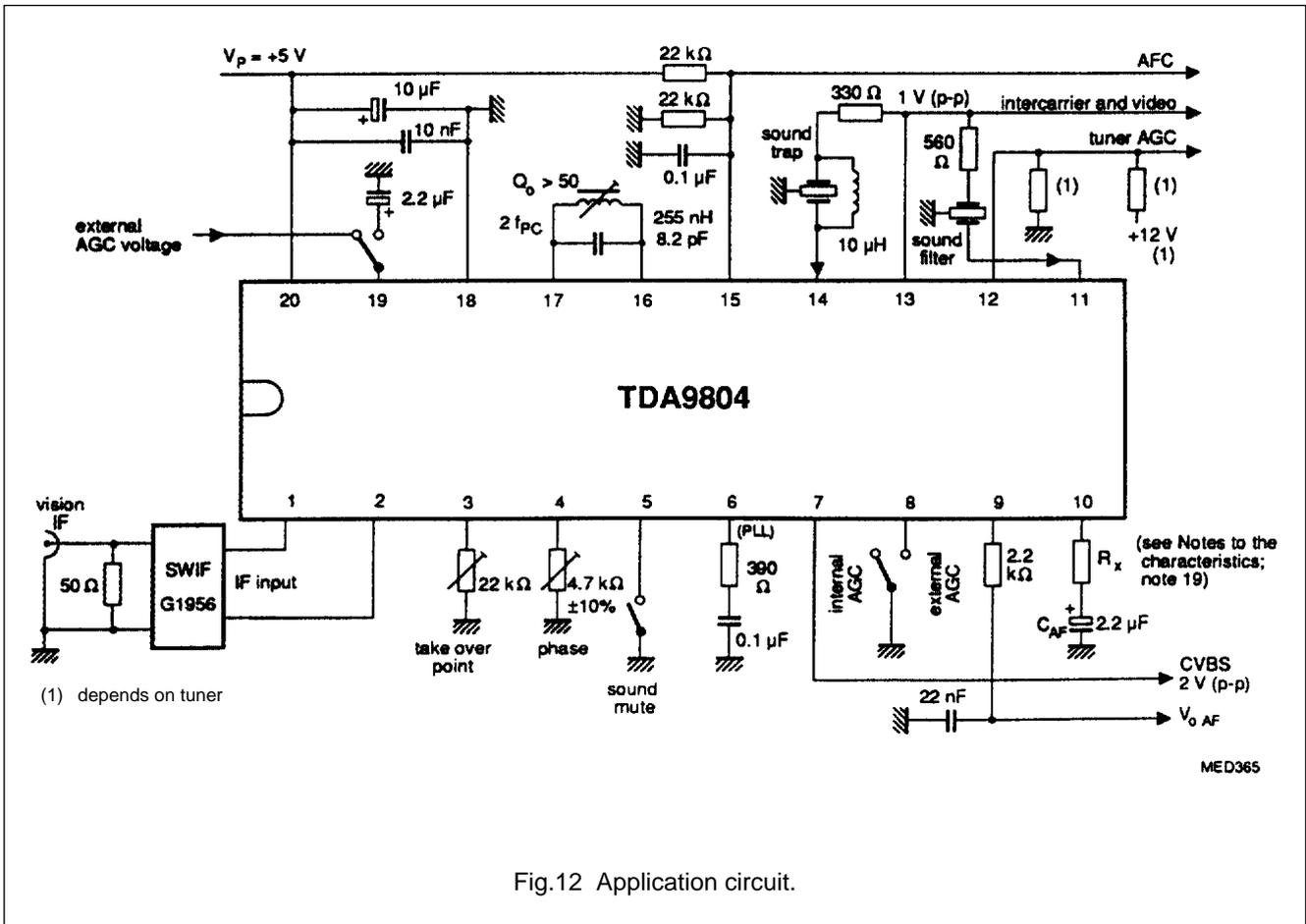
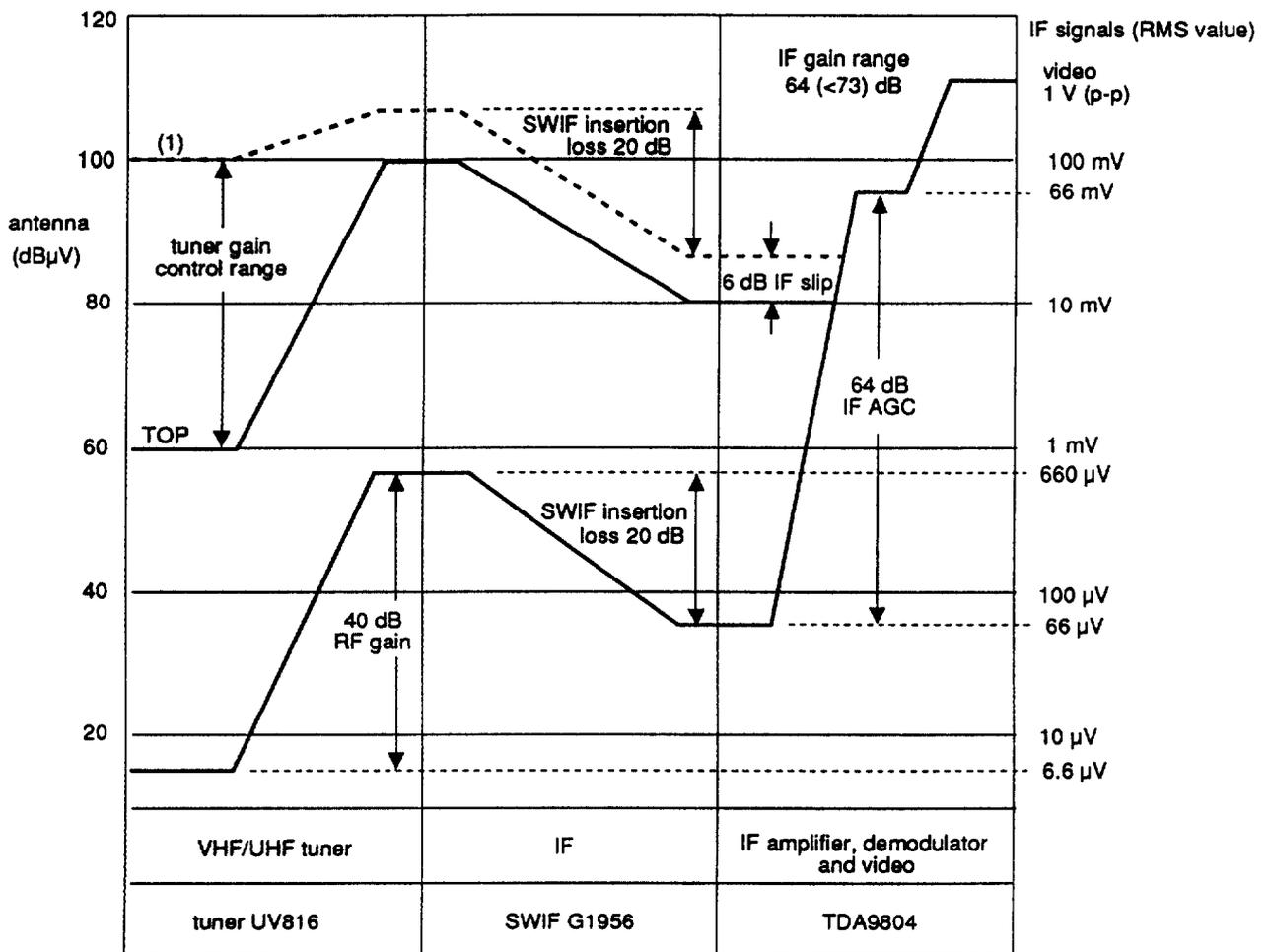


Fig.12 Application circuit.

VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804



(1) depends on TOP

MED366

Fig.13 Front end level diagram.

# VIF-PLL demodulator with internal or external AGC and FM-PLL detector

## TDA9804

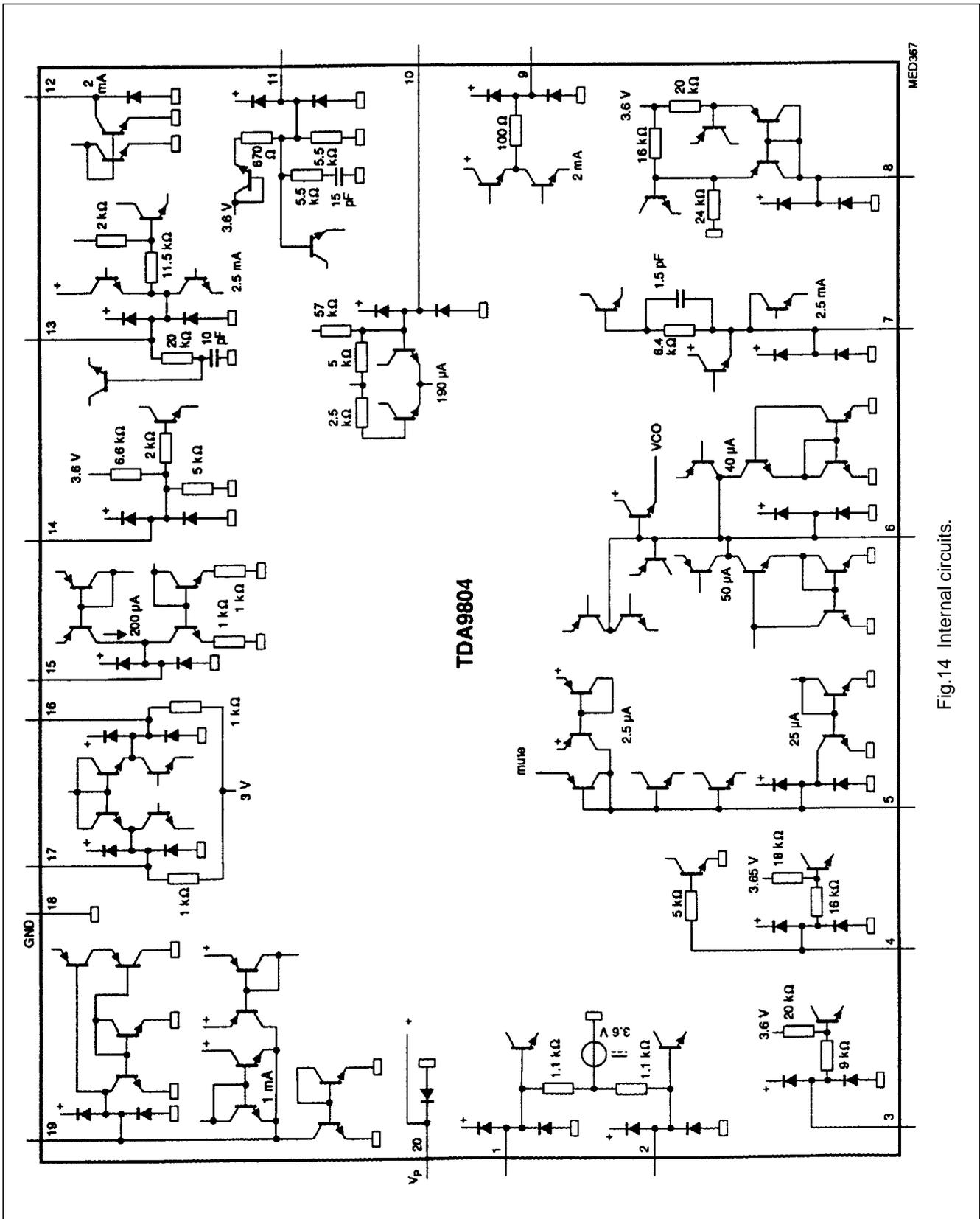


Fig.14 Internal circuits.

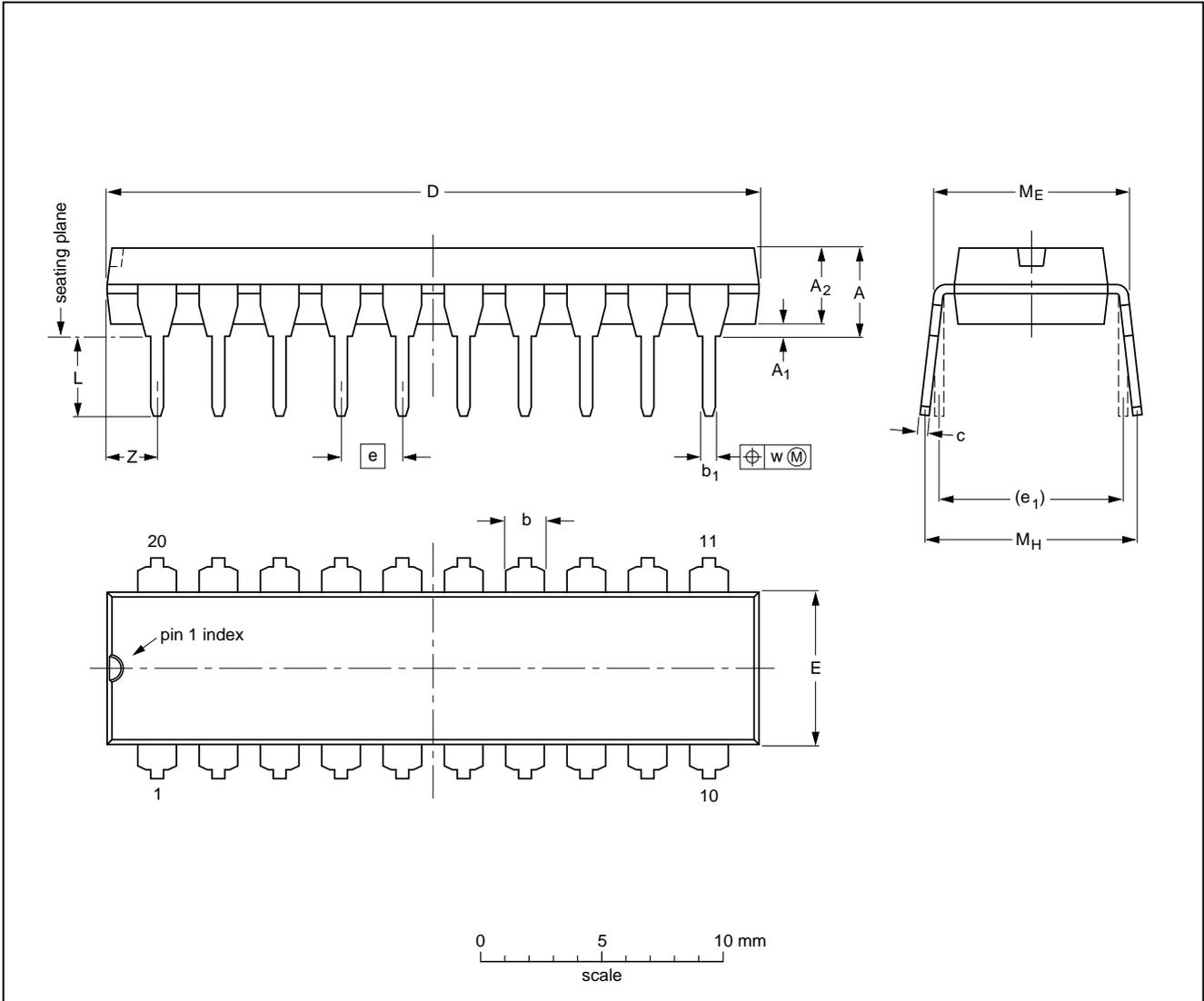
VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804

PACKAGE OUTLINE

DIP20: plastic dual in-line package; 20 leads (300 mil)

SOT146-1



DIMENSIONS (inch dimensions are derived from the original mm dimensions)

UNIT	A max.	A <sub>1</sub> min.	A <sub>2</sub> max.	b	b <sub>1</sub>	c	D <sup>(1)</sup>	E <sup>(1)</sup>	e	e <sub>1</sub>	L	M <sub>E</sub>	M <sub>H</sub>	w	Z <sup>(1)</sup> max.
mm	4.2	0.51	3.2	1.73 1.30	0.53 0.38	0.36 0.23	26.92 26.54	6.40 6.22	2.54	7.62	3.60 3.05	8.25 7.80	10.0 8.3	0.254	2.0
inches	0.17	0.020	0.13	0.068 0.051	0.021 0.015	0.014 0.009	1.060 1.045	0.25 0.24	0.10	0.30	0.14 0.12	0.32 0.31	0.39 0.33	0.01	0.078

Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT146-1			SC603			92-11-17 95-05-24

# VIF-PLL demodulator with internal or external AGC and FM-PLL detector

TDA9804

## SOLDERING

### Introduction

There is no soldering method that is ideal for all IC packages. Wave soldering is often preferred when through-hole and surface mounted components are mixed on one printed-circuit board. However, wave soldering is not always suitable for surface mounted ICs, or for printed-circuits with high population densities. In these situations reflow soldering is often used.

This text gives a very brief insight to a complex technology. A more in-depth account of soldering ICs can be found in our "IC Package Databook" (order code 9398 652 90011).

### Soldering by dipping or by wave

The maximum permissible temperature of the solder is 260 °C; solder at this temperature must not be in contact with the joint for more than 5 seconds. The total contact time of successive solder waves must not exceed 5 seconds.

The device may be mounted up to the seating plane, but the temperature of the plastic body must not exceed the specified maximum storage temperature ( $T_{stg\ max}$ ). If the printed-circuit board has been pre-heated, forced cooling may be necessary immediately after soldering to keep the temperature within the permissible limit.

### Repairing soldered joints

Apply a low voltage soldering iron (less than 24 V) to the lead(s) of the package, below the seating plane or not more than 2 mm above it. If the temperature of the soldering iron bit is less than 300 °C it may remain in contact for up to 10 seconds. If the bit temperature is between 300 and 400 °C, contact may be up to 5 seconds.

## DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

## LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.