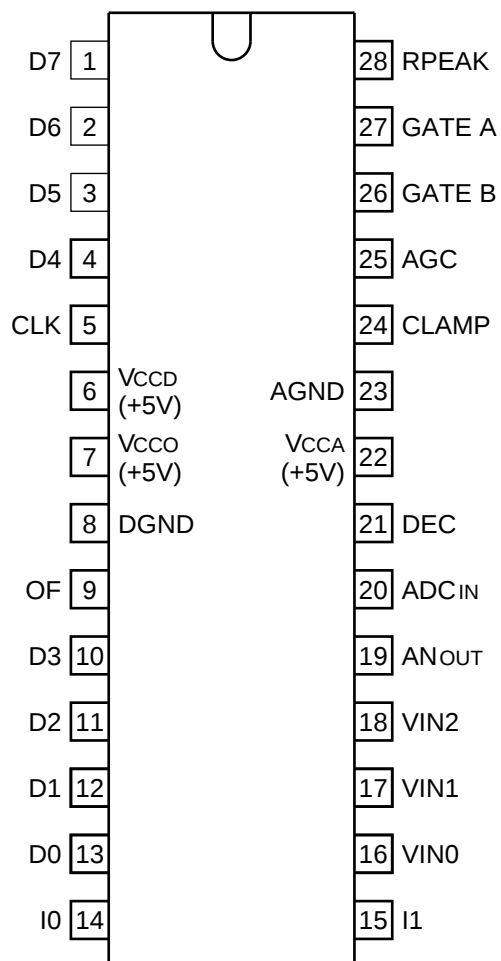

VIDEO ANALOG INPUT INTERFACE

- TOP VIEW -



INPUT

ADC IN ; ANALOG TO DIGITAL CONVERTER INPUT
AGC ; AGC CAPACITOR CONNECTION
CLAMP ; CLAMP CAPACITOR CONNECTION
CLK ; CLOCK INPUT
DEC ; DECOUPLING INPUT
GATE A ; SYNC LEVEL SYNCHRONIZATION PULSE
GATE B ; BLACK LEVEL SYNCHRONIZATION PULSE
I0,I1 ; VIDEO INPUT SELECTION (BIT 0,1)
OF ; OUTPUT FORMAT/CHIP ENABLE
(3-STATE INPUT)
RPEAK ; PEAK LEVEL CURRENT RESISTOR INPUT
VIN0-2 ; VIDEO INPUT 0-2

OUTPUT

AN OUT ; ANALOG VOLTAGE OUTPUT
D0-D7 ; DATA OUTPUT (BIT0-7)
(BIT0:LSB,BIT7:MSB)

