

Product Brief

TDA 5200

ASK Super Heterodyne Receiver



The IC is a very low power consumption single chip ASK Superheterodyne Receiver (SHR) for the frequency bands 868-870 MHz and 433-435 MHz. The SHR offers a high level of

integration and needs only a few external components. The device contains a low noise amplifier (LNA), a double balanced mixer, a fully integrated VCO, a PLL synthesizer, a crystal oscillator, a

limiter with RSSI generator, a data filter, a data comparator and a peak detector. Additionally there is a power down feature to save battery life.

Potential Application

- Keyless entry systems
- Remote control systems
- Alarm systems
- Communication systems

Features

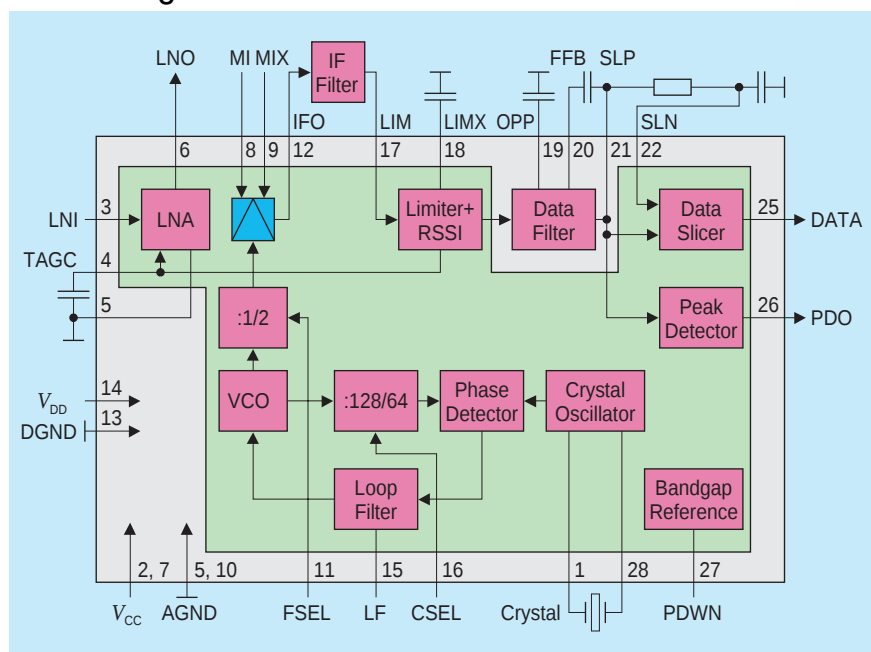
- Low supply current
- Supply voltage range 5V $\pm$ 10%
- Power down mode
- Fully integrated VCO and PLL synthesizer
- Sensitivity 1 μ V at 50 Ω input impedance

- Switchable frequency range 868-870 / 433-435 MHz
- Limiter frequency range 9 kHz to 15 MHz
- 2nd order low pass data filter with external capacitors
- Data comparator with selectable threshold

Packing

Type	Ordering Code	Package
TDA 5200	Q67036-A1071	P-TSSOP-28

Block Diagram



Price Projection

- Standard price list
- For Key and Focus accounts or high volumes you may contact your regional Siemens sales partner

Documentation	Date of Issue/Version
Specification	7/98
Articles/Press Releases – Components	4/98

Development and Support Tools

- Evaluation Board TDA 5200

Availability

**ES available;
Product development release 04/99**

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