INTEGRATED CIRCUITS



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### **GENERAL DESCRIPTION**

The TDA1016 is a monolithic integrated audio power amplifier, preamplifier and A.L.C. circuit designed for applications in radio-recorders and recorders. The wide supply voltage range makes this circuit very suitable for d.c. and a.c. apparatus. The circuit incorporates the following features:

### Features

- Power amplifier/monitor amplifier
- · Preamplifier/record and playback amplifier
- Automatic Level Control (A.L.C.) circuit
- Voltage stabilizer
- Short-circuit (up to 12 V a.c.) and thermal protection.

### QUICK REFERENCE DATA

Supply voltage range	VP		3,6 to 15 V
Supply current; total quiescent at $V_P = 6 V$	I <sub>tot</sub>	typ.	10 mA
Operating ambient temperature range	T <sub>amb</sub>		–25 to 150 °C
Power amplifier			
Output power at d <sub>tot</sub> = 10%			
$V_P = 6 V; R_L = 4 \Omega$	Po	typ.	1 W
$V_P = 9 V; R_L = 4 \Omega$	Po	typ.	2 W
Closed loop gain	G <sub>c</sub>	typ.	36 dB
Preamplifier			
Open loop gain	Go	min.	70 dB
Minimum closed loop voltage gain	G <sub>c min</sub>	min.	35 dB
Output voltage at d <sub>tot</sub> = 1%	Vo	min.	1 V
Automatic Level Control (A.L.C.)			
Gain variation for $\Delta V_i = 40 \text{ dB}$	$\Delta G_v$	typ.	2 dB
Stabilized supply voltage			
Output voltage	V <sub>5-16</sub>	typ.	2,6 V

### PACKAGE OUTLINE

16-lead DIL; plastic, with internal heat spreader (SOT38); SOT38-1; 1996 July 23.

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## Recording/playback and 2 W audio power amplifier



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### RATINGS

Limiting values in accordance with the Absolute Maximum System (IEC 13	34)		
Supply voltage (pin 3)	VP	max.	18 V
Repetitive peak output current	I <sub>ORM</sub>	max.	1 A
Non-repetitive peak output current (pin 1)	I <sub>OSM</sub>	max.	2 A
Total power dissipation	see dera	ting curve Fig	.2
A.C. short-circuit duration of load during			
sinewave drive; $V_P = 12 V$	t <sub>sc</sub>	max.	100 hours
Crystal temperature	Т <sub>с</sub>	max.	150 °C
Storage temperature range	T <sub>stg</sub>	-55	to + 150 °C
Operating ambient temperature range	T <sub>amb</sub>	-25	to + 150 °C

### THERMAL RESISTANCE

The power derating curve (Fig.2) is based on the following data

From junction to ambient

 $R_{th j-a} = 55 \text{ K/W}$ 



Fig.2 Power derating curve.

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### CHARACTERISTICS

 $V_P = 6 V$ ;  $R_L = 4 \Omega$ ; f = 1 kHz;  $T_{amb} = 25 \text{ °C}$ ; measured in test circuit Fig.1; unless otherwise specified

SYMBOL	PARAMETER	MIN.	TYP.	MAX.	UNIT
Supply (pin 3)	-				4
V <sub>P</sub>	Supply voltage	3,6	6	15	V
	Supply current; total quiescent				
I <sub>tot</sub>	at $V_P = 6 V$	_	10	_	mA
Power amplifier					4
	Output power at d <sub>tot</sub> = 10%; see note 1				
Po	$V_{P} = 6 V$	_	1	_	w
Po	$V_{P} = 9 V$	_	2	_	w
G <sub>c</sub>	Closed loop voltage gain	_	36	_	dB
d <sub>tot</sub>	Total harmonic distortion at $P_0 = 0.5 W$	_	_	1	%
Z <sub>i</sub>	Input impedance	0,5	_	_	MΩ
RR	Ripple rejection at f = 100 Hz ( $R_S = 0 \Omega$ )	40	50	_	dB
	Noise output voltage (r.m.s. value)				
V <sub>n(rms)</sub>	$R_{S} = 0 \Omega$ ; B = 60 Hz to 15 kHz	_	90	200	μV
<b>、</b> ,	Noise output voltage at 500 kHz				
V <sub>n</sub>	R <sub>S</sub> = 0 Ω; B = 5 kHz	_	8	_	μV
Preamplifier					•
G <sub>o</sub>	Open loop voltage gain at f = 10 kHz	70	78	_	dB
G <sub>c</sub>	Closed loop voltage gain	_	52	_	dB
	Minimum closed loop voltage gain				
G <sub>c min</sub>	(when changing R <sub>f</sub> )	35	_	_	dB
V <sub>o</sub>	Output voltage at d <sub>tot</sub> = 1%	1	_	_	V
	Output voltage with A.L.C.				
Vo	$V_i = 2 \text{ mV}$	0,45	0,5	0,55	V
	Total harmonic distortion with A.L.C.				
d <sub>tot</sub>	$V_i = 2 \text{ mV}$	_	_	1	%
d <sub>tot</sub>			_	3	%
	Signal-to-noise ratio related to $V_i = 1,2 \text{ mV}$ ;				
S/N	$R_S = 1 \text{ k}\Omega; B = 60 \text{ Hz to } 15 \text{ kHz}$	_	60	_	dB
Z <sub>i</sub>	Input impedance	100	_	_	kΩ
	Ripple rejection				
RR	at f = 100 Hz; $R_S = 0 \Omega$	50	54	_	dB
Z <sub>o</sub>	Output impedance; see note 2	_	_	50	Ω

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SYMBOL	PARAMETER	MIN.	TYP.	MAX.	UNIT
Automatic Level Control (A.L.C.) (see Fig.3); see note 4					
$\Delta G_v$	Gain variation for $\Delta V_i = 45 \text{ dB}$	- 2 3			dB
t <sub>l</sub>	Limiting time; see note 3	50			ms
t <sub>s</sub>	Level setting time; see note 3	50			ms
t <sub>r</sub>	Recovery time; see notes 3 and 5 - 100 -				s
Voltage stabilize	er				
V <sub>11-15</sub>	Output voltage	_	2,6	_	V
I <sub>11</sub>	Load current	_	_	1,5	mA
RR	Ripple rejection at f = 100 Hz	40	_	_	dB

### Notes

1. Measured with an ideal coupling capacitor connected to the speaker load.

2. I<sub>P</sub> (effective value) must not exceed 1 mA.

3. At  $\Delta V_i = dB$  with respect to  $V_i = 1,2$  mV.

4. The A.L.C. tracking in stereo has a typical spread of 1 dB if pins 6 of both ICs are connected to the same RC network.

5. Without a shunt resistor across A.L.C. With 1 M $\Omega$  or 2,2 M $\Omega$  across A.L.C. recovery time becomes 22 or 50 seconds.



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### PACKAGE OUTLINE

### DIP16: plastic dual in-line package; 16 leads (300 mil); long body



#### Note

1. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

OUTLINE		REFERENCES			EUROPEAN ISSUE DATE	
VERSION	IEC	JEDEC	EIAJ		PROJECTION	ISSUE DATE
SOT38-1	050G09	MO-001AE				<del>92-10-02</del> 95-01-19

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### SOLDERING

#### Introduction

There is no soldering method that is ideal for all IC packages. Wave soldering is often preferred when through-hole and surface mounted components are mixed on one printed-circuit board. However, wave soldering is not always suitable for surface mounted ICs, or for printed-circuits with high population densities. In these situations reflow soldering is often used.

This text gives a very brief insight to a complex technology. A more in-depth account of soldering ICs can be found in our "IC Package Databook" (order code 9398 652 90011).

#### Soldering by dipping or by wave

The maximum permissible temperature of the solder is 260 °C; solder at this temperature must not be in contact with the joint for more than 5 seconds. The total contact time of successive solder waves must not exceed 5 seconds.

The device may be mounted up to the seating plane, but the temperature of the plastic body must not exceed the specified maximum storage temperature (Tstg max). If the printed-circuit board has been pre-heated, forced cooling may be necessary immediately after soldering to keep the temperature within the permissible limit.

#### Repairing soldered joints

Apply a low voltage soldering iron (less than 24 V) to the lead(s) of the package, below the seating plane or not more than 2 mm above it. If the temperature of the soldering iron bit is less than 300 °C it may remain in contact for up to 10 seconds. If the bit temperature is between 300 and 400 °C, contact may be up to 5 seconds.

#### DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	

Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

#### **Application information**

Where application information is given, it is advisory and does not form part of the specification.

#### LIFE SUPPORT APPLICATIONS

These products are not designed for use in life support appliances, devices, or systems where malfunction of these products can reasonably be expected to result in personal injury. Philips customers using or selling these products for use in such applications do so at their own risk and agree to fully indemnify Philips for any damages resulting from such improper use or sale.