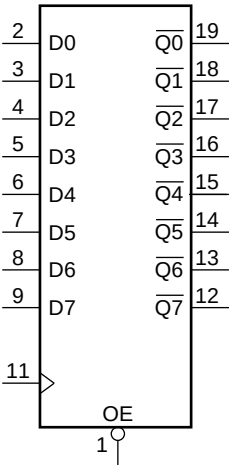
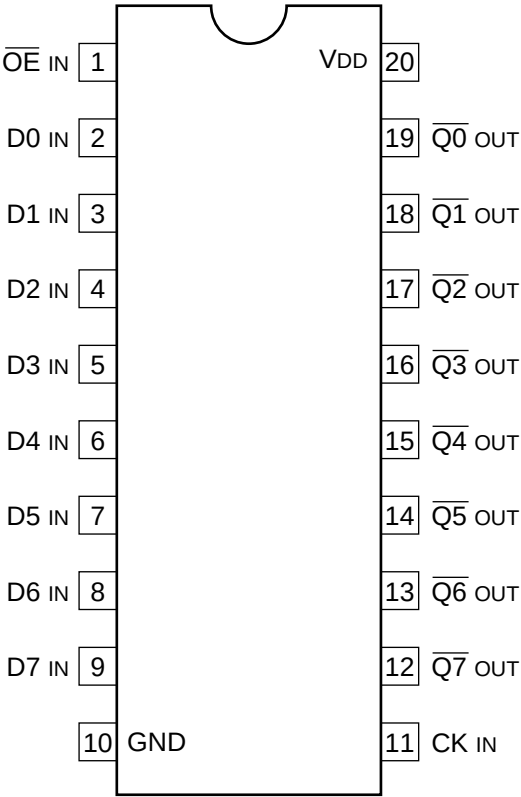


C-MOS D-TYPE FLIP-FLOPS WITH 3-STATE OUTPUTS

—TOP VIEW—



OE	CK	D	$\overline{Q}$
1	X	X	HI-Z
0		X	$\overline{Qn}$
0		0	1
0		1	0

0 ; LOW LEVEL
1 ; HIGH LEVEL
X ; DON'T CARE
HI-Z ; HIGH IMPEDANCE
 $\overline{Qn}$; NO CHANGE

