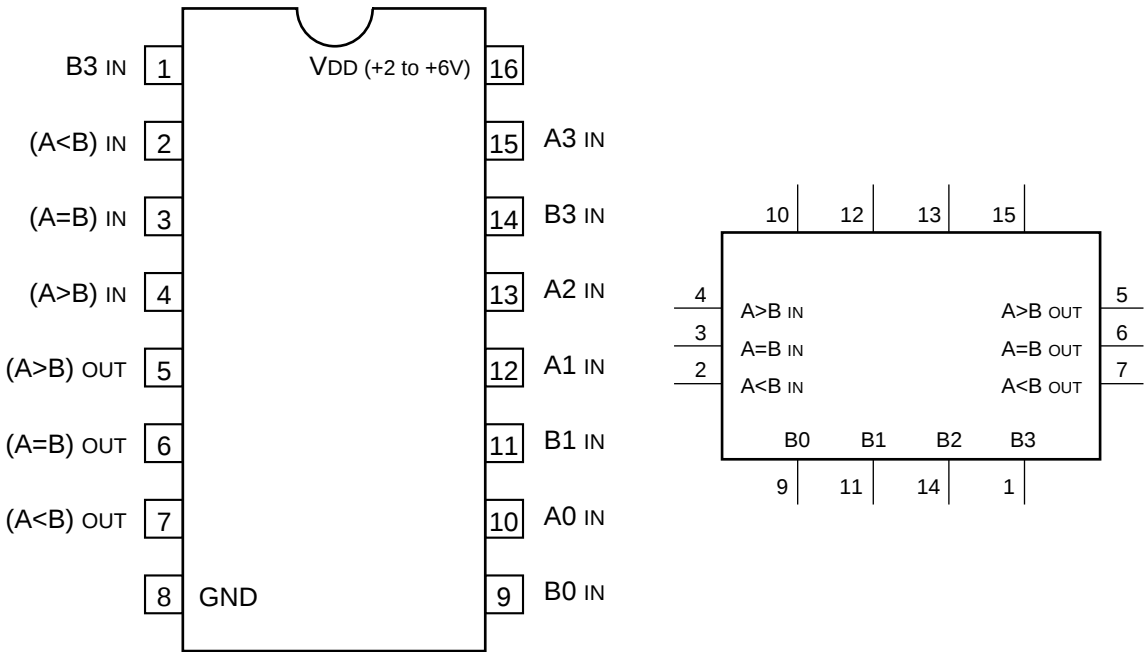

C-MOS 4-BIT MAGNITUDE COMPARATOR

—TOP VIEW—



INPUTS										
DATA COMPARING					CASCADING			OUTPUTS		
	A3, B3	A2, B2	A1, B1	A0, B0	A<B	A=B	A>B	A<B	A=B	A>B
A>B	A3>B3	X	X	X	X	X	X	0	0	1
	A3=B3	A2>B2	X	X						
	A3=B3	A2=B2	A1>B1	X						
	A3=B3	A2=B2	A1=B1	A0>B0						
A=B	A3=B3	A2=B2	A1=B1	A0=B0	0	0	0	1	0	1
					0	0	1	0	0	1
					X	1	X	0	1	0
					1	0	0	1	0	0
A<B	A3=B3	A2=B2	A1=B1	A0<B0	X	X	X	1	0	0
A<B	A3<B3	X	X	X	X	X	X	1	0	0
	A3=B3	A2<B2	X	X						
	A3=B3	A2=B2	A1<B1	X						
	A3>B3	X	X	X						

0; LOW LEVEL 1; HIGH LEVEL X; DON'T CARE