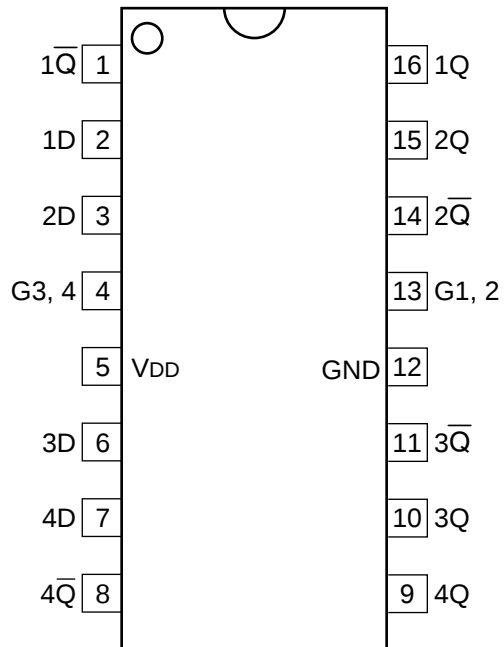


C-MOS 4-BIT D-TYPE LATCH

—TOP VIEW—



INPUTS		OUTPUTS		FUNCTION
D	G	Q	$\overline{Q}$	
0	1	0	1	—
1	1	1	0	—
x	0	Q_n	$\overline{Q}_n$	LATCH

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

