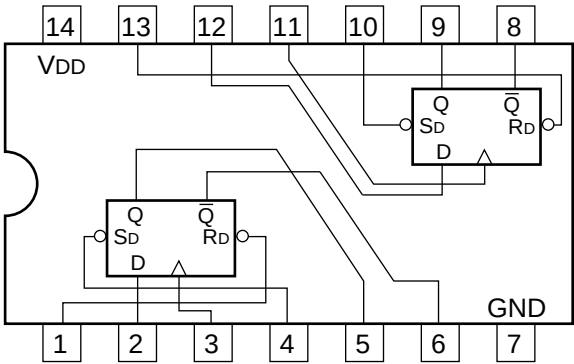


C-MOS DUAL D-TYPE FLIP-FLOPS WITH DIRECT SET/RESET

—TOP VIEW—



INPUTS				OUTPUTS	
$\overline{S_D}$	$\overline{R_D}$	CK	D	Q_{n+1}	$\overline{Q}_{n+1}$
0	1	x	x	1	0
1	0	x	x	0	1
0	0	x	x	1	1
1	1		1	1	0
1	1		0	0	1
1	1	0	x	Q_n	$\overline{Q}_n$

0 : LOW LEVEL
1 : HIGH LEVEL
x : DON'T CARE

