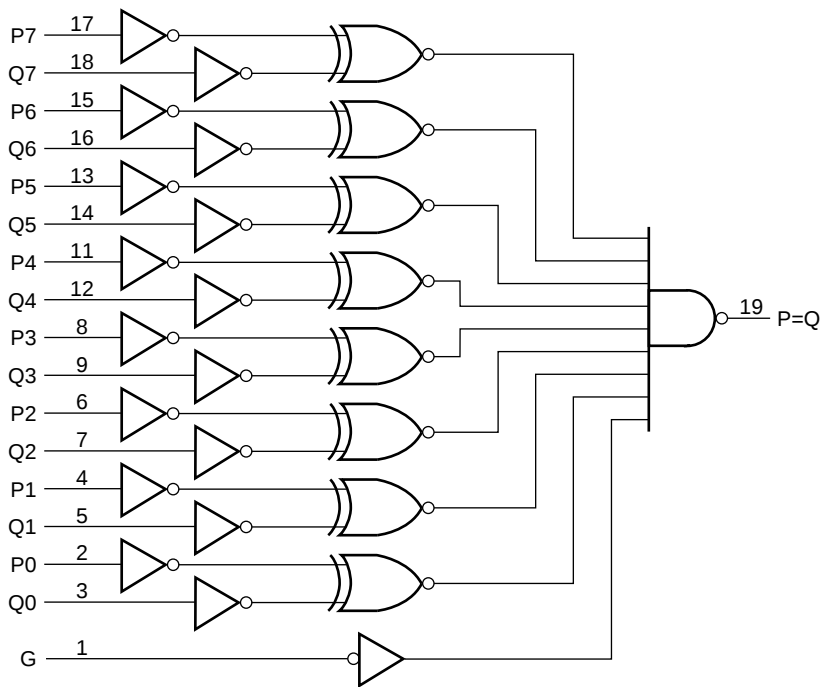
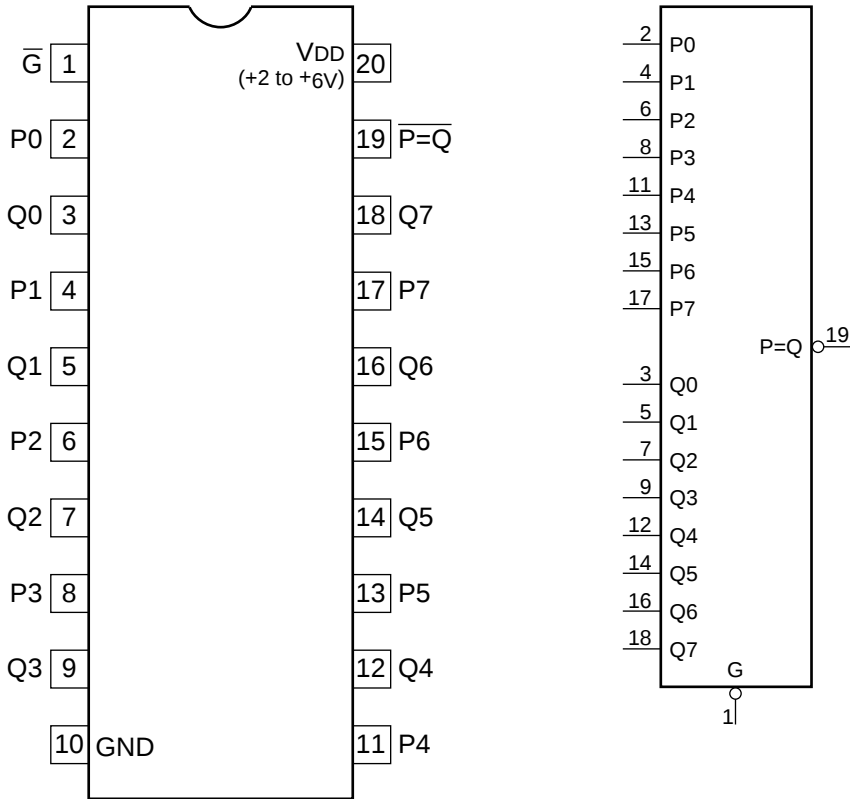


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C-MOS 8-BIT EQUALITY DETECTOR  
-TOP VIEW-



| INPUTS |   | OUTPUTS |
|--------|---|---------|
| P, Q   | G | P=Q     |
| P=Q    | 0 | 0       |
| P>Q    | 0 | 1       |
| P<Q    | 0 | 1       |
| X      | 1 | 1       |

0 ; LOW LEVEL  
1 ; HIGH LEVEL  
X ; DON'T CARE