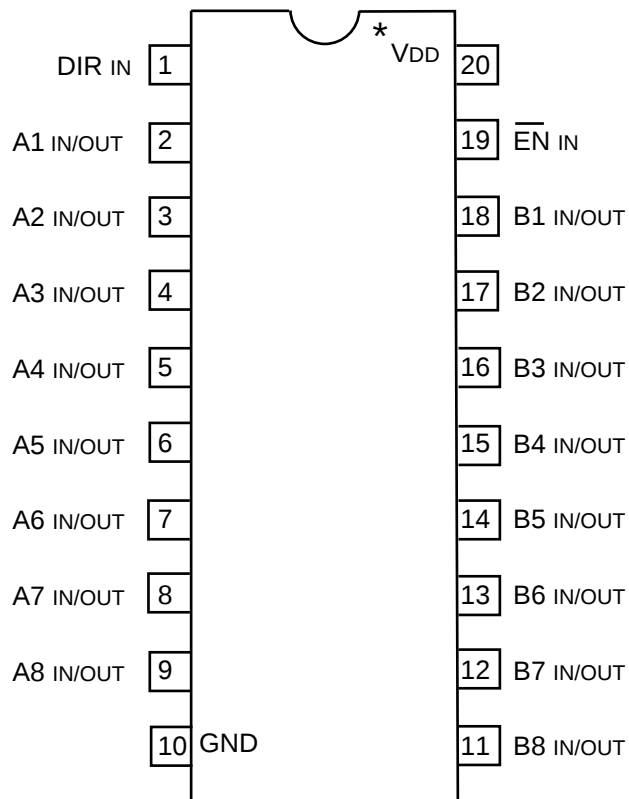

C-MOS 3-STATE INVERTING BUS TRANSCEIVER

- TOP VIEW -



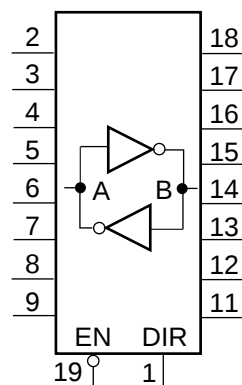
EN	DIR	OPERATION
0	0	B to A
0	0	A to B
1	X	HI-Z

0 ; LOW LEVEL

1 ; HIGH LEVEL

X ; DON'T CARE

HI-Z ; HIGH IMPEDANCE



* VDD HC ; +2 to +6 V

HCT ; +5 V

FCT ; +5 V

