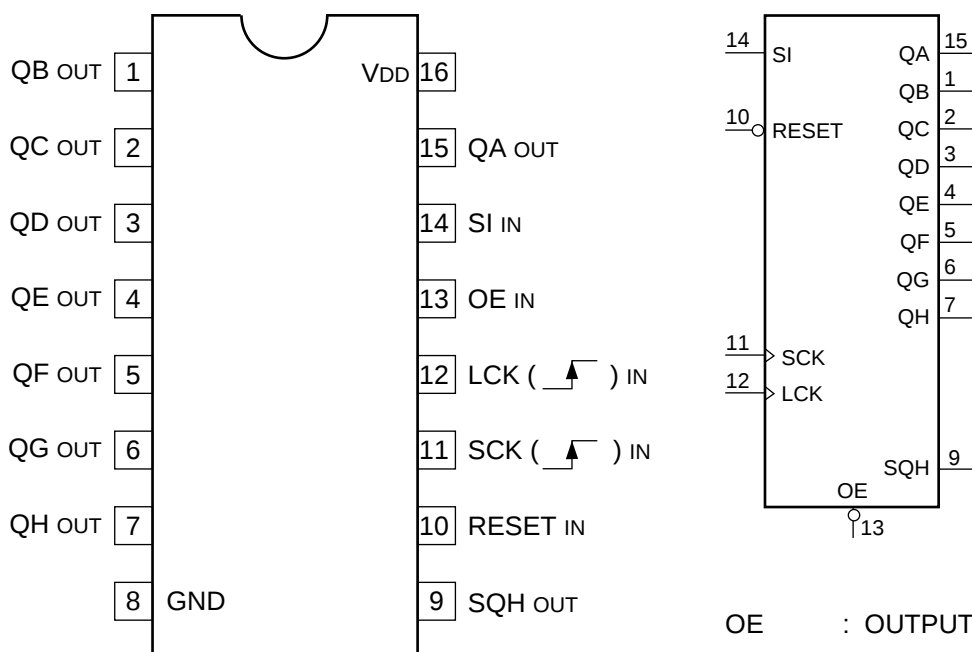


C-MOS 8-BIT SERIAL-INPUT/SERIAL- OR PARALLEL-OUTPUT SHIFT REGISTER WITH LATCHED 3-STATE OUTPUT

—TOP VIEW—



OE : OUTPUT ENABLE INPUT
LCK : LATCH CLOCK INPUT
SCK : SHIFT CLOCK INPUT
RESET : SHIFT-REGISTER RESET INPUT
SI : SERIAL IN
QA - QH : PARALLEL OUTPUT
SQH : SERIAL OUT

