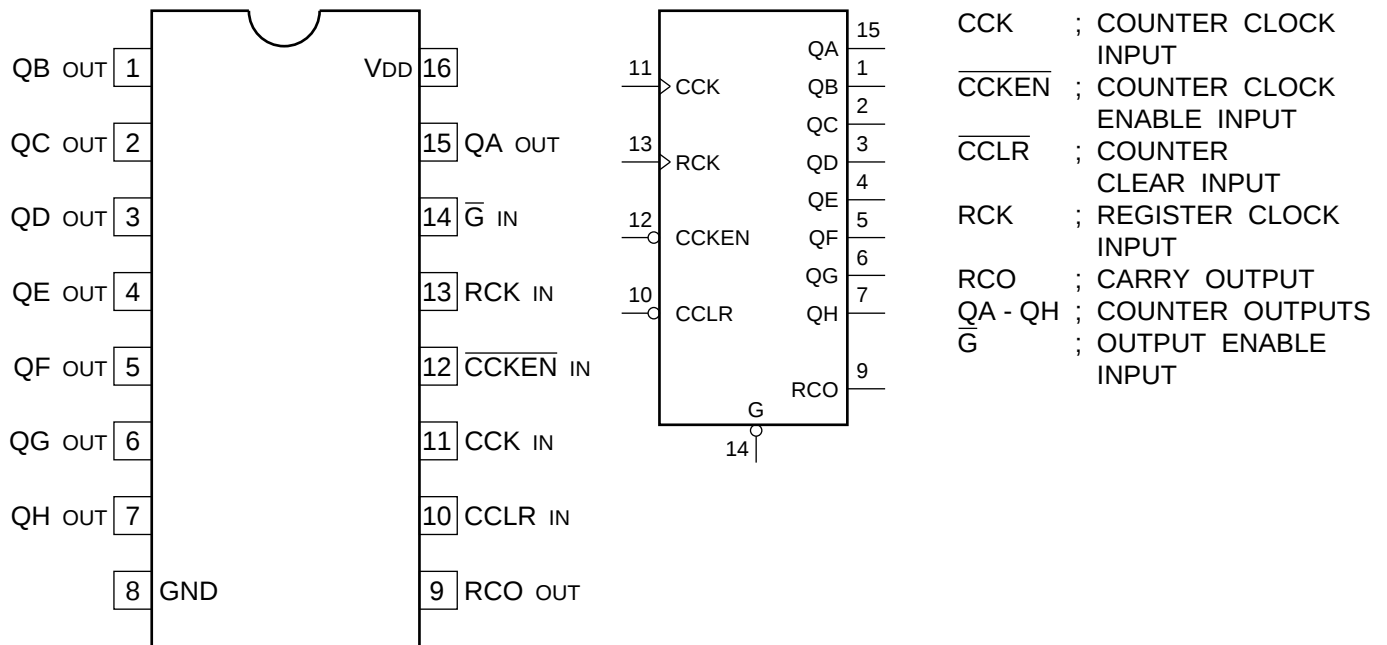


C-MOS 8-BIT BINARY COUNTER / REGISTER WITH 3-STATE OUTPUTS

—TOP VIEW—



INPUTS					FUNCTION
G	RCK	CCLR	CCKEN	CCK	
1	X	X	X	X	OUTPUT DISABLE
0	X	X	X	X	OUTPUT ENABLE
X	$\uparrow$	X	X	X	DATA OF COUNTER ARE STORED WITH REGISTER
X	$\downarrow$	X	X	X	REGISTER STATUS HOLD
X	X	0	X	X	COUNTER CLEAR
X	X	1	0	$\uparrow$	ONE COUNT UP
X	X	1	0	$\downarrow$	NO COUNT
X	X	1	1	X	NO COUNT

