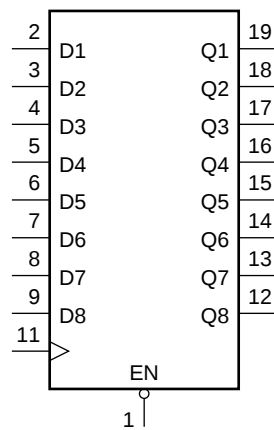
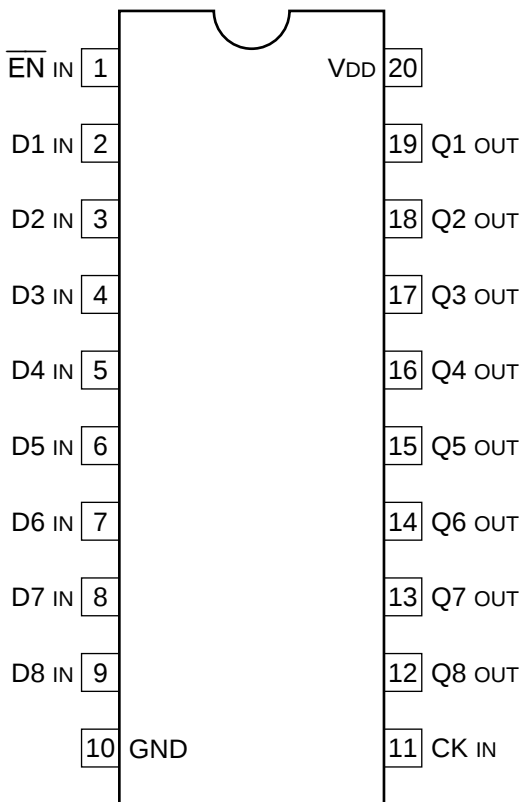


C-MOS 3-STATE D-TYPE EDGE-TRIGGERED FLIP-FLOP

—TOP VIEW—



EACH FLIP-FLOP

INPUTS			OUT
EN	CK	D	Q
0		1	1
0		0	0
0		x	Q ₀
1	x	x	HI-Z

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE

Q₀ : NONSTABLE