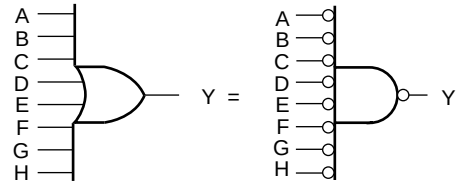
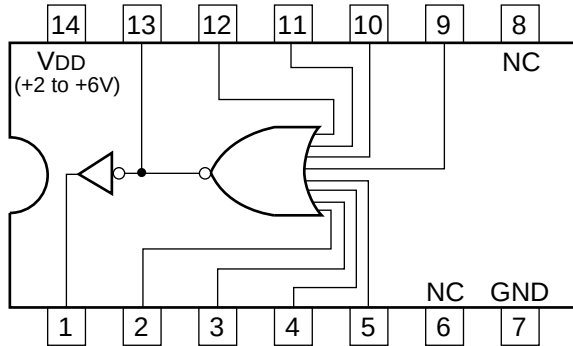
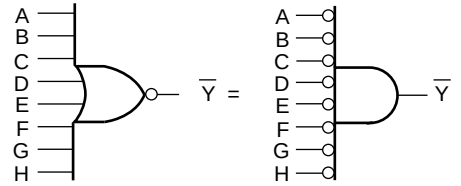

TC74HC4078P
IL00

C-MOS 8-INPUT OR / NOR GATE -TOP VIEW-



$$Y = A + B + C + D + E + F + G + H$$

$$= \overline{\overline{A} \cdot \overline{B} \cdot \overline{C} \cdot \overline{D} \cdot \overline{E} \cdot \overline{F} \cdot \overline{G} \cdot \overline{H}}$$



$$\overline{Y} = \overline{A + B + C + D + E + F + G + H}$$

$$= \overline{A} \cdot \overline{B} \cdot \overline{C} \cdot \overline{D} \cdot \overline{E} \cdot \overline{F} \cdot \overline{G} \cdot \overline{H}$$

A	B	C	D	E	F	G	H	Y	$\overline{Y}$
0	0	0	0	0	0	0	0	0	1
0	0	0	0	0	0	0	0	1	0
0	0	0	0	0	0	1	0	1	0
0	0	0	0	0	0	1	1	1	0
⋮	⋮	⋮	⋮	⋮	⋮	⋮	⋮	1	0
1	1	1	1	1	1	1	0	1	0
1	1	1	1	1	1	1	1	1	0

0 ; LOW LEVEL
1 ; HIGH LEVEL