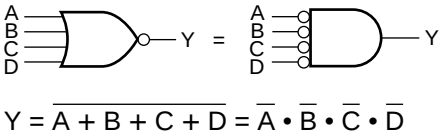
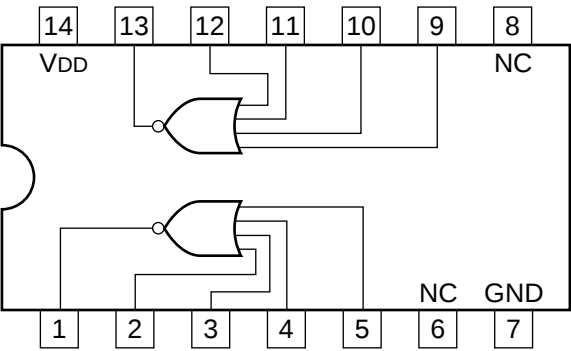


C-MOS 4-INPUT NOR GATE

—TOP VIEW—



A	B	C	D	Y
0	0	0	0	1
x	x	x	1	0
x	x	1	x	0
x	1	x	x	0
1	0	x	x	0

0 : LOW LEVEL
1 : HIGH LEVEL
x : DON'T CARE