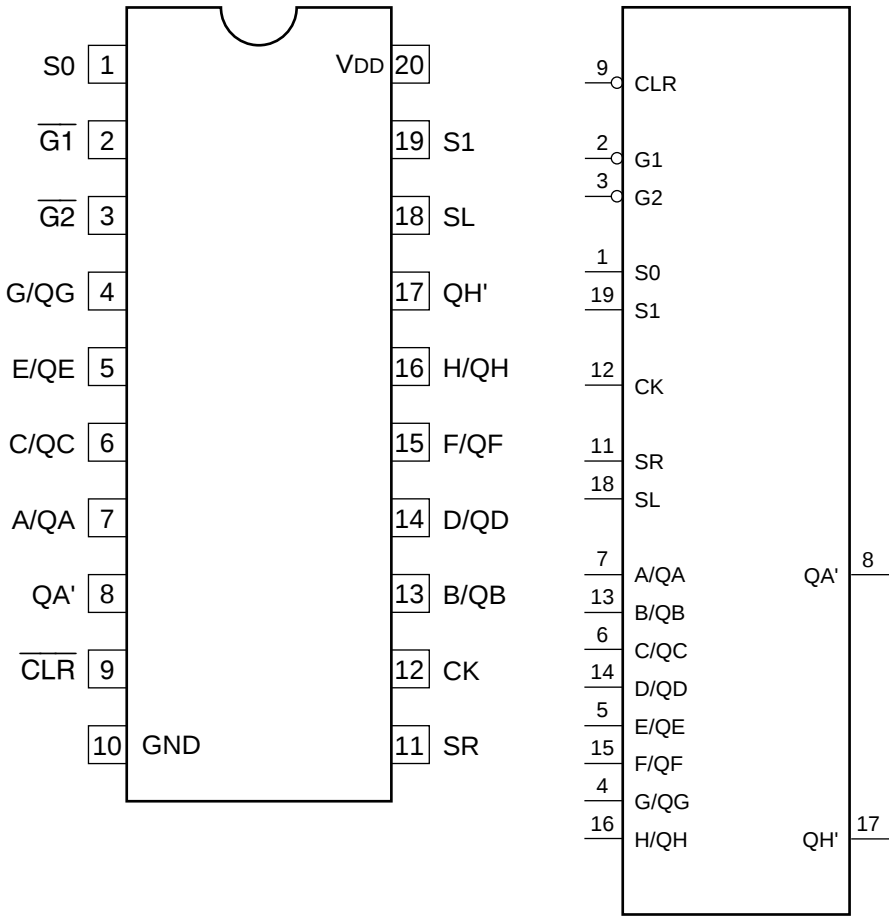


C-MOS 8-BIT PIPO SHIFT REGISTER WITH ASYNCHRONOUS CLEAR
—TOP VIEW—



MODE	INPUTS								INPUTS/OUTPUTS								OUTPUTS	
	CLR	CK	FUNCTION		OUTPUT CONTROL		SHIFT		A/ QA	B/ QB	C/ QC	D/ QD	E/ QE	F/ QF	G/ QG	H/ QH	QA	QH
			S1	S0	G1	G2	SL	SR										
HI-Z	0	X	1	1	X	X	X	X	HI-Z								0	0
CLEAR	0	X	0	X	0	0	X	X	0	0	0	0	0	0	0	0	0	0
	0	X	X	0	0	0	X	X	0	0	0	0	0	0	0	0	0	0
HOLD	1	X	0	0	0	0	X	X	QA0	QB0	QC0	QD0	QE0	QF0	QG0	QH0	QA0	QH0
SHIFT RIGHT	1		0	1	0	0	X	1	1	QAn	QBn	QCn	QDn	QEn	QFn	QGn	1	QGn
	1		0	1	0	0	X	0	0	QAn	QBn	QCn	QDn	QEn	QFn	QGn	0	QGn
SHIFT LEFT	1		1	0	0	0	1	X	QBn	QCn	QDn	QEn	QFn	QGn	QHn	1	QBn	1
	1		1	0	0	0	0	X	QBn	QCn	QDn	QEn	QFn	QGn	QHn	0	QBn	0
LOAD	1		1	1	X	X	X	X	a	b	c	d	e	f	g	h	a	h

a---h = The level of the steady-state input at inputs A through H respectively.

0 ; LOW LEVEL

1 ; HIGH LEVEL

X ; DON'T CARE

HI-Z ; HIGH IMPEDANCE

INPUT/OUTPUT

A/QA - H/QH ; 8 BIT PARALLEL DATA

INPUT

CK ; CLOCK
 CLR ; CLEAR
 G1, G2 ; OUTPUT CONTROL
 S0, S1 ; FUNCTION SELECT
 SL ; SHIFT LEFT
 SR ; SHIFT RIGHT

OUTPUT

QA', QH' ; PARALLEL DATA

