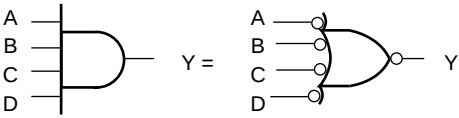
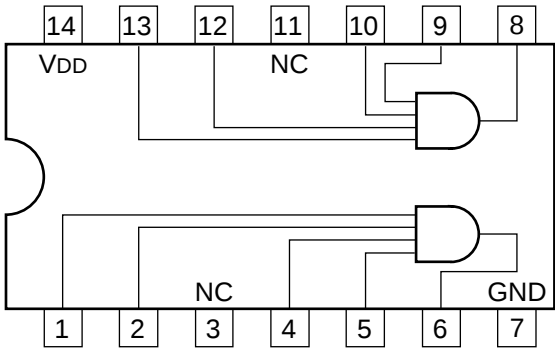


C-MOS DUAL 4-INPUT POSITIVE AND GATE

—TOP VIEW—



$Y = A \cdot B \cdot C \cdot D = \overline{\overline{A} + \overline{B} + \overline{C} + \overline{D}}$

A	B	C	D	Y
0	0	0	0	0
0	0	0	1	0
0	0	1	0	0
0	0	1	1	0
0	1	0	0	0
⋮	⋮	⋮	⋮	⋮
1	1	0	1	0
1	1	1	0	0
1	1	1	1	1

0 : LOW LEVEL
1 : HIGH LEVEL