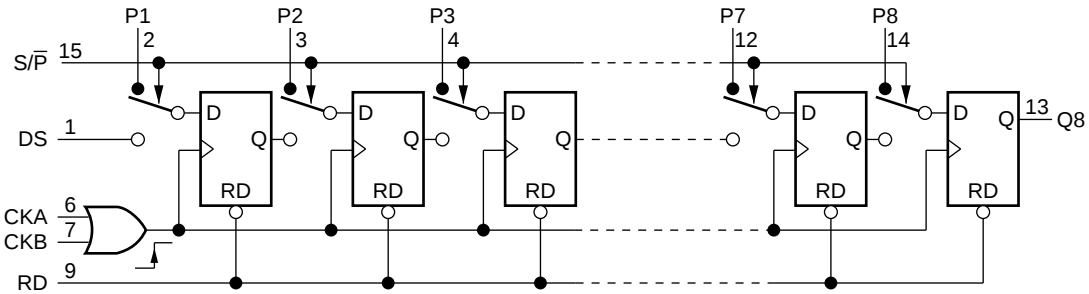
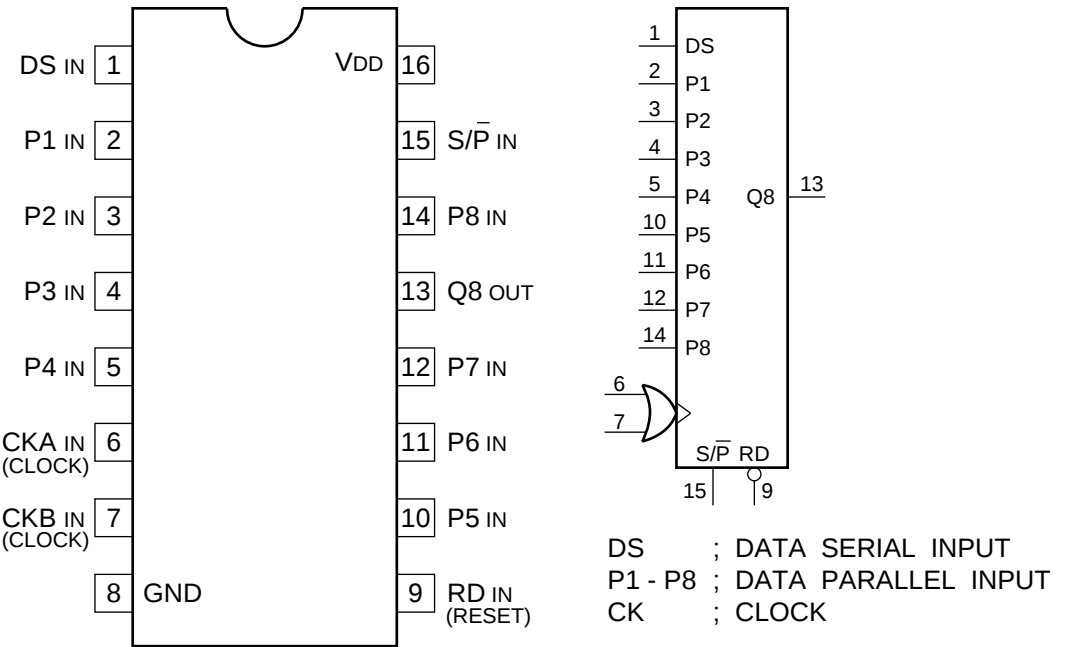


C-MOS 8-BIT SHIFT REGISTER

—TOP VIEW—



CKA	CKB	CK
0	0	0
X	1	1
1	X	1
1		1
	1	1
0		
	0	

INPUT					OUTPUT
RD	S/P	CK	DS	P1-P8	Q8
0	X	X	X	X	0
1	X	0	X	X	Q8o
1	0		X	1-8	8
1	1		1	X	Q7n
1	1		0	X	Q7n
1	X		X	X	A8o

0 ; LOW LEVEL
1 ; HIGH LEVEL
X ; DON'T CARE