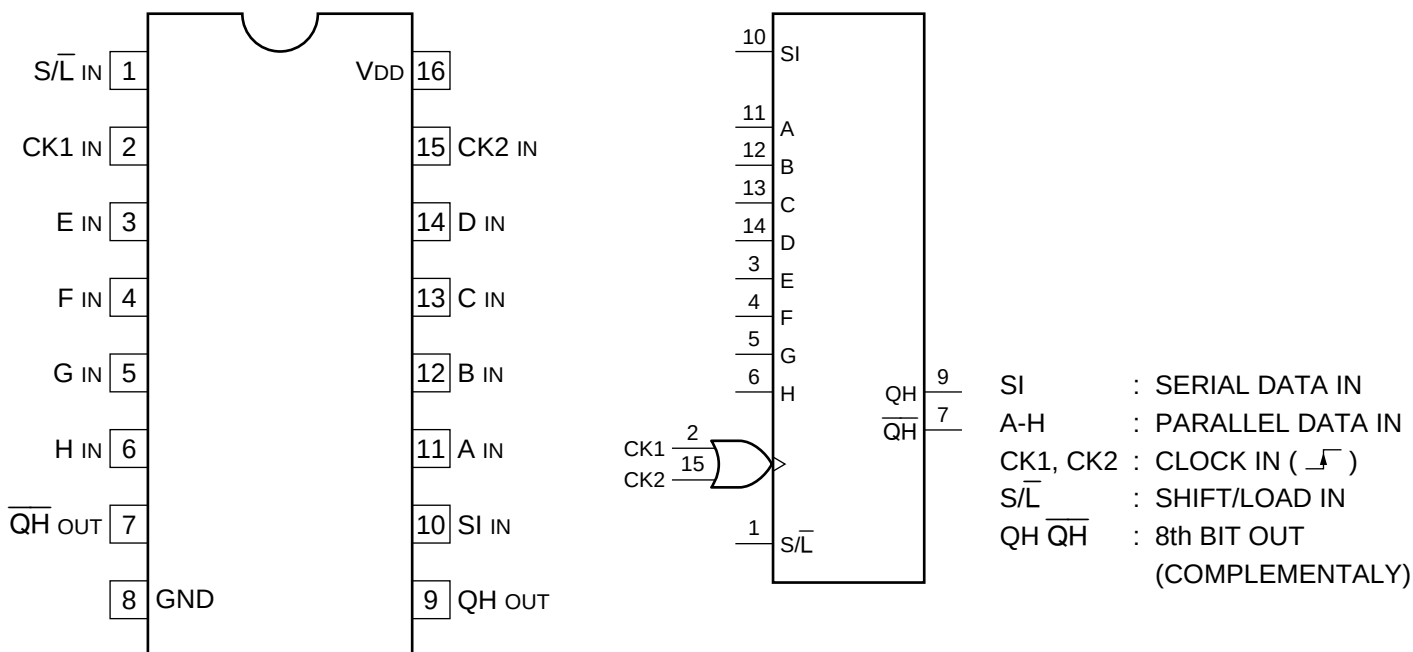


C-MOS SERIAL-OR PARALLEL-INPUT SHIFT REGISTER

—TOP VIEW—



INPUTS				CONTENTS		OUTPUT	OPERATION
S/L	CK1+CK2	SI	A-----H	QA	QB-----	QH	
0	x	x	a-----h	a	b-----	h	PARALLEL LOAD
1	$\downarrow$	0	x	0	QA0---	QG0	RIGHT SHIFT
1	$\downarrow$	1	x	1	QA0---	QG0	
1	$\uparrow$	x	x	QA0	QB0---	QH0	NO COUNT
1	0	x	x	QA0	QB0---	QH0	
1	1	x	x	QA0	QB0---	QH0	

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

a-h : LEVEL OF INPUTS A-H

QA0 - QH0 : LEVEL OF QA-QH BEFORE THE INDICATED INPUT CONDITIONS WERE ESTABLISHED

