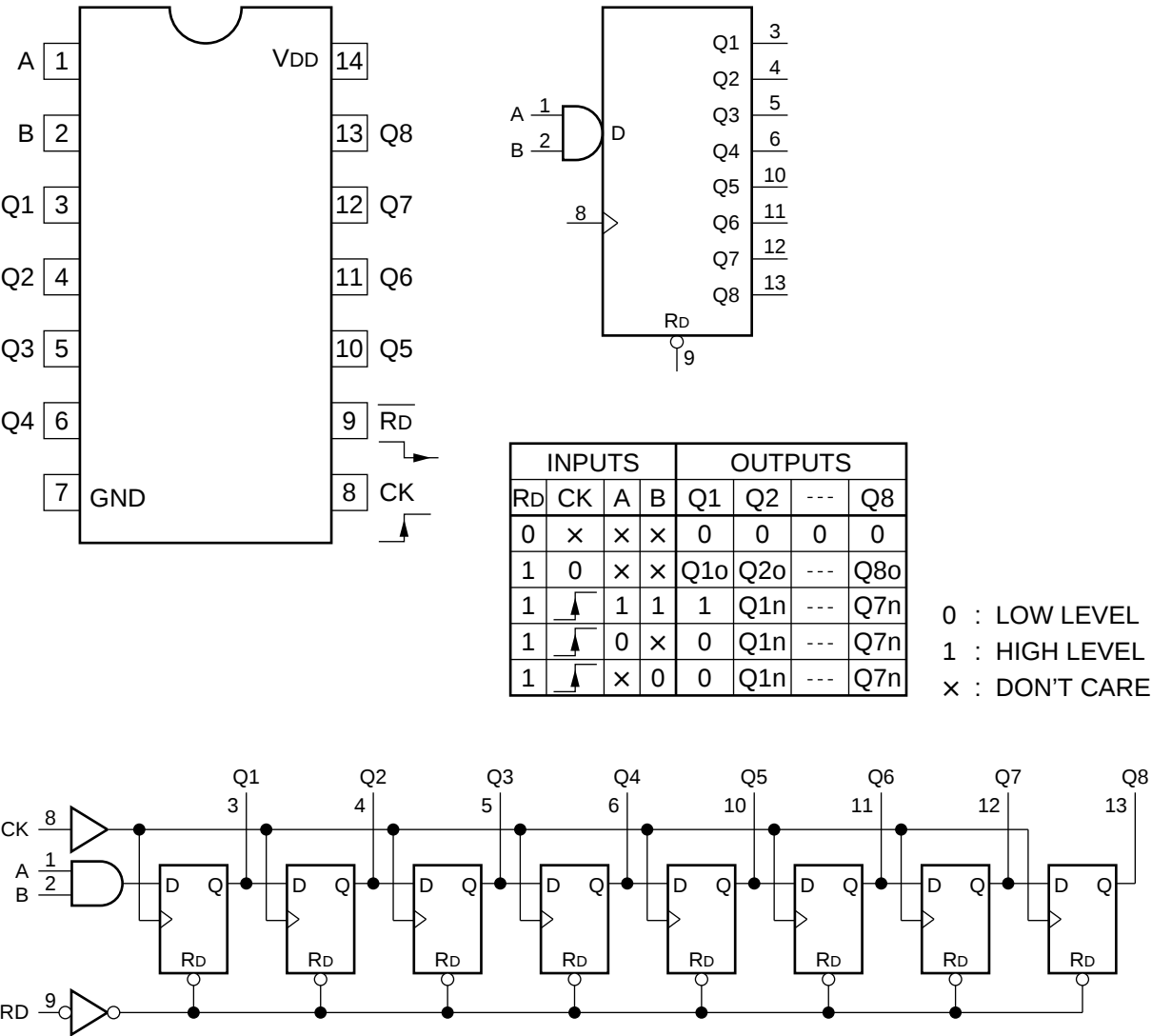


C-MOS 8-BIT SERIAL-IN/PARALLEL-OUT SHIFT REGISTER

—TOP VIEW—



Detailed internal circuit diagram showing the serial chain of flip-flops, clock distribution, and reset logic.