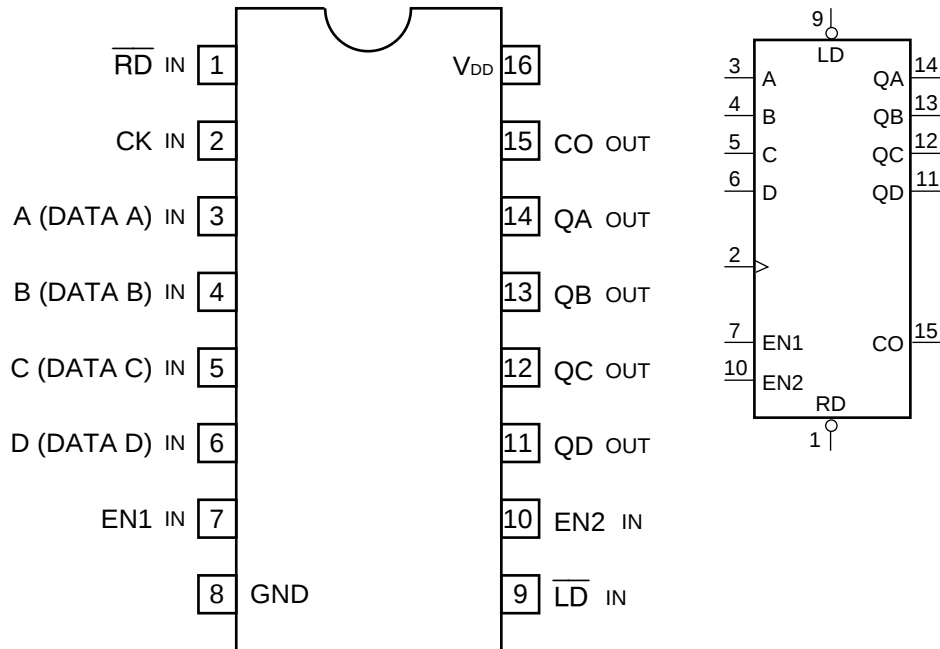


C-MOS PRESETTABLE SYNCHRONOUS 4-BIT BINARY COUNTER

—TOP VIEW—



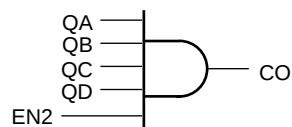
MODE SELECTION

CONTROL INPUTS				MODE
RD	LD	EN1	EN2	
0	x	x	x	RESET (SYNCHRONOUS)
1	0	x	x	PRESET (SYNCHRONOUS)
1	1	0	x	NO COUNT
1	1	x	0	NO COUNT
1	1	1	1	COUNT

COUNT SEQUENCE

COUNT	OUTPUTS			
	QD	QC	QB	QA
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	0	1	0	1
6	0	1	1	0
7	0	1	1	1
8	1	0	0	0
9	1	0	0	1
10	1	0	1	0
11	1	0	1	1
12	1	1	0	0
13	1	1	0	1
14	1	1	1	0
15	1	1	1	1

CARRY OUTPUT "CO"



CO IS HIGH WHEN EN2 INPUT IS HIGH AND COUNT IS "15".