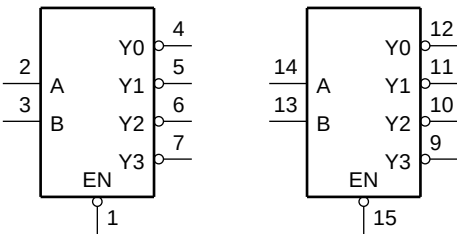
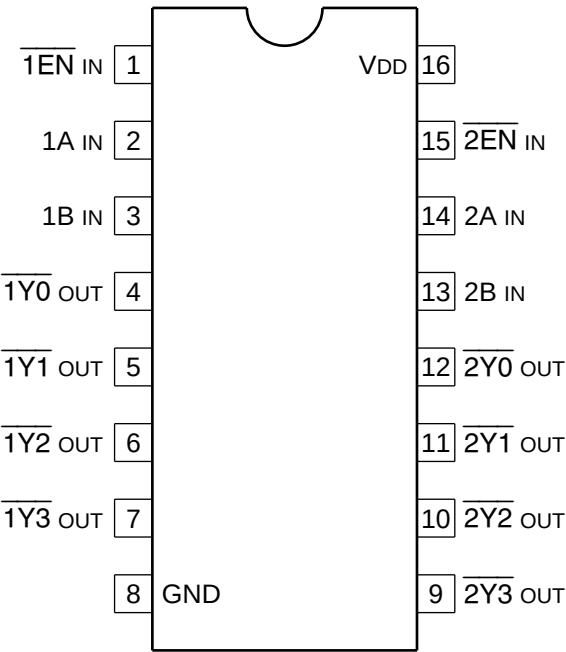


C-MOS DUAL 2-TO-4 DECODER/DEMULTIPLEXER

—TOP VIEW—



INPUTS			OUTPUTS			
EN	B	A	Y3	Y2	Y1	Y0
0	0	0	1	1	1	0
0	0	1	1	1	0	1
0	1	0	1	0	1	1
0	1	1	0	1	1	1
1	x	x	1	1	1	1

0 : LOW LEVEL
1 : HIGH LEVEL
x : DON'T CARE