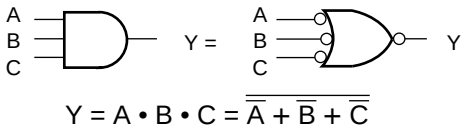
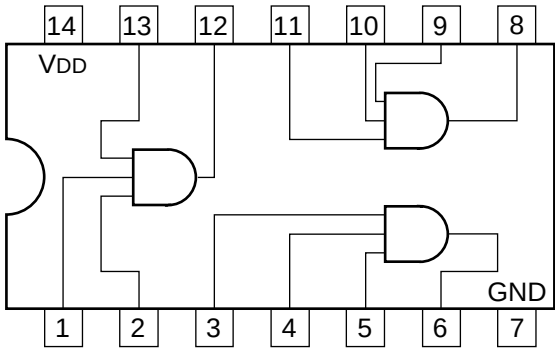


C-MOS 3-INPUT POSITIVE-AND GATE

—TOP VIEW—



A	B	C	Y
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	0
1	0	0	0
1	0	1	0
1	1	0	0
1	1	1	1

0 ; LOW LEVEL
1 ; HIGH LEVEL