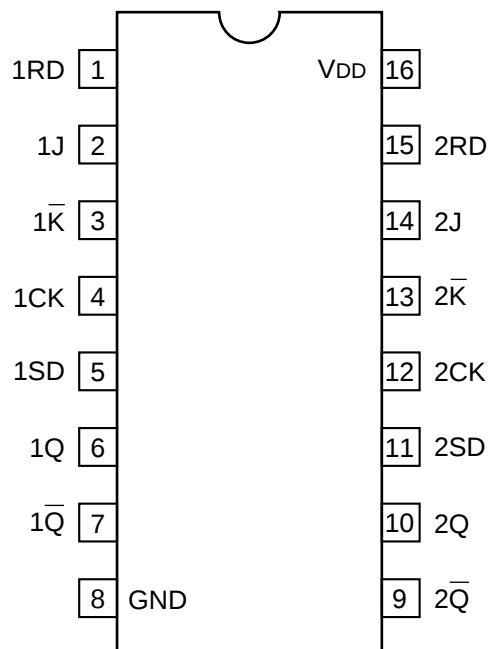


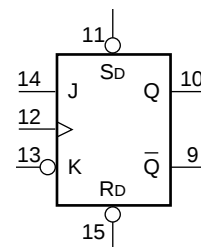
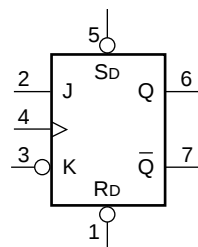
C-MOS J-K FLIP-FLOP WITH DIRECT SET/RESET

—TOP VIEW—



NOTE:

TYPE	V _{DD}
TC74AC/TC74VHC	+2 to +5.5 V
HCT/ACT	+5 V
OTHER TYPES	+2 to +6 V



INPUTS					OUTPUTS	
S _D	R _D	CK	J	K	Q _{n+1}	Q _{n+1}
0	1	x	x	x	1	0
1	0	x	x	x	0	1
0	0	x	x	x	1*	1*
1	1		0	1	Q _n	Q _n
			0	0	0	1
			1	1	1	0
			1	0	Q _n	Q _n
1	1	0	x	x	Q _n	Q _n

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

* : NONSTABLE