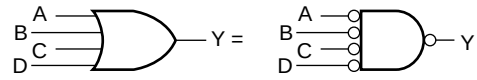
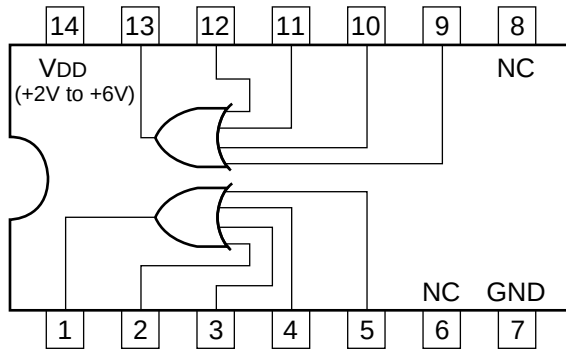

C-MOS 4-INPUT OR GATE

-TOP VIEW-



$$Y = A + B + C + D = \overline{\overline{A} \cdot \overline{B} \cdot \overline{C} \cdot \overline{D}}$$

D	C	B	A	Y
0	0	0	0	0
X	X	X	1	1
X	X	1	X	1
X	1	X	X	1
1	X	X	X	1

0; LOW LEVEL
 1; HIGH LEVEL
 X; DON'T CARE