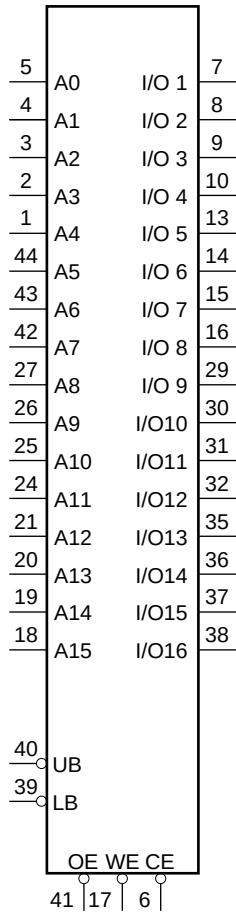
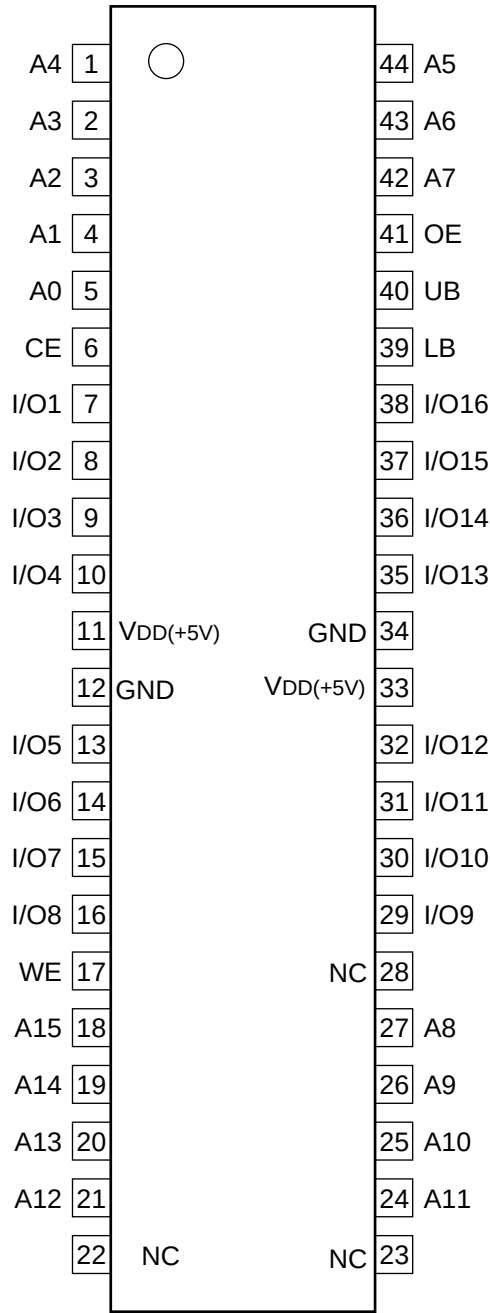

C-MOS 1M(65,536X16)-BIT STATIC RAM
-TOP VIEW-



- A0~A15 : ADDRESS INPUTS
I/O1~I/O16 : DATA INPUTS/OUTPUTS
 $\overline{CE}$: CHIP ENABLE INPUT
 $\overline{WE}$: WRITE ENABLE INPUT
 $\overline{OE}$: OUTPUT BUFFER CONTROL INPUT
 $\overline{LB}, \overline{UB}$: DATA BIT CONTROL INPUT

