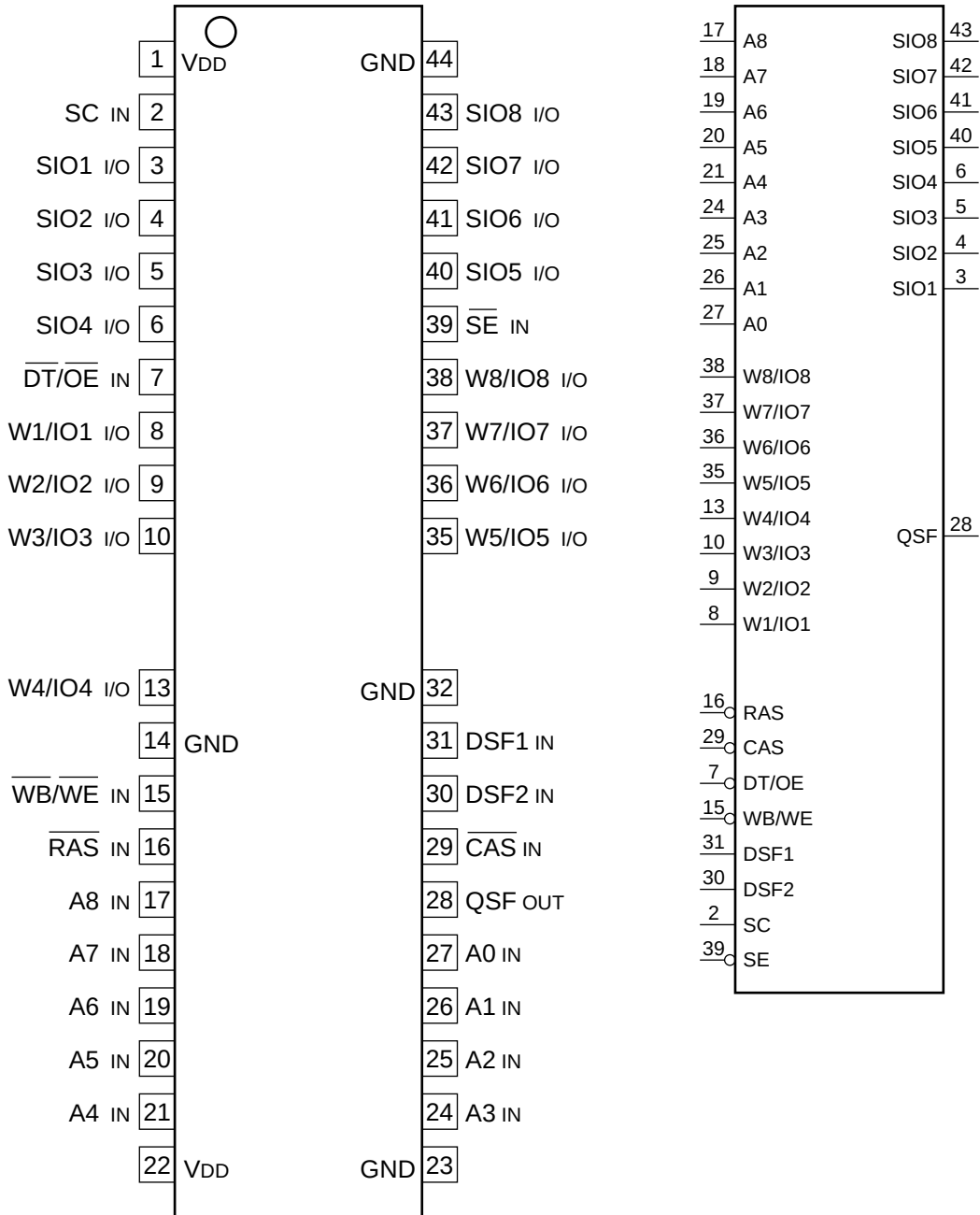


C-MOS 2 M (262,144 × 8)-BIT MULTIPORT DRAM

—TOP VIEW—

**INPUT**

A0 - A8 ; ADDRESS
 CAS ; COLUMN ADDRESS STROBE
 DSF1, DSF2 ; SPECIAL FUNCTION CONTROL
 DT/OE ; DATA TRANSLATE/OUTPUT ENABLE
 RAS ; ROW ADDRESS STROBE
 SC ; SERIAL CLOCK
 SE ; SERIAL ENABLE
 WB/WE ; WRITE PER BIT/WRITE ENABLE

OUTPUT

QSF ; SPECIAL FLAG

INPUT/OUTPUT

SIO1 - SIO8 ; SERIAL DATA
 W1/IO1 - W8/IO8 ; WRITE MASK/DATA I/O

