



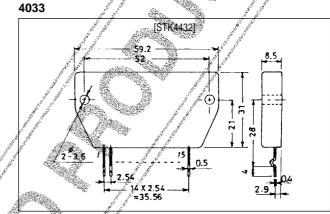
AF Power Amplifier (25W + 25W min, THD = 1.0%)

#### **Features**

- Small and slim package with 31 mm height
- Pin compatible with STK430 series heretofore in use
- Greatly reduced heat sink due to case temperature 125°C guaranteed
- Excellent cost performance

## Package Dimensions

unit: mm



# **Specifications**

## **Maximum Ratings** at Ta = 25°C

Parameter	Symbol Conditions	Ratings	Unit
Maximum supply voltage	V <sub>CC</sub> max	70	V
Operating substrate temperature	TG	125	°C
Storage temperature	// <b></b>	-30 to +125	°C
Available time for load short-circuit	$V_{CC} = 49V$ , $R_L = 8\Omega$ , $f = 50Hz$ , $Po = 25W$	2	S

## Recommended Operating Conditions at Ta = 25°C

Parameter Symbol	Conditions	Ratings	Unit
Recommended operating voltage V <sub>CC</sub>		49	V
Load resistance R <sub>L</sub>		8	Ω

# Operating Characteristics at Ta = 25 °C, $V_{CC}$ = 49V, $R_L$ = 8 $\Omega$ , Rg = 600 $\Omega$ , VG = 40dB

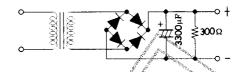
Parameter Quiescent current	Symbol	Conditions	min	typ	max	Unit
	Je Icco	V <sub>CC</sub> = 58V	20	60	120	mA
Output power	Po (1)	THD = 1.0%, f = 1kHz	25			W
Output power	Po (2)	THD = 1.0%, f = 30Hz to 20kHz	13			W
Total harmonic distortion	THD	Po = 0.1W, f = 1kHz			0.3	%
Frequency response	f <sub>L</sub> , f <sub>H</sub>	Po = 0.1W, $^{+0}_{-3}$ dB		20 to 100k		Hz
Input impedance	r <sub>i</sub>	Po = 0.1W, f = 1kHz		110		kΩ
Output noise voltage	V <sub>NO</sub>	$V_{CC}$ = 58V, Rg = 10k $\Omega$			0.8	mVrms

Notes.

For power supply at the time of test, use a constant-voltage power supply unless otherwise specified.

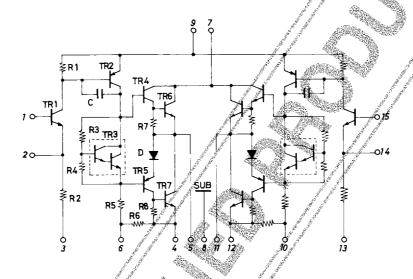
For measurement of the available time for load short-circuit and output noise voltage, use the specified transformer power supply shown right.

The output noise voltage is represented by the peak value on rms scale (VTVM) of average value indicating type. For AC power supply, use an AC stabilized power supply (50Hz) to eliminate the effect of flicker noise in AC primary line.

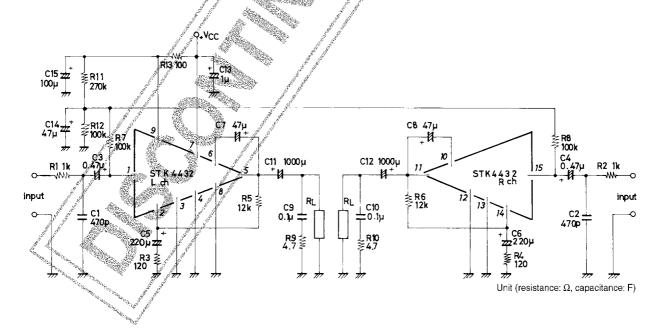


Specified Transformer Power Supply (Equivalent to RP 25)

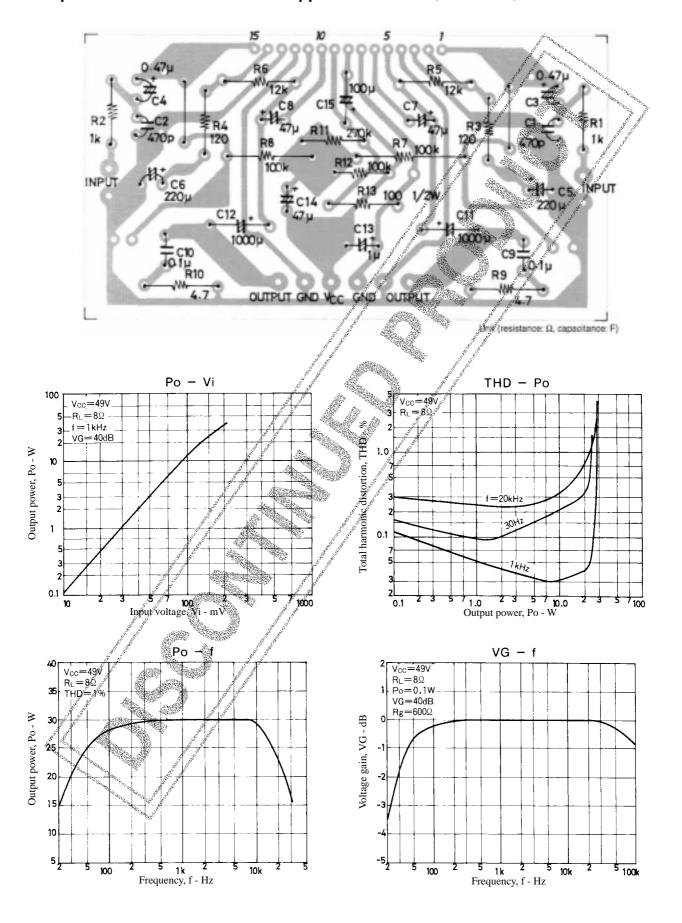
# **Equivalent Circuit**

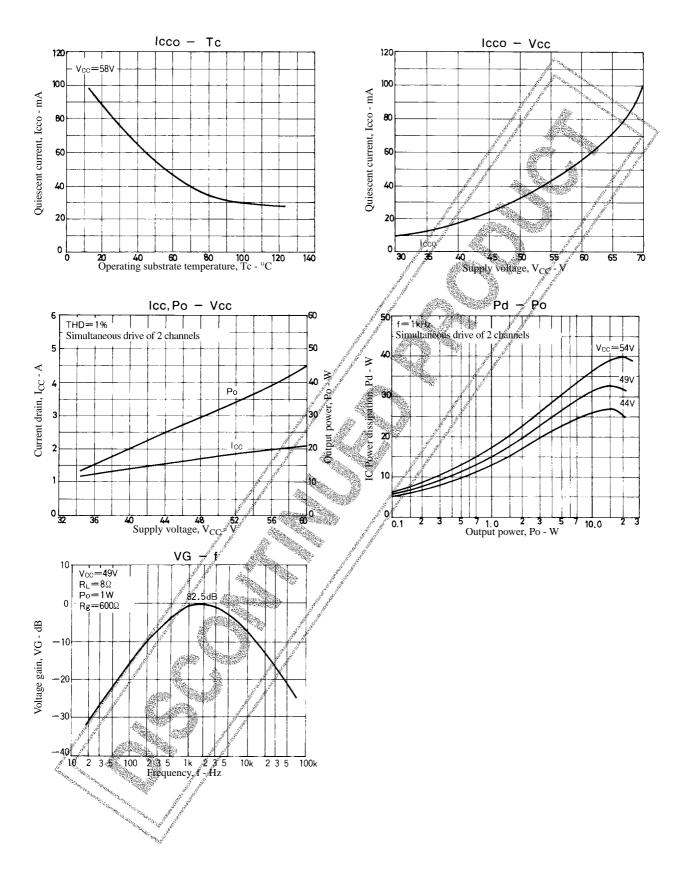


# Sample Application Circuit: 25W min 2-Channel AF Power Amplifier

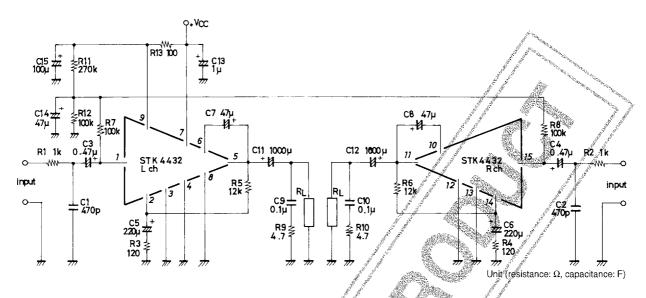


# **Sample Printed Circuit Pattern for Application Circuit** (Cu-foiled side)



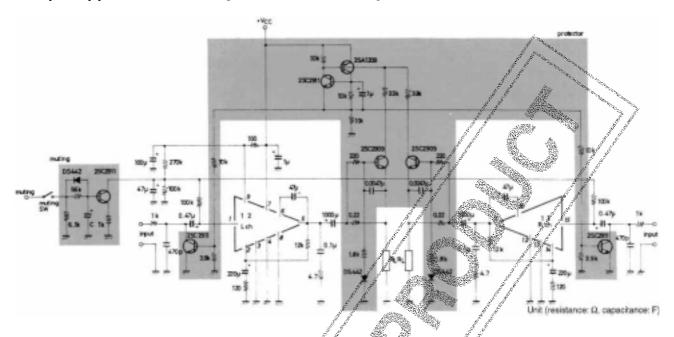


# **Description of External Parts**



C1, C2	Input filter capacitors  • A filter formed with R1 or R2 can be used to reduce noise at high frequencies.
C3, C4	Input coupling capacitors  • Used to block DC current. When the reactance of the capacitor increases at low frequencies, the dependence of 1/f noise on signal source resistance causes the output noise to worsen. It is better to decrease the reactance.
C5, C6	NF capacitors • These capacitors fix the low cutoff frequency shown below. $f_L = \frac{1}{2\pi \cdot C5 \cdot R3} \left[ H^2 \right]$ To provide the desired voltage gain at low frequencies, it is better to increase C5. However, do not increase C5 more than needed because the pop noise level becomes higher at the time of application of power.
C15	Capacitor for ripple filter  • Used to eliminate the ripple components that the input side from the power line (+V <sub>CC</sub> ).
R7, R8	Front stage bias resistors
R11	Front stage bias resistor
C9, C10	Oscillation blocking capacitors  • A polyester film capacitor, being excellent in temperature characteristic, frequency characteristic, is recommended for C9, C10.
R1, R2	Resistors for input filter
R12	Front stage bias resistor
R3, R5 (R4, R6)	These resistors fix voltage gain VG. It is recommended to use R3 (R4) = 120 for VG = 40dB. To adjust VG, it is desirable to change R3 (or R4).
C7, C8	Bootstrap capacitors  • When the capacitor value is decreased, the distortion is liable to be higher at low frequencies.
R/3	Resistor for pipple filter (Limiting resistor for prediver TR at the time of load short)
C11, C12	Output capacitors fix the low cutoff frequency.
C13	Oscillation blocking capacitor  • Must be inserted as close to the IC power supply pins as possible so that the power supply impedance is decreased to operate the IC stably.
C14	Decoupling capacitor  • When the capacitor value is increased, the starting time is made longer.
R9, R10	Oscillation blocking resistors

#### Sample Application Circuit (protection circuit and muting circuit)



#### **Thermal Design**

The IC power dissipation of the STK4432 at the IC operated mode is 30W max. at load resistance  $8\Omega$  (simultaneous drive of 2 channels) for continuous sine wave as shown in Figure 1.

In an actual application where a music signal is used, it is impractical to estimate the power dissipation based on the continuous signal as shown right, because too large a heat sink must be used. It is reasonable to estimate the power dissipation as 1/10 Po max. (EIAJ).

That is, Pd = 21.5W at  $8\Omega$ 

Thermal resistance  $\theta c$ -a of a heat sink for this IC power dissipation (Pd) is fixed under conditions 1 and 2 shown below.

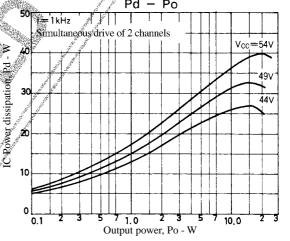


Figure 1. STK4432 Pd – Po ( $R_L = 8\Omega$ )

Assuming that the power dissipation is shared equally among the four power transistors (2 channels  $\times$  2), thermal resistance  $\theta$ j-c is 2.8°C W and

$$Pd \times (\theta c - a + 2.8/4) + Ta \le 150^{\circ}C$$
 (3)

Thermal resistance  $\theta$ c-a of a heat sink must satisfy inequalities (1) and (3).

Figure 2 shows the relation between Pd and  $\theta$ c-a given from (1) and (3) with Ta as a parameter.

[Example] The thermal resistance of a heat sink is obtained when the ambient temperature specified for a stereo amplifier is 50°C.

Assuming  $V_{CC} = 49V$ ,  $R_L = 8\Omega$ ,

 $R_L = 8\Omega$ : Pd = 21.5W at 1/10 Po max.

The thermal resistance of a heat sink is obtained from Figure 2.

 $R_L = 8\Omega : \theta c - a = 3.49$ °C/W

Tj when a heat sink is used is obtained from (3).

 $R_{L} = 8\Omega : T_{j} = 140.1^{\circ}C$ 

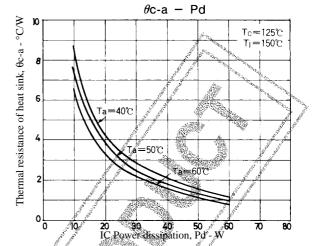


Figure 2 STK4432 9c-a - Pd

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