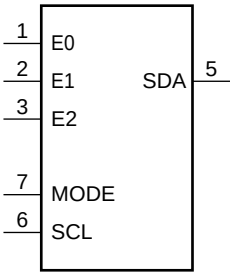
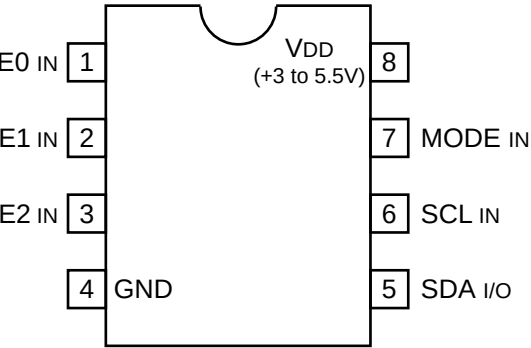

C-MOS SERIAL ACCESS 1K (128·8)-BIT EEPROM
— TOP VIEW —



E0 - E2 ; CHIP ENABLE INPUTS
MODE ; WRITE MODE INPUT
SCL ; SERIAL CLOCK INPUT
SDA ; SERIAL DATA ADDRESS INPUT/OUTPUT