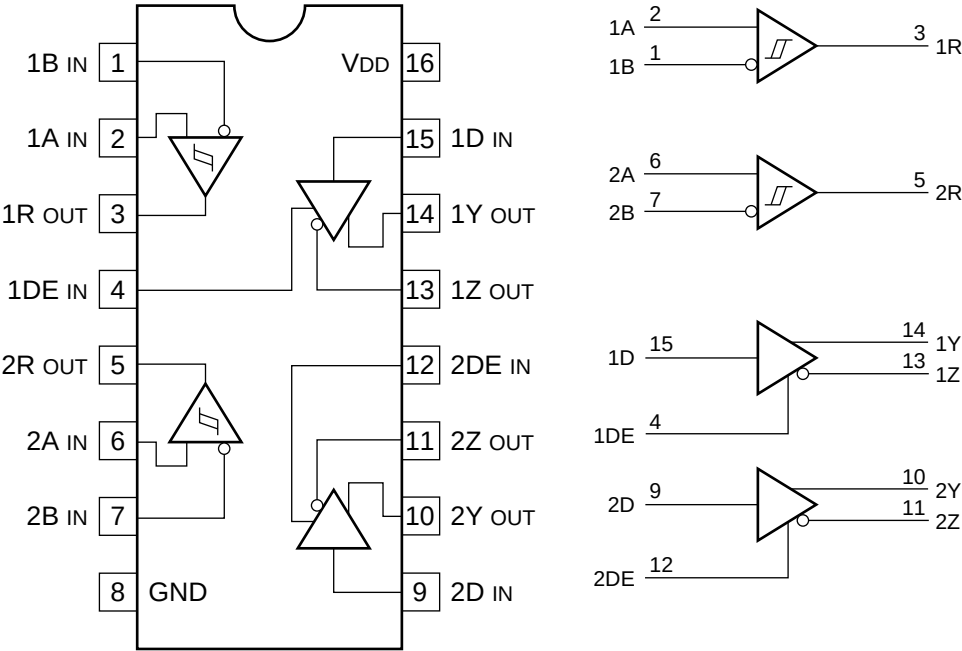


BI-CMOS DUAL DIFFERENTIAL LINE DRIVER/RECEIVER

—TOP VIEW—



DRIVER BLOCK

INPUT D	ENABLE DE	OUTPUTS	
		Y	Z
1	1	1	0
0	1	0	1
x	0	HI-Z	HI-Z

0 : LOW LEVEL
1 : HIGH LEVEL
? : INDETERMINATE
x : DON'T CARE
HI-Z : HIGH IMPEDANCE

RECEIVER BLOCK

DIFFERENTIAL INPUT A - B	OUTPUT R
$V_{ID} \geq 0.2 \text{ V}$	1
$-0.2 \text{ V} < V_{ID} < 0.2 \text{ V}$	?
$V_{ID} \leq -0.2 \text{ V}$	0
OPEN	1