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DUAL DIFFERENTIAL LINE DRIVER/RECEIVER
—TOP VIEW—

DRIVER BLOCK

INPUT	ENABLE	OUTPUTS	
D	DE	Y	Z
1	1	1	0
0	1	0	1
X	0	HI-Z	HI-Z

RECEIVER BLOCK

DIFFERENTIAL INPUT	ENABLE	OUTPUT
A-B	RE	R
$V_{ID} \geqq 0.2$ V	0	1
-0.2 V $< V_{ID} < 0.2$ V	0	×
$V_{ID} \leqq -0.2$ V	0	0
0 OR 1	1	HI-Z

0   ; LOW LEVEL
1   ; HIGH LEVEL
×   ; DON'T CARE
HI-Z ; HIGH IMPEDANCE
V_{ID} ; XA-XB
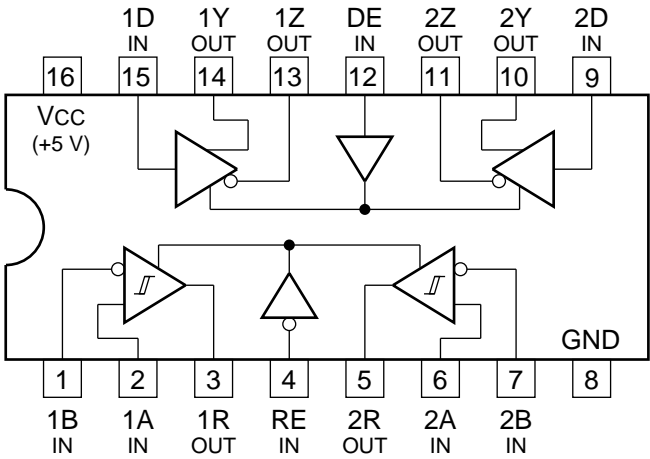
INPUT
1A, 1B, 2A, 2B ; RECEIVER
1D, 2D   ; DRIVER
DE   ; DRIVER OUTPUT CONTROL
RE   ; RECEIVER OUTPUT CONTROL

OUTPUT
1R, 2R   ; RECEIVER
1Y, 2Y, 1Z, 2Z ; DRIVER