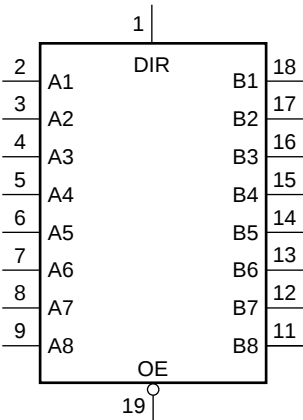
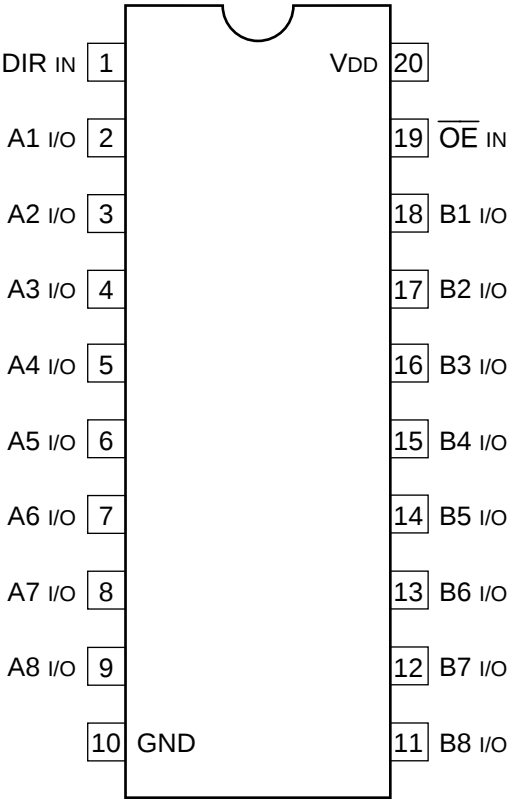


BIC-MOS BUS TRANSCEIVERS WITH 3-STATE OUTPUTS

—TOP VIEW—



INPUTS		OPERATION
OE	DIR	
0	0	B DATA TO A BUS
0	1	A DATA TO B BUS
1	x	ISOLATION

0 : LOW LEVEL
1 : HIGH LEVEL
x : DON'T CARE

INPUT
DIR : DIRECTION CONTROL
OE : OUTPUT ENABLE

INPUT/OUTPUT
A1 - A8 : DATA
B1 - B8 : DATA

