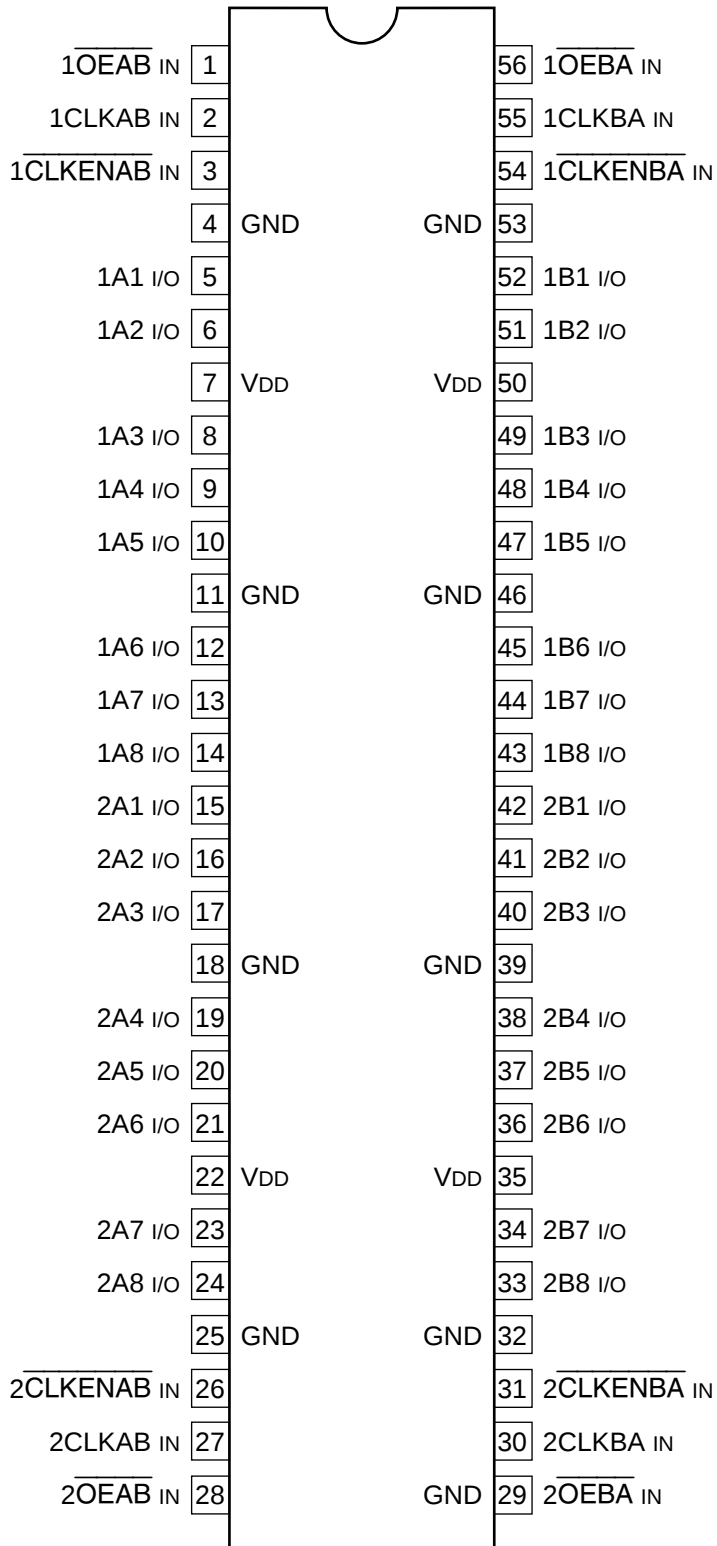


## C-MOS 16-BIT REGISTERED TRANSCEIVERS WITH 3-STATE OUTPUTS

—TOP VIEW—



| INPUTS  |       |      |   | OUTPUT |
|---------|-------|------|---|--------|
| CLKENAB | CLKAB | OEAB | A | B      |
| 1       | x     | 0    | x | B0     |
| x       | 0     | 0    | x | B0     |
| 0       |       | 0    | 0 | 0      |
| 0       |       | 0    | 1 | 1      |
| x       | x     | 1    | x | HI-Z   |

A-to-B data flow is shown; B-to-A data flow is similar but uses  $\overline{\text{CLKENBA}}$ ,  $\text{CLKBA}$  and  $\overline{\text{OEBA}}$

0 : LOW LEVEL  
 1 : HIGH LEVEL  
 x : DON'T CARE  
 HI-Z : HIGH IMPEDANCE

**INPUT**

$\overline{1\text{OEAB}}$ ,  $\overline{1\text{OEBA}}$ , : OUTPUT ENABLE

$\overline{2\text{OEAB}}$ ,  $\overline{2\text{OEBA}}$

$\overline{1\text{CLKENAB}}$ ,  $\overline{1\text{CLKENBA}}$ , : CLOCK ENABLE

$\overline{2\text{CLKENAB}}$ ,  $\overline{2\text{CLKENBA}}$

$\overline{1\text{CLAB}}$ ,  $\overline{1\text{CLBA}}$ , : CLOCK

$\overline{2\text{CLAB}}$ ,  $\overline{2\text{CLBA}}$

**INPUT/OUTPUT**

1A1 - 1A8, 2A1 - 2A8 : A BUS DATA

1B1 - 1B8, 2B1 - 2B8 : B BUS DATA

