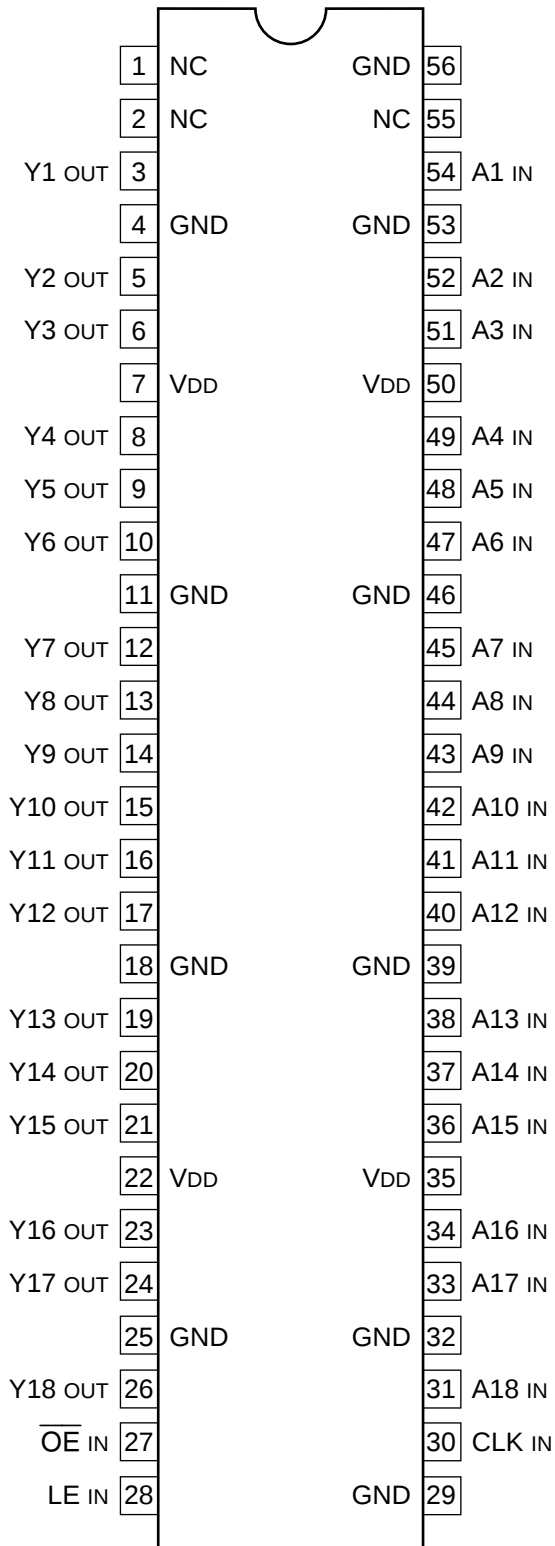


C-MOS 18-BIT UNIVERSAL BUS DRIVER WITH 3-STATE OUTPUTS

—TOP VIEW—



INPUTS				OUTPUT
OE	LE	CLK	A	Y
1	x	x	x	HI-Z
0	1	x	0	0
0	1	x	1	1
0	0	↑	0	0
0	0	↑	1	1
0	0	1	x	Y0
0	0	0	x	Y0

0 : LOW LEVEL

1 : HIGH LEVEL

x : DON'T CARE

HI-Z : HIGH IMPEDANCE

INPUT

A1 - A18 : DATA

CLK : CLOCK

LE : LATCH ENABLE

OE : OUTPUT ENABLE

OUTPUT

Y1 - Y18 : DATA

