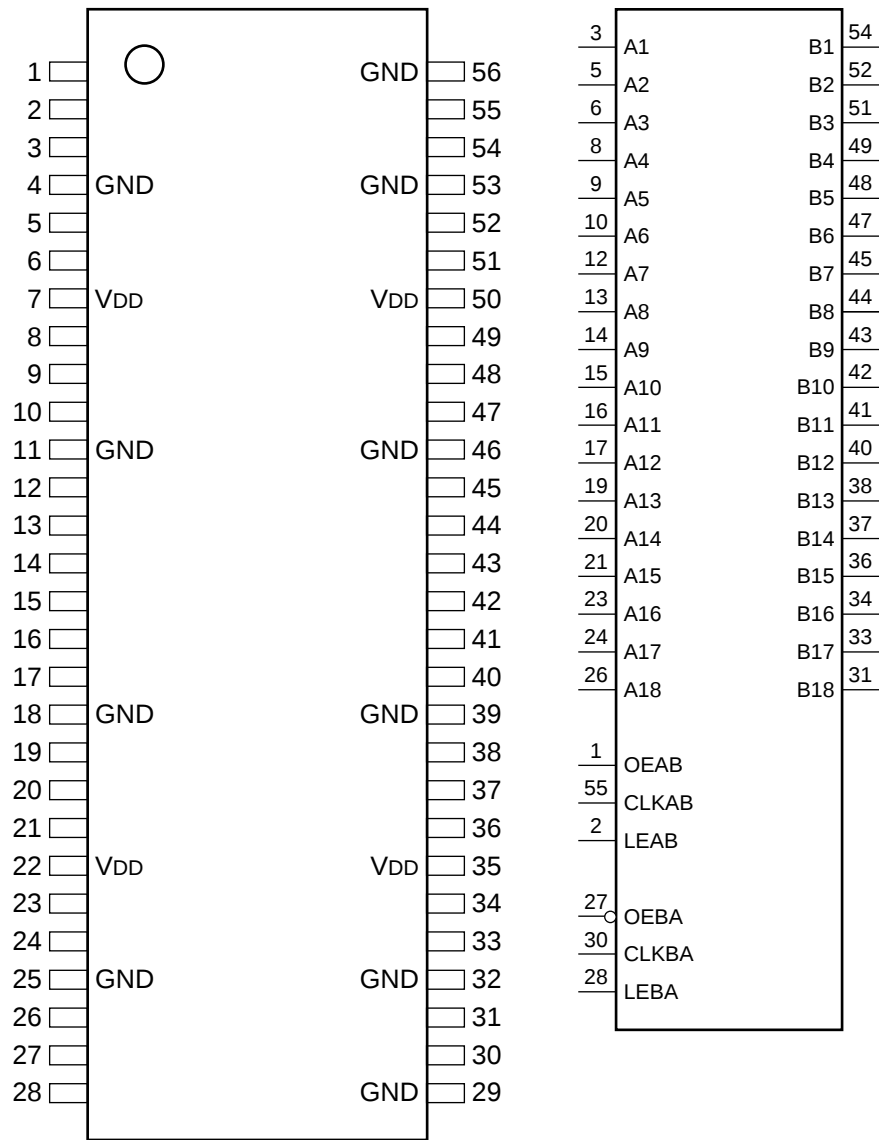


C-MOS 3-STATE 18-BIT UNIVERSAL BUS TRANSCEIVERS

—TOP VIEW—

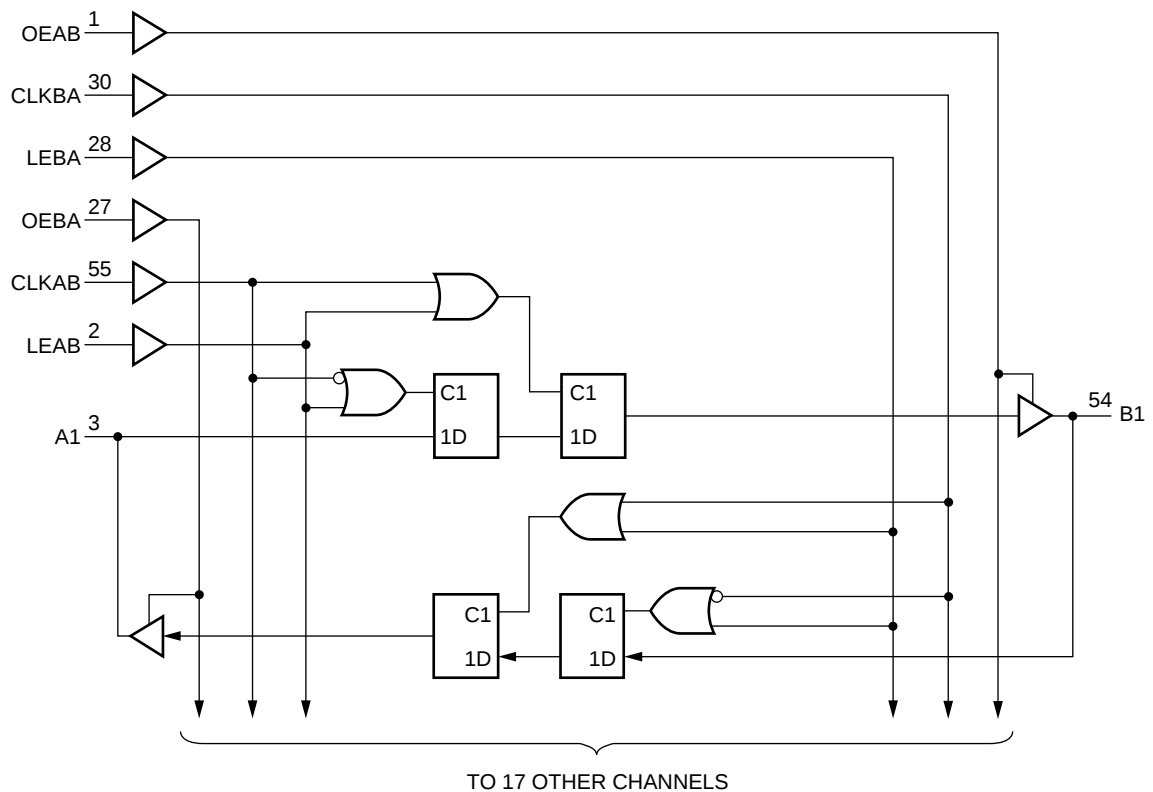


INPUT

CLKAB : CLOCK (A-TO-B DATA FLOW)
 CLKBA : CLOCK (B-TO-A DATA FLOW)
 LEAB : LATCH ENABLE (A-TO-B DATA FLOW)
 LEBA : LATCH ENABLE (B-TO-A DATA FLOW)
 OEAB : OUTPUT ENABLE (A-TO-B DATA FLOW)
 OEBA : OUTPUT ENABLE (B-TO-A DATA FLOW)

INPUT/OUTPUT

A1 - A18 : A-BUS DATA
 B1 - B18 : B-BUS DATA



INPUTS				OUTPUT B
OEAB	LEAB	CLKAB	A	
0	x	x	x	HI-Z
1	1	x	0	0
1	1	x	1	1
1	0	↑	0	0
1	0	↑	1	1
1	0	1	x	B ₀
1	0	0	x	B ₀

0 : LOW LEVEL
 1 : HIGH LEVEL
 x : DON'T CARE
 HI-Z : HIGH IMPEDANCE