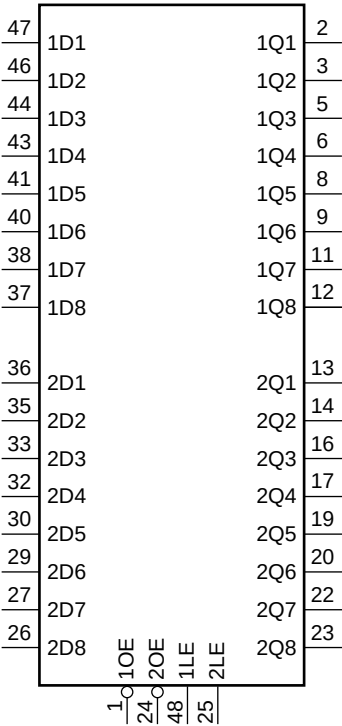
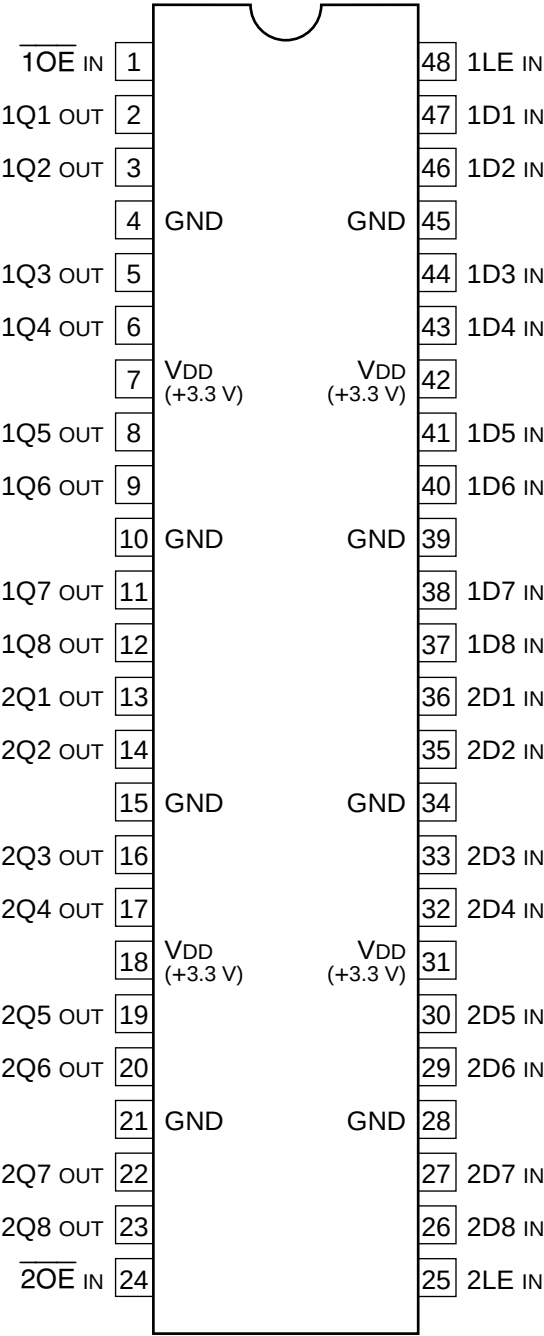


C-MOS 16-BIT TRANSPARENT D-TYPE LATCHES

—TOP VIEW—



PIN DESCRIPTION

PIN NAMES	DESCRIPTION
xDx	DATA INPUTS
xLE	LATCH ENABLE INPUTS (ACTIVE HIGH)
xOE	OUTPUT ENABLE INPUTS (ACTIVE LOW)
xQx	3-STATE OUTPUTS

FUNCTION TABLE (*1)

INPUTS			OUTPUTS
xDx	xLE	xOE	xQx
1	1	0	1
0	1	0	0
x	0	0	Q0
x	x	x	HI-Z

NOTE :

(*1) 0 : LOW LEVEL
1 : HIGH LEVEL
x : DON'T CARE
HI-Z : HIGH IMPEDANCE

SN74LVT16373DRLR (2/2)

