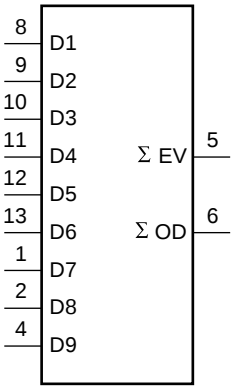
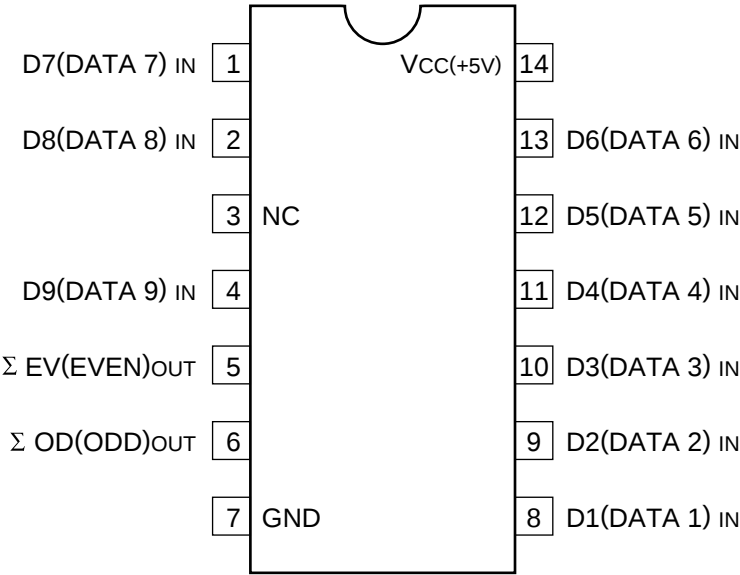

SN74LS280N
IL00

TTL 9-BIT ODD / EVEN PARITY GENERATOR / CHECKER
-TOP VIEW-



SUM OF H'S AT D1 THRU D9	OUTPUTS	
	Σ EV	Σ OD
EVEN(0, 2, 4, 6, 8)	1	0
ODD(1, 3, 5, 7, 9)	0	1

0 ; LOW LEVEL
1 ; HIGH LEVEL