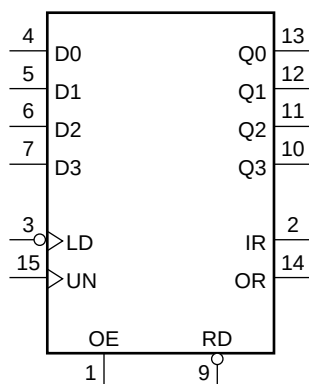
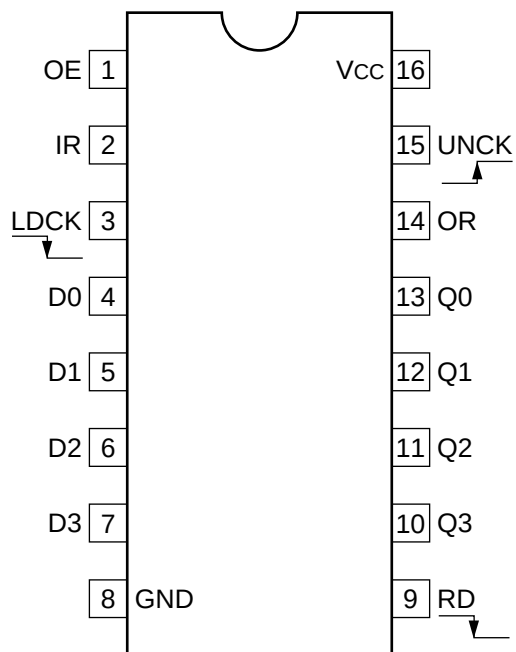


TTL (16 WORD × 4)-BIT ASYNCHRONOUS FIRST-IN FIRST-OUT MEMORY

—TOP VIEW—

**INPUT**

D0 - D3 : DATA
 LDCK : LOAD CLOCK ()
 OE : OUTPUT ENABLE
 RD : CLEAR
 UNCK : UNLOAD CLOCK ()

OUTPUT

IR : INPUT READY
 OR : OUTPUT READY
 Q0 - Q3 : DATA (3-STATE)

