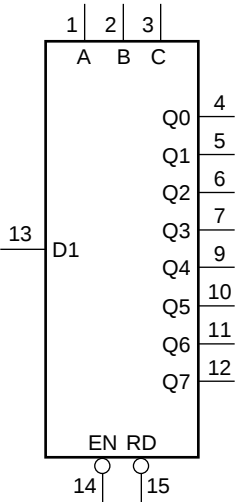
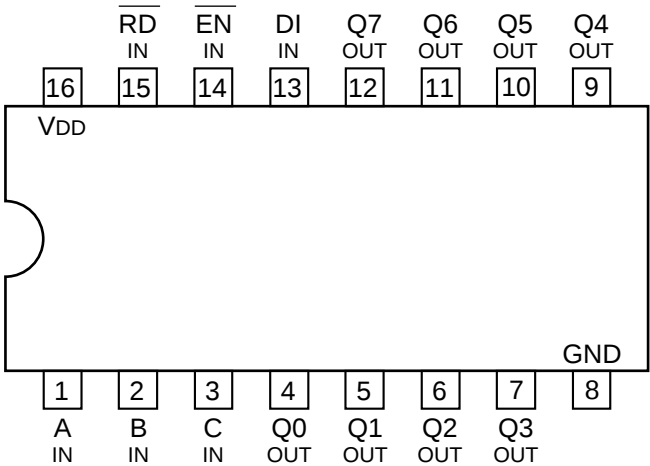

C-MOS 8-BIT ADDRESSABLE LATCHES

—TOP VIEW—



MODE SELECTION

CONTROL INPUTS		OUTPUT OF ADDRESSED LATCH	OUTPUT OF EACH OTHER LATCH
Rd	EN		
1	0	D1	Qno
1	1	Qno	Qno
0	0	D1	0
0	1	0	0

0 ; LOW LEVEL

1 ; HIGH LEVEL

NOTE:

TYPE	VDD
TC40H	+2V to +8V
OTHERS	+2V to +6V

LATCH SELECTION

CONTROL INPUTS			LATCH ADDRESSED
C	B	A	
0	0	0	0
0	0	1	1
0	1	0	2
0	1	1	3
1	0	0	4
1	0	1	5
1	1	0	6
1	1	1	7