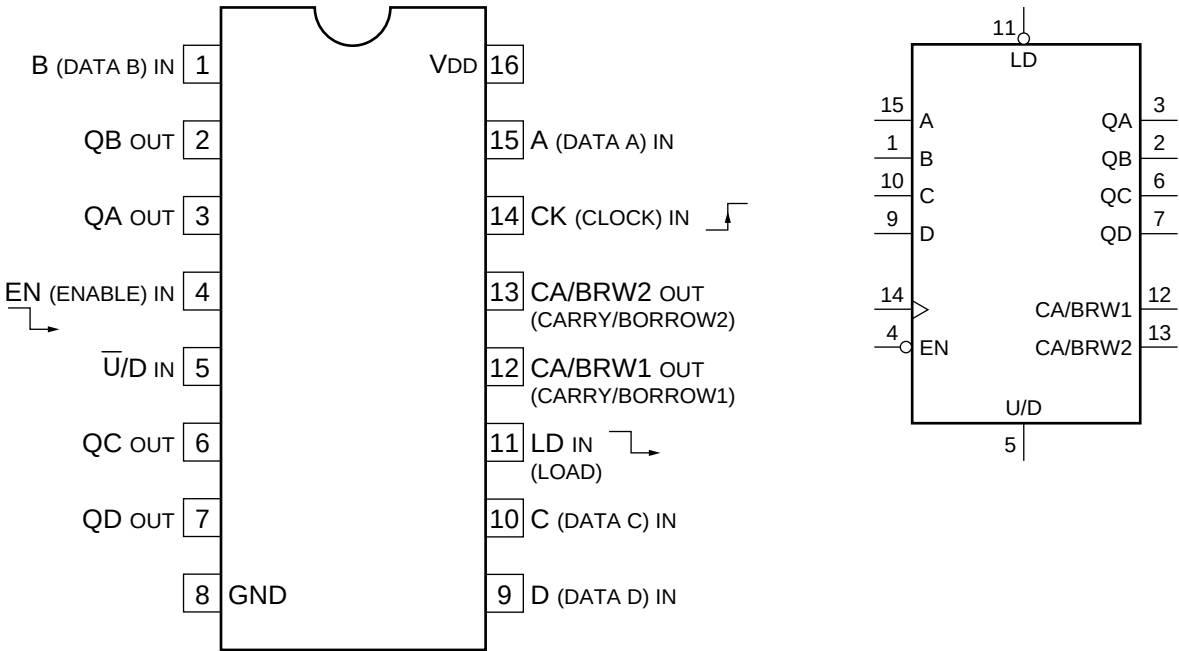


C-MOS PRESETTABLE SYNCHRONOUS 4-BIT BINARY UP/DOWN COUNTER

—TOP VIEW—

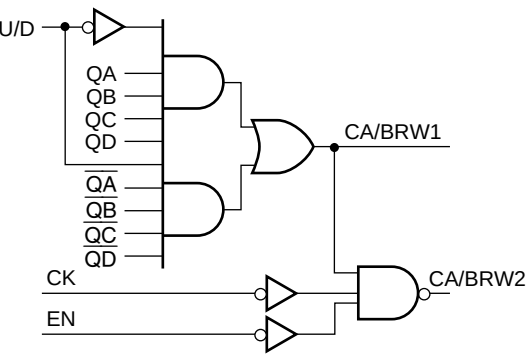


MODE SELECTION

CONTROL INPUTS			MODE
LD	EN	$\overline{\text{U/D}}$	
0	X	X	PRESET (ASYNCHRONOUS)
1	1	X	NO COUNT
1	0	0	UP COUNT
1	0	1	DOWN COUNT

0 ; LOW LEVEL
1 ; HIGH LEVEL
X ; DON'T CARE

CA/BRW OUTPUTS



COUNT SEQUENCE

COUNT	OUTPUTS			
	QD	QC	QB	QA
0	0	0	0	0
1	0	0	0	1
2	0	0	1	0
3	0	0	1	1
4	0	1	0	0
5	0	1	0	1
6	0	1	1	0
7	0	1	1	1
8	1	0	0	0
9	1	0	0	1
10	1	0	1	0
11	1	0	1	1
12	1	1	0	0
13	1	1	0	1
14	1	1	1	0
15	1	1	1	1

UP COUNT
DOWN COUNT

CA/BRW1 OUTPUT IS HIGH WHEN COUNT IS “15” AT UP-COUNT OR WHEN COUNT IS “0” AT DOWN COUNT.

CA/BRW2 OUTPUT IS LOW WHEN BOTH THE CLOCK AND EN INPUTS ARE LOW AND CA/BRW1 OUTPUT IS HIGH.