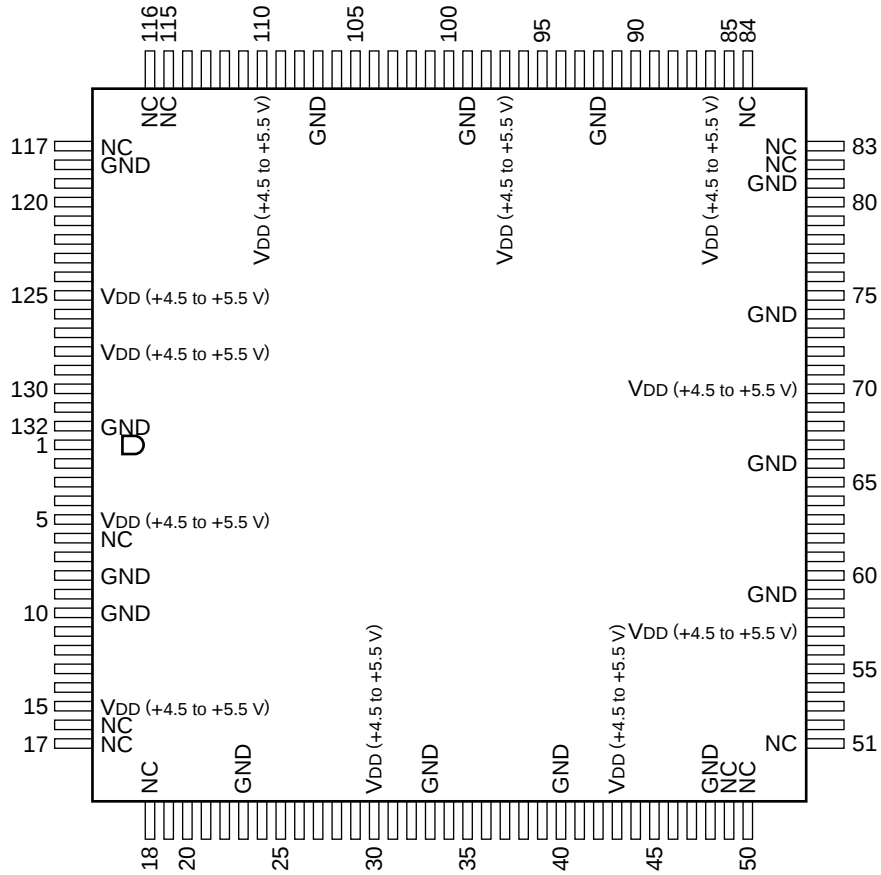


C-MOS 512 × 36 CLOCK FIRST-IN/FIRST-OUT MEMORY

—TOP VIEW—



(V_{DD} = +4.5 to +5.5 V)

PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL	PIN NO.	I/O	SIGNAL
1	I	FS0/SD	34	I/O	B23	67	I/O	A0	100	I/O	A23
2	I	FS1/ $\overline{\text{SEN}}$	35	I/O	B22	68	I/O	A1	101	I/O	A24
3	I	RTM	36	I/O	B21	69	I/O	A2	102	I/O	A25
4	I	RFM	37	I/O	B20	70	—	V _{DD}	103	I/O	A26
5	—	V _{DD}	38	I/O	B19	71	I/O	A3	104	I/O	A27
6	—	NC	39	I/O	B18	72	I/O	A4	105	I/O	A28
7	I	MBB	40	—	GND	73	I/O	A5	106	I/O	A29
8	—	GND	41	I/O	B17	74	—	GND	107	—	GND
9	O	$\overline{\text{MBF1}}$	42	I/O	B16	75	I/O	A6	108	I/O	A30
10	—	GND	43	—	V _{DD}	76	I/O	A7	109	I/O	A31
11	I	$\overline{\text{CSB}}$	44	I/O	B15	77	I/O	A8	110	—	V _{DD}
12	I	$\overline{\text{W/RB}}$	45	I/O	B14	78	I/O	A9	111	I/O	A32
13	I	ENB	46	I/O	B13	79	I/O	A10	112	I/O	A33
14	I	CLKB	47	I/O	B12	80	I/O	A11	113	I/O	A34
15	—	V _{DD}	48	—	GND	81	—	GND	114	I/O	A35
16	—	NC	49	—	NC	82	—	NC	115	—	NC
17	—	NC	50	—	NC	83	—	NC	116	—	NC
18	—	NC	51	—	NC	84	—	NC	117	—	NC
19	I/O	B35	52	I/O	B11	85	I/O	A12	118	—	GND
20	I/O	B34	53	I/O	B10	86	—	V _{DD}	119	I	CLKA
21	I/O	B33	54	I/O	B9	87	I/O	A13	120	I	ENA
22	I/O	B32	55	I/O	B8	88	I/O	A14	121	I	$\overline{\text{W/RA}}$
23	—	GND	56	I/O	B7	89	I/O	A15	122	I	$\overline{\text{CSA}}$
24	I/O	B31	57	—	V _{DD}	90	I/O	A16	123	O	IR
25	I/O	B30	58	I/O	B6	91	I/O	A17	124	O	OR
26	I/O	B29	59	—	GND	92	—	GND	125	—	V _{DD}
27	I/O	B28	60	I/O	B5	93	I/O	A18	126	O	$\overline{\text{AF}}$
28	I/O	B27	61	I/O	B4	94	I/O	A19	127	O	$\overline{\text{AE}}$
29	I/O	B26	62	I/O	B3	95	I/O	A20	128	—	V _{DD}
30	—	V _{DD}	63	I/O	B2	96	I/O	A21	129	O	$\overline{\text{MBF2}}$
31	I/O	B25	64	I/O	B1	97	—	V _{DD}	130	I	MBA
32	I/O	B24	65	I/O	B0	98	I/O	A22	131	I	$\overline{\text{RST}}$
33	—	GND	66	—	GND	99	—	GND	132	—	GND

INPUT

CLKA : PORT-A CLOCK
 CLKB : PORT-B CLOCK
 $\overline{\text{CSA}}$: PORT-A CHIP SELECT
 $\overline{\text{CSB}}$: PORT-B CHIP SELECT
 ENA : PORT-A MASTER ENABLE
 ENB : PORT-B MASTER ENABLE
 $\overline{\text{FS0/SD}}$: FLAG OFFSET SELECT 0/SERIAL DATA
 $\overline{\text{FS1/SEN}}$: FLAG OFFSET SELECT1/SERIAL ENABLE
 MBA : PORT-A MAILBOX SELECT
 MBB : PORT-B MAIL BOX SELECT
 RFM : READ FROM MARK
 $\overline{\text{RST}}$: RESET
 RTM : RETRANSMIT MODE
 $\overline{\text{W/RA}}$: PORT-A WRITE/READ SELECT
 $\overline{\text{W/RB}}$: PORT-B WRITE/READ SELECT

OUTPUT

$\overline{\text{AE}}$: ALMOST-EMPTY FLAG
 $\overline{\text{AF}}$: ALMOST-FULL FLAG
 $\overline{\text{IR}}$: INPUT-READY FLAG
 $\overline{\text{MBF1}}$: MAIL 1 REGISTER FLAG
 $\overline{\text{MBF2}}$: MAIL 2 REGISTER FLAG
 OR : OUTPUT-READY FLAG

INPUT/OUTPUT

A0 - A35 : PORT-A DATA
 B0 - B35 : PORT-B DATA

