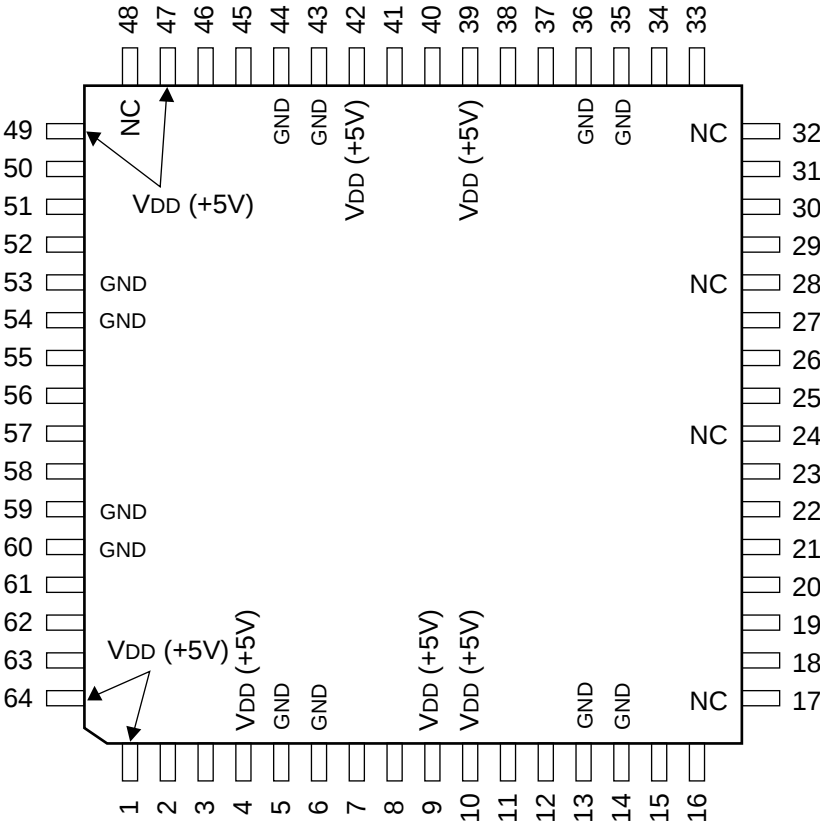

C-MOS 1024 × 9-BIT EACH FIFO MEMORY

—TOP VIEW—



PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL	PIN No.	I/O	SIGNAL
1	—	V _{DD}	14	—	GND	27	O	EMPTYA	40	I/O	B6	53	—	GND
2	I/O	A3	15	O	AF/AEA	28	—	NC	41	I/O	B5	54	—	GND
3	I/O	A4	16	O	HFA	29	I	UNCKA	42	—	V _{DD}	55	I	GAB
4	—	V _{DD}	17	—	NC	30	O	FULLB	43	—	GND	56	I	SAB
5	—	GND	18	I	LDCKA	31	I	LDCKB	44	—	GND	57	I	SBA
6	—	GND	19	O	FULLA	32	—	NC	45	I/O	B4	58	I	GBA
7	I/O	A5	20	I	UNCKB	33	O	HFB	46	I/O	B3	59	—	GND
8	I/O	A6	21	O	EMPTYB	34	O	AF/AEB	47	—	V _{DD}	60	—	GND
9	—	V _{DD}	22	I	DAF	35	—	GND	48	—	NC	61	I/O	A0
10	—	V _{DD}	23	I	RSTA	36	—	GND	49	—	V _{DD}	62	I/O	A1
11	I/O	A7	24	—	NC	37	I/O	B8	50	I/O	B2	63	I/O	A2
12	I/O	A8	25	I	RSTB	38	I/O	B7	51	I/O	B1	64	—	V _{DD}
13	—	GND	26	I	DBF	39	—	V _{DD}	52	I/O	B0			

INPUT
 $\overline{\text{DAF}}$; DEFINE A FLAG
 $\overline{\text{DBF}}$; DEFINE B FLAG
 GAB ; G ENABLE (Control the tranceiver functions)
 GBA ; G ENABLE (Control the tranceiver functions)
 LDCKA ; LOAD CLOCK A
 LDCKB ; LOAD CLOCK B
 RSTA ; RESET A
 RSTB ; RESET B
 SAB ; S CONTROL (Operating modes)
 SBA ; S CONTROL (Operating modes)
 UNCKA ; UNLOAD CLOCK A
 UNCKB ; UNLOAD CLOCK B

OUTPUT
 AF/AEA ; ALMOST FULL/ALMOST EMPTY FLAGS A
 AF/AEB ; ALMOST FULL/ALMOST EMPTY FLAGS B
 EMPTYA ; EMPTY FLAGS A
 EMPTYB ; EMPTY FLAGS B
 FULLA ; FULL FLAGS A
 FULLB ; FULL FLAGS B
 HFA ; HALF-FULL FLAGS A
 HFB ; HALF-FULL FLAGS B

INPUT/OUTPUT

A0-A8 ; DATA A
 B0-B8 ; DATA B

23	$\overline{\text{RSTA}}$	A8	12
22	$\overline{\text{DAF}}$	A7	11
18	LDCKA	A6	8
29	UNCKA	A5	7
		A4	3
		A3	2
		A2	63
		A1	62
		A0	61
		FULLA	19
		HFA	16
		AF/AEA	15
		EMPTYA	27
25	$\overline{\text{RSTB}}$	B8	37
26	$\overline{\text{DBF}}$	B7	38
31	LDCKB	B6	40
20	UNCKB	B5	41
		B4	45
		B3	46
		B2	50
		B1	51
		B0	52
		FULLB	30
		HFB	33
		AF/AEB	34
		EMPTYB	21
56	SAB		
57	SBA		
58	GBA		
55	GAB		

