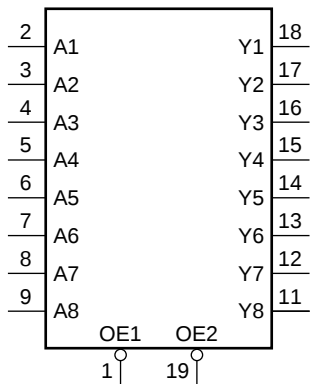
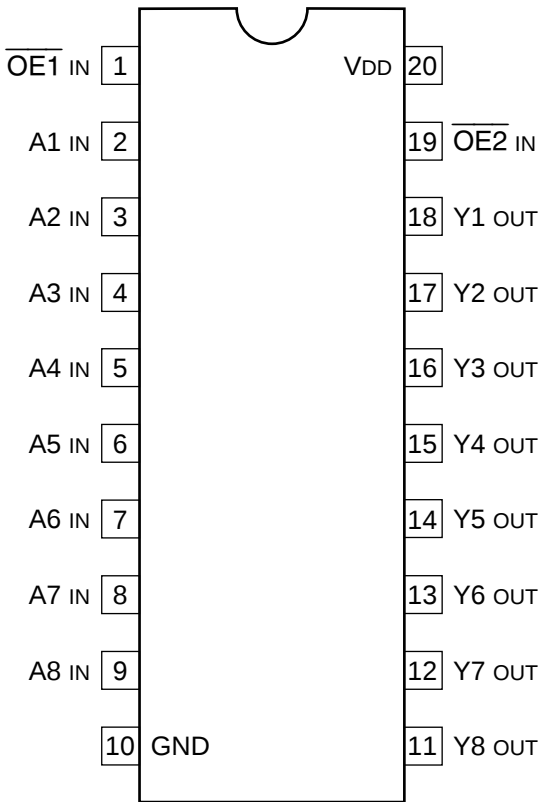


BiC-MOS BUFFERS/DRIVERS WITH 3-STATE OUTPUTS

—TOP VIEW—



INPUTS			OUTPUT
OE1	OE2	A	Y
0	0	0	0
0	0	1	1
1	x	x	HI-Z
x	1	x	HI-Z

0 : LOW LEVEL
1 : HIGH LEVEL
x : DON'T CARE
HI-Z : HIGH IMPEDANCE

INPUT
A1 - A8 : DATA
OE1, OE2 : OUTPUT ENABLE

OUTPUT
Y1 - Y8 : DATA

