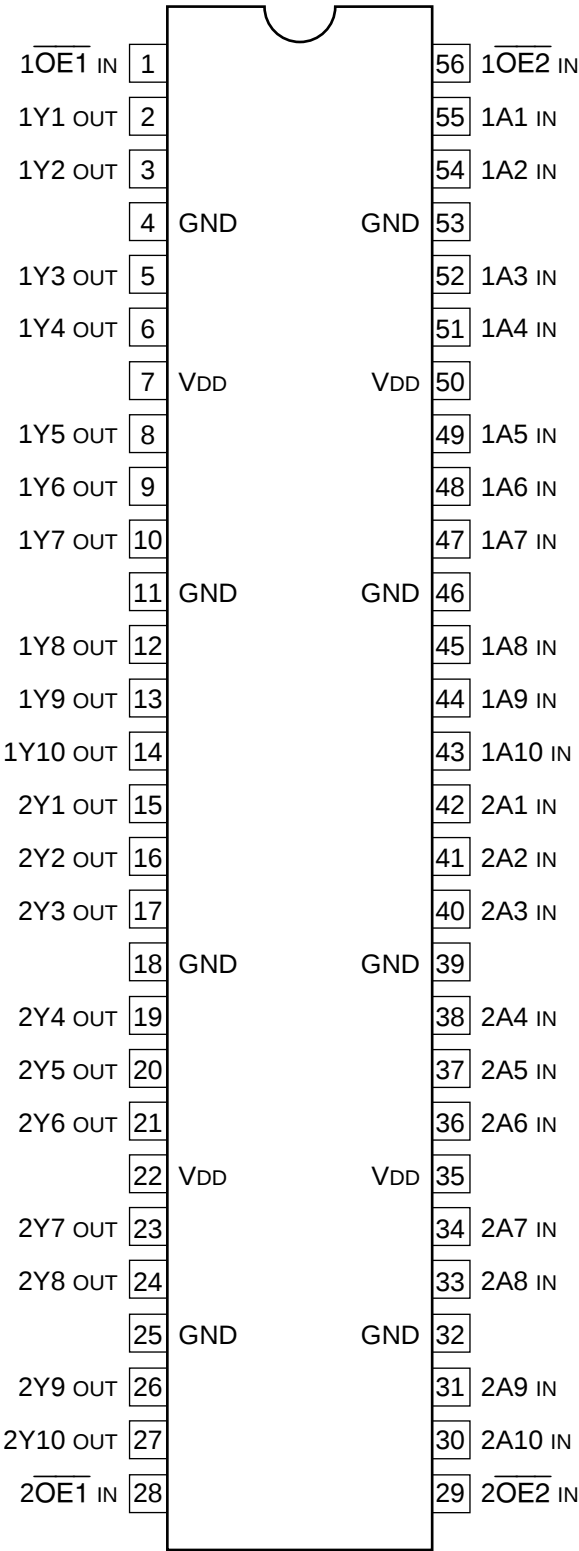


C-MOS 20-BIT BUFFERS/DRIVERS WITH 3-STATE OUTPUTS

—TOP VIEW—



| INPUTS           |                  |   | OUTPUT |
|------------------|------------------|---|--------|
| $\overline{OE1}$ | $\overline{OE2}$ | A | Y      |
| 0                | 0                | 0 | 0      |
| 0                | 0                | 1 | 1      |
| 1                | x                | x | HI-Z   |
| x                | 1                | x | HI-Z   |

0 : LOW LEVEL  
1 : HIGH LEVEL  
x : DON'T CARE  
HI-Z : HIGH IMPEDANCE

**INPUT**  
1A1 - 1A10, 2A1 - 2A10 : DATA  
 $\overline{1OE1}$ ,  $\overline{1OE2}$ ,  $\overline{2OE1}$ ,  $\overline{2OE2}$  : OUTPUT ENABLE

**OUTPUT**  
1Y1 - 1Y10, 2Y1 - 2Y10 : DATA

