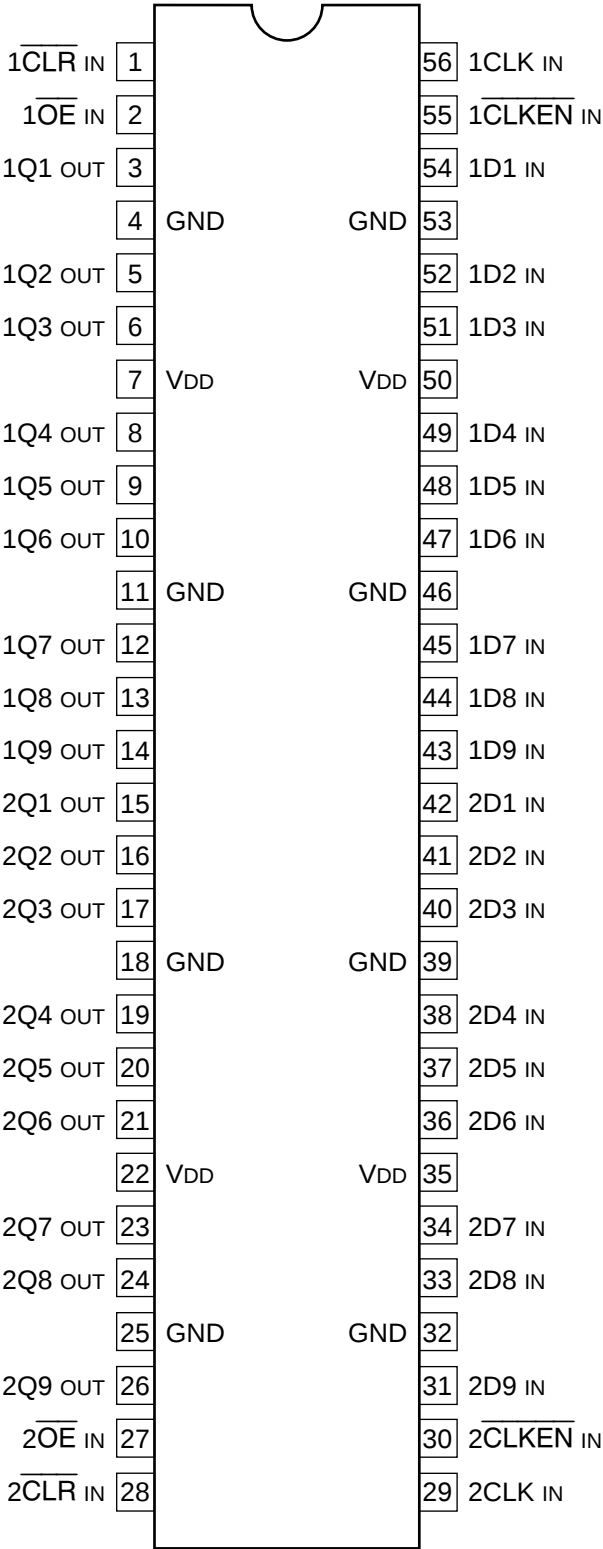


C-MOS 18-BIT BUS-INTERFACE FLIP-FLOPS WITH 3-STATE OUTPUTS

—TOP VIEW—



| INPUTS |     |       |     |   | OUTPUT |
|--------|-----|-------|-----|---|--------|
| OE     | CLK | CLKEN | CLK | D | Q      |
| 0      | 0   | x     | x   | x | 0      |
| 0      | 1   | 0     |     | 1 | 1      |
| 0      | 1   | 0     |     | 0 | 0      |
| 0      | 1   | 0     | 0   | x | Q0     |
| 0      | 1   | 1     | x   | x | Q0     |
| 1      | x   | x     | x   | x | HI-Z   |

0 : LOW LEVEL  
1 : HIGH LEVEL  
x : DON'T CARE  
HI-Z : HIGH IMPEDANCE

**INPUT**  
1D1 - 1D9, 2D1 - 2D9 : DATA  
1OE, 2OE : OUTPUT ENABLE  
1CLKEN, 2CLKEN : CLOCK ENABLE  
1CLR, 2CLR : CLEAR

**OUTPUT**  
1Q1 - 1Q9, 2Q1 - 2Q9 : DATA

SN74ABT16823DGGR (2/2)

